



Rajarajeswari College of Engineering

(An Autonomous Institution)

(Affiliated to VTU, Belagavi & Approved by AICTE, UGC & Govt. of Karnataka)
#14, Ramohalli Cross, Kumbalagodu, Mysore Road, Bengaluru -560074, Karnataka



Department of Electronics & Communication Engineering

REPORT ON ALUMNI TALK

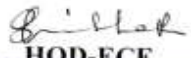
DATE	09/05/2026
VENUE	ECE SEMINAR HALL
EVENT NAME	ALUMNI TECHNICAL TALK
STAFF CO ORDINATOR	Dr. Vani S Badiger, Associate Professor, Dept, ECE
HOD	Dr. Sunitha R, HOD, Dept, ECE
RESOURCE PERSON	Miss Varna V Nadiger, Independent Verification and Validation Engineer. DRDO, Bangalore
PARTICIPANTS	4 th SEM ECE STUDENTS

Outcome of the Program:

Students gained information about the following-

- Understand the fundamentals of FPGA architecture and its role in modern VLSI systems.
- Gain knowledge about verification and validation techniques used in VLSI design flow.
- Learn implementation of digital designs on FPGA platforms for real-time testing and prototyping.
- Develop practical skills in hardware debugging, simulation, and performance analysis.
- Understand the importance of FPGA-based prototyping in reducing design errors and development time.
- Explore modern verification methodologies used in semiconductor industries.
- Enhance awareness about industry-oriented VLSI tools and FPGA development environments.
- Improve problem-solving abilities related to hardware design and system validation.
- Bridge the gap between theoretical VLSI concepts and practical hardware implementation.
- Motivate students and researchers towards advanced research and career opportunities in VLSI and Embedded Systems.


Name & Signature
Dr. Vani S. Badiger


HOD-ECE
PROFESSOR & HEAD
DEPT. OF ELECTRONICS & COMMUNICATION ENG
RAJARAJESWARI COLLEGE OF ENGG.
BENGALURU - 560 074



Rajarajeswari College of Engineering

(An Autonomous Institution)

(Affiliated to VTU, Belagavi & Approved by AICTE, UGC & Govt. of Karnataka)

#14, Ramohalli Cross, Kumbalagodu, Mysore Road, Bengaluru -560074, Karnataka



Department of Electronics & Communication Engineering



RAJARAJESWARI COLLEGE OF ENGINEERING



An Autonomous Institution

Under VTU, Approved by AICTE, UGC & GoK



ELECTRONICS AND COMMUNICATION ENGINEERING

Organizing "Alumni Talk " on

'FPGA Based Verification and Validation in Modern VLSI Systems'

RESOURCE PERSON:

Alumna - 2018-2022 Batch

Miss. VARNA V NADIGER

Independent Verification and
Validation Engineer

DRDO, Bengaluru



Coordinator:

Dr.Vani S Badiger, Associate Professor

DATE & TIME



Saturday
9th May 2026



Start From
10:45 AM TO 12:45 PM

Venue: ECE Seminar Hall, RRCE Campus

Dr. Sunitha R
HoD - ECE

Dr. Rangaiah L
Professor & Dean-Academics

Dr. R. Balakrishna
Principal

CONTACT US

99163 93828



www.rrce.org



Rajarajeswari College of Engineering

(An Autonomous Institution)

(Affiliated to VTU, Belagavi & Approved by AICTE, UGC & Govt. of Karnataka)
#14, Ramohalli Cross, Kumbalagodu, Mysore Road, Bengaluru -560074, Karnataka



Department of Electronics & Communication Engineering




Name & Signature
Dr. Vani. S. Badiger


PROFESSOR & HEAD
DEPT. OF ELECTRONICS & COMMUNICATION ENG.
RAJARAJESWARI COLLEGE OF ENGG.
BENGALURU - 560 074



Rajarajeswari College of Engineering

(An Autonomous Institution)

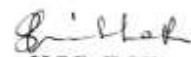
(Affiliated to VTU, Belagavi & Approved by AICTE, UGC & Govt. of Karnataka)
#14, Ramohalli Cross, Kumbalagodu, Mysore Road, Bengaluru -560074, Karnataka



Department of Electronics & Communication Engineering




Name & Signature
Dr. Vani. S. Badiger


HOD-ECE
PROFESSOR & HEAD
DEPT. OF ELECTRONICS & COMMUNICATION ENG.
RAJARAJESWARI COLLEGE OF ENGG.
BENGALURU - 560 074