



# Rajarajeswari College of Engineering

(An Autonomous Institution, Affiliated to VTU, Belagavi & Approved by AICTE, UGC & GoK)

Sponsored by: MOOGAMBIGAI CHARITABLE AND EDUCATIONAL TRUST

## Dept. of Electronics & Communication Engineering

Ref: Academic/DAC/ECE/02/2024-25

Date: 10-01-2025

### DEPARTMENT ADVISORY COMMITTEE MEETING-2

#### MINUTES OF MEETING

##### Members present in the DAC Meeting

Sl.No	Name of the member	Designation	Position of DAC
1	Dr. S B Bhanu Prashanth	Professor, Dept. of Medical Electronics, BMSCE, Bangalore.	Academic Expert
2	Dr. A M Vijaya Prakash	Professor, Dept. of ECE, BIT, Bangalore.	Academic Expert
3	Mr. Shivakumar P M	SAP labs, Whitefield, Bangalore	Industry Expert
4	Mr. Srikanth K S	Intel India Ltd, Hyderabad.	Industry Expert
5	Mr. Vishwajith V Pai	Applications Engineer, Engineer Technology and Product Group, Synopsys Bangalore.	Alumni Member
6	Mr. Deepak R	Manager, Bharath Electronics Ltd	Parent
7	Miss. Mamatha S	4 <sup>th</sup> SEM Student	Student
8	Dr. L Rangaiah	Professor & Dean-Academics	Academic Expert, RRCE
9	Dr. Sunitha R	HOD, Dept. of ECE	Chairperson
10	Dr. A Muruganadham	Professor	Member
11	Dr. A M Prasanna Kumar	Professor	Member
12	Dr. S Anitha	Professor	Member
13	Dr. Deepika J	Associate Professor	Member
14	Prof. V Sreepathi	Assistant Professor	Member
15	Prof. Lokesha K	Assistant Professor	Member
16	Prof. Chaithanya S	Assistant Professor	Member
17	Prof. Vani S Badiger	Assistant Professor	Member
18	Prof. Santhosh Chavan	Assistant Professor	Member
19	Prof. Ajay M	Assistant Professor	Member
20	Prof. Haritha K S	Assistant Professor	Member
21	Prof. Suresh M	Assistant Professor	Member

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The Meeting started with welcome address by Dr. Sunitha R, HOD, ECE Department extending a warm welcome to the DAC members and a sincere gratitude was expressed towards all members present for the meeting. The following items were taken up for discussion and consideration

1. The minutes of the meeting of DAC meeting held on 10.01.2025 have been circulated to the members through email soliciting their suggestion and comments. The members approved the minutes of the DAC Meeting of held on 10.08.2024.

2. **Deliberation on the proposed scheme and syllabus for the 3rd and 4th semesters of the UG program for the academic year 2025-26.**

- Dr. Sunitha R, HOD, ECE Department presented the 2<sup>nd</sup> year scheme and syllabus of the above program as per the VTU guidelines and the same was discussed with the members of the DAC.
- Dr. Bhanuprashnath, Academic member suggested to make a title specific- to the subjects like Transforms probabilities and random variables instead of Mathematics-III for ECE and also for Mathematics-IV for ECE.
- Dr. Vijaya Prakash, Academic member suggested to specify clearly in the syllabus document that the contents of each module are derived from standard textbooks. Any textbook that does not cover the syllabus should be removed from the recommended list.
- Dr. Vijaya Prakash and Dr. Bhanu Prashanth suggested making the Electromagnetic Theory (Field Theory) syllabus, ensuring a deep understanding of fundamental topics. Limit the number of textbooks to a maximum of three and include fewer reference books, ensuring they are of the latest edition. Course Outcomes (COs) should be defined with appropriate taxonomy.
- Dr. Vijaya Prakash suggested, for Digital System Design using Verilog HDL, it is advised to preferably use the textbook "Verilog HDL" by Samar Palnitkar.
- Dr. Bhanuprashnath suggested, in the lab component, make it a generalized component so that different decoders (circuits) can be chosen each year.
- Dr. Vijaya Prakash, Academic member and Dr. Bhanuprashnath pointed out that Integrated Professional Core Course (IPCC) at Visvesvaraya Technological University (VTU) is to integrate professional theory with practical application of the same

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course. The theory part of IPCC is evaluated by both Continuous Internal Evaluation (CIE) and Semester End Examination (SEE). The practical part of IPCC is evaluated by CIE only. Questions from the practical part of IPCC may be included in the SEE question paper.

- They also asked the clarification on whether students must achieve a minimum of 40% in the combined CIE and SEE for both IPCC theory and IPCC lab to pass the IPCC course. This implies that a student must score at least 40 out of 100 in both components, which could make the assessment process more complex.
- BOS (Board of Studies) members have agreed to replace Analog Electronics by Linear Integrated Circuits (LIC) in the 3rd semester because most of the Analog Electronics topics are already covered in 1st-year Basic Electronics. He said to reframe Linear Integrated Circuits syllabus accordingly.
- Include lab components in the Ability Enhancement Course (AEC) to enhance practical learning. Establish a connection between the Engineering Science Course (ESC) and the Ability Enhancement Course (AEC) for better integration.
- Dr. Bhanuprashnath suggested designing the curriculum in accordance with the latest NBA accreditation standards.
- Evaluate whether the "Power and Instrumentation" subject is necessary and consider introducing new subjects.
- All Members suggested incorporating Ability Enhancement Courses (AEC) focusing on avoiding highly technical (core, complicated) subjects in the Ability Enhancement Course (AEC), as it may put excessive strain on students for a single credit. Instead, consider subjects such as:
  - Placement-oriented courses
  - Professional ethics
  - Signal integrity and data algorithms
  - Personal financial management
  - Personal healthcare courses, etc.
- Incorporate a Communication Lab to enhance practical learning in communication-related topics. Separate "Network and Control Systems" into two distinct subjects for better

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clarity and structured learning.

- Mr. Vishwajith Pai, Alumni also recommended restructuring the curriculum by introducing Microprocessors in the lower semesters and Microcontrollers in the higher semesters. This approach ensures that students first develop a strong foundational understanding of microprocessor architecture, programming, and applications before advancing to microcontrollers.
- Mr. Shivakumar P integrating ECE subjects with AI/ML into the curriculum to align with emerging M. proposed industry trends and technological advancements. This addition would provide students with hands-on experience in programming robots using Artificial Intelligence (AI) and Machine Learning (ML) techniques.

### Agenda 3: Discussion on 5th to 8th semester scheme of 2024 scheme proposed.

- Dr. Bhanuprashnath, Insisted to follow revised National Board of Accreditation (NBA) guidelines, the number of Program Outcomes (POs) has been streamlined to 11, ensuring a more focused on industry-relevant approach in engineering education. Align with global accreditation standards (e.g., Washington Accord).
- Incorporate an internship before major projects to ensure that the internship experience leads to project ideas.
- Include industry-driven courses to align the curriculum with industry needs.
- Encourage peer-to-peer learning by supporting fast learners and enabling them to teach slow learners.
- Modify the title of "Advanced VLSI" by removing "Advanced" and considering alternatives such as "Low Power VLSI Design," "ASIC Design," or "Analog and Mixed-Mode VLSI."
- Mr. Vishwajith and Mr. Shivakumar P M proposed adding a mini-project as a mandatory component in the curriculum to provide students with practical exposure and hands-on learning experience. Also suggested that the Technical Seminar should not carry 4 credits and should not be scheduled in the 8th semester to optimize the curriculum structure.

### Agenda 4: Discussion on LABS, MOOCs, NPTEL and Projects 5th to 8th semester scheme of 2024 scheme proposed.

- All DAC members suggested to limit each semester to Two Labs (One Core Lab + One Interdisciplinary /Industry Lab) reduce repetitive theory courses and merge similar

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subjects for better efficiency, encourage Self-Learning through internships, and research projects. Develop some labs with simulation-based learning (MATLAB, Labview, IoT platforms, etc.)

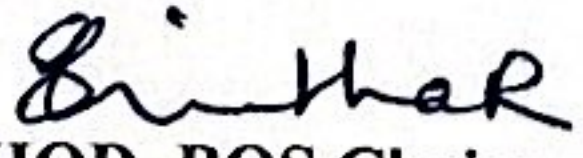
- Mr. Shivakumar P M proposed to incorporate Massive Open Online Courses (MOOCs), NPTEL etc. into the 2nd and 3rd years of the curriculum to enhance students' learning experience and exposure to emerging technologies.
- All DAC members suggested making interdisciplinary projects a mandatory part of the curriculum to encourage collaboration across different engineering and science disciplines, fostering innovation and problem-solving.

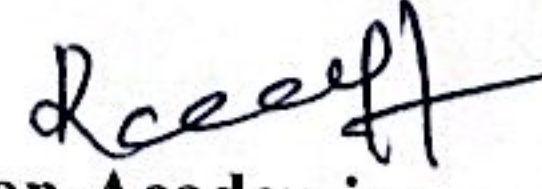
### Agenda 5: Review the academic performance of a program/department and improve its quality.


- All member of the DAC suggested revising and refining the rubrics for Course Outcomes (COs) to ensure clear, measurable, and outcome-driven assessment criteria.
- Course Outcome (CO) Attainment Analysis:
  - CO attainment levels of all subjects need to analyzed.
  - Courses with attainment below the target level Should identified.
  - Faculty discussed reasons such as difficult topics, lack of practice, or student participation.
- PO attainment is calculated using weighted average of CO attainments mapped to Pos.
- All DAC member suggested including the latest editions of textbooks as prescribed texts in the curriculum. By incorporating the newest editions, the curriculum can stay relevant, reflecting the latest research, technological developments, and best practices in the field.
- All DAC member suggested that general subjects should be assessed using Multiple-Choice Questions (MCQs), while practical subjects should not follow this assessment method.
- All DAC Members expressed overall satisfaction and appreciated the work at the curriculum development process.

Agenda 6: DAC Chairman Dr. Sunitha R, HOD, ECE Department expressed her sincere thanks towards all the members for attending the meeting and also for their valuable inputs and formally concluded the meeting.

  
DAC Coordinator

  
HOD, BOS Chairperson  
PROFESSOR & HEAD  
DEPT. OF ELECTRONICS & COMMUNICATION ENG  
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