RajaRajeswari College of Engineering
(An Autonomous Institute, Affiliated Visvesvaraya Technological University, Belagavi,
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CSE, ECE, ISE

Department of Electronics and Communication Engineering

Report on"Faculty Development Programme"

DATE	**03-02-2025 to 07-02-2025**
VENUE	**VLSI Lab, ECE Department, RRCE**
EVENT NAME	**Faculty Development Programme**, "Hands on Sessions of EC Laboratories"
STAFF COORDINATOR	**Dr. Anitha S** **Prof. Daneshwari Modi**
ADDRESSED BY	**Faculty Members, Dept. of ECE**
NO. OF PARTICIPANTS	**25**

1st Day (Morning Session) ABOUT SPEAKER

Chaithanya S Received B.E degree in Electronics and Communication Engineering from Nagarjuna College of Engineering and Technology, Bangalore, in the year 2006 and M.Tech in Digital Electronics and Communication from Dayanand Sagar College of Engineering, Bangalore in the year 2013, and Pursuing Ph.D under Visveswariah Technological University, Belgaum. Attended various Workshops, Faculty Development Programs, International and National Conferences on various technologies, also published papers in National Conference, International Conference and International journals. Has got 13 years of teaching experience. Fellow Member of IETE. Interest areas are Wireless and Computer Communication, Cryptography, Network Security.

TITLE: Microcontroller Lab

Objectives

- To write a program 8051 microcontroller using Assembly Language and C.
- To develop hardware interfacing programs.

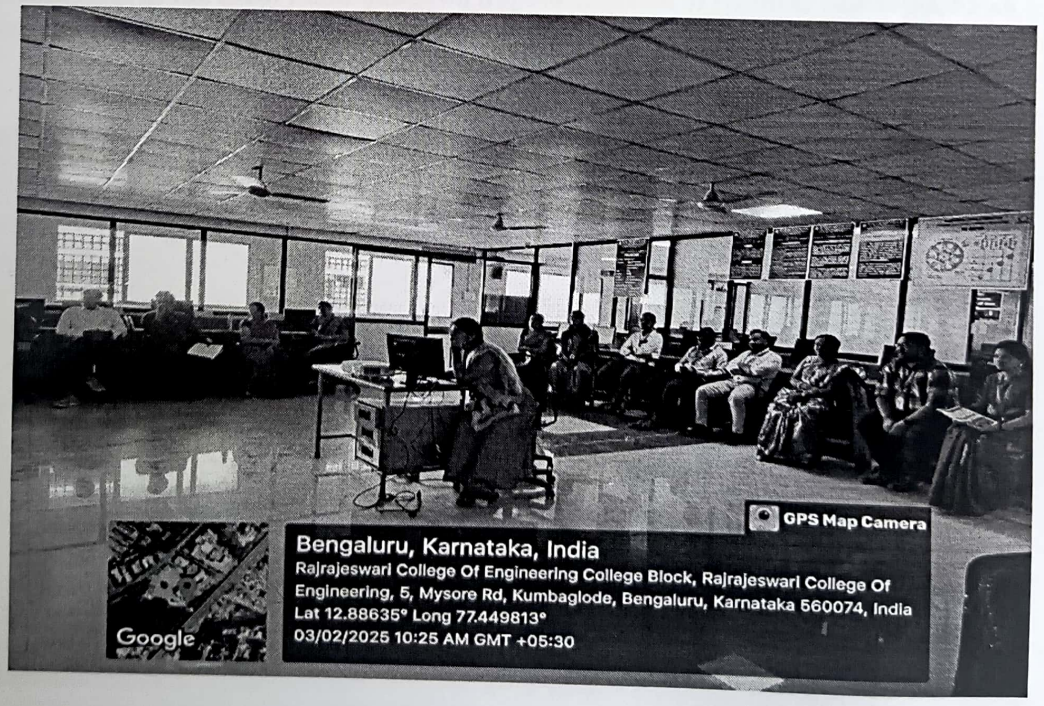
The following mentioned titles are discussed & addressed by the speaker which are:

- Discussed the types of 8051 Microcontroller Addressing modes & Instructions with Assembly Language Programs and execution.

Outcomes:

- Executed ALP programs using different instructions.
- Learnt the hardware interfacing programs also.

Total Number of participants: 17



1st Day (Afternoon Session) ABOUT SPEAKER

Prof. V Sreepathi

AMIE in Electronics & Communication from Institute of Engineers India (IEI), Calcutta, in year 1995, ME from Bangalore University, in Power Electronics in year 1999 and currently pursuing Ph.D in the area OFDMA from Andhra university, Visakhapatnam, India. Worked in various Engineering Colleges at various levels. Has 18 years of teaching experience. Published 1 paper in international journal, 10 papers in various international &amp: national conferences. Member in Professional bodies ISTE, IEI (I). Has good teaching experience specialized in power electronics,satellite communication, electronic circuits and signals and systems. Prepared AEC lab manual and Power Electronics Lab manual. Member in anti-ragging committee and cultural secretary in college level.

TITLE: Control Systems lab

Objectives

- To implement programs on control systems

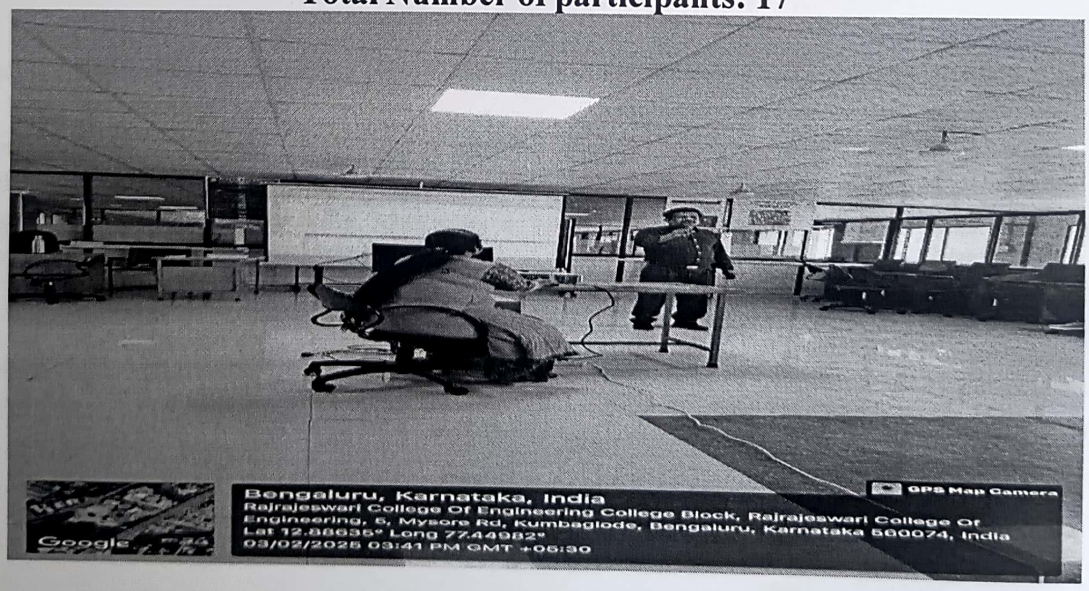
The following mentioned titles are discussed & addressed by the speaker which are:

- Experiments on transfer functions, block diagram reduction technique, root locus etc.,

Outcomes:

- **The faculties learnt the theory and hands on implementation of transfer functions, block diagram reduction technique, root locus.**

Total Number of participants: 17



2nd Day ABOUT SPEAKER

Dr. Deepika J

Dr. Deepika. J graduated B.E in Electronics & Communication Engineering from ACS College of Engineering, Bangalore, VTU, Belgavi, Post graduated M.Tech in Digital Electronics &Communication Systems, ACSCE, Bangalore. She completed Ph.D (ECE) in the area of Wireless Mobile Ad-hoc Networks from VTU, Belagavi. She has got experience of 6.5 years in teaching. She published 17 papers in International Journals and Conferences. She has ability to work in the frontier areas of Digital Electronics and Communications Systems.

TITLE: Communication Lab

Objectives

- Understand and analyze the electronic circuits used for AM and FM modulation and demodulation circuits.

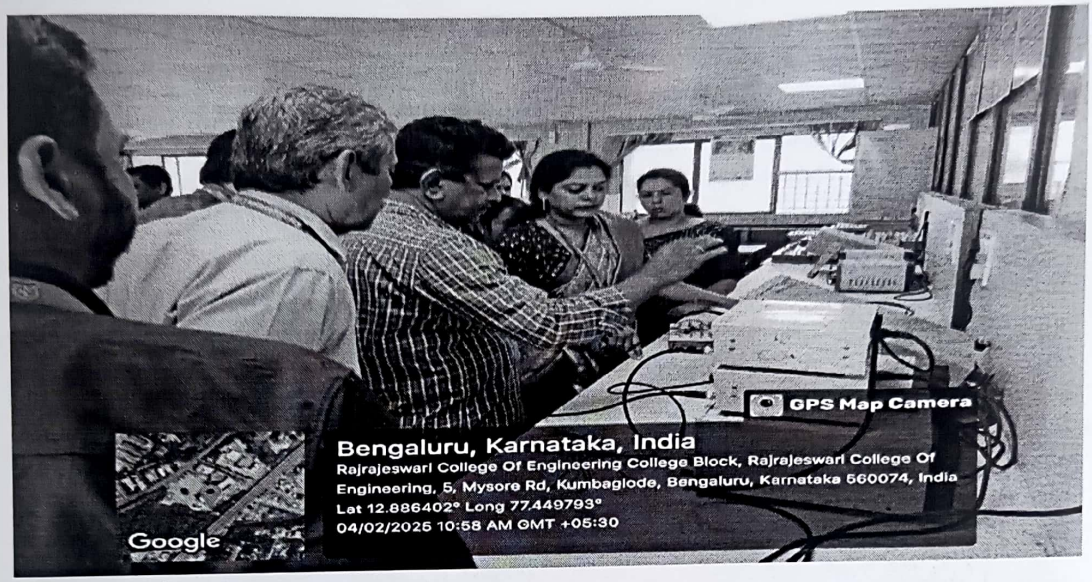
The following mentioned titles are discussed & addressed by the speaker are:

- The demonstration of AM generation and detection using suitable electronic circuits.
- Design and testing of the sampling, Multiplexing and pulse modulation techniques using electronic hardware were conducted.

Outcomes:

- Demonstrated AM generation and detection using suitable electronic circuits.
- Designed and tested the sampling pulse modulation techniques using electronic hardware.

Total Number of participants: 17



3rd Day (Morning Session) ABOUT SPEAKER

Prof. Ajay M

Brief Profile Graduated from Sri Jagadguru Balagangadharanataha Swamiji Institute of Technology, Bangalore (VTU), BE in, Statistical Switching in Telecommunicaton and Multiplexing. Labs handled are Electronic Circuits, Logic Electronics & Communication in the year 2008 and Post Graduated from UVCE Bangalore (BU), ME in Electronics and Communication in 2012. Has got 4 years of teaching experience. Has got published 04 technical paper 03 in national conference and 1 in International Conference. Subjects handled are Electronic Circuits, Logic Design, Microprocessors, Microcontrollers, Electronic Instrumentation Design, Microprocessors and Microcontrollers, Basic Electronics.

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Department of Electronics and Communication Engineering

TITLE: Principles of Communication System

Objectives

- Design and analyze the electronic circuits for AM and FM modulation and demodulation.
- Understand the concepts of random variable and random process to model communication systems.

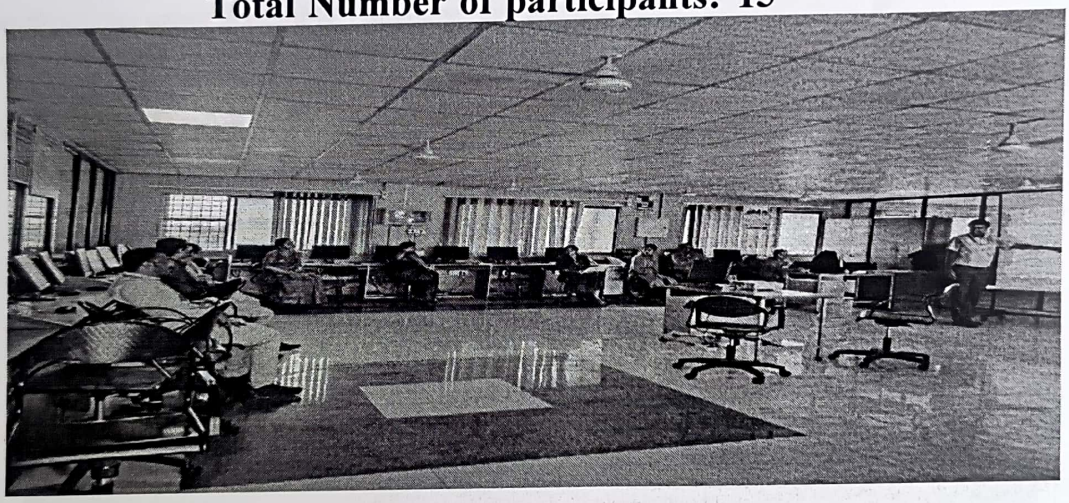
The following mentioned titles are discussed & addressed by the speaker are:

- Identifying the schemes for analog modulation and demodulation and compare their performance.
- Designing of PCM systems through the processes sampling, quantization and encoding.

Outcomes:

- Learnt about the generation and display of the relevant signals and its spectrums using Matlab code.

Total Number of participants: 15



3rd Day (Afternoon Session) ABOUT SPEAKER

Prof. M.Suresh

Prof. M.Suresh holds bachelor's degree in Electronics and Communication Engineering from Bharath Niketan Engineering College under Anna University and M.E in Applied Electronics from PSNA College of Engineering. Attended various workshops, conferences and faculty development programs on Signals and systems, MATLAB, LATEX. Has got 9 year and 1 months of teaching experience.

TITLE: Embedded System Lab

Objectives

- To write programs using Assembly Language and C.
- To develop hardware interfacing programs.

The following mentioned titles are discussed & addressed by the speaker are:

- Execution of different instruction set and hardware interfacing programs.

Outcomes:

- Executed different instruction set programs and hardware interfacing programs.

Total Number of participants: 15

4th Day ABOUT SPEAKER

Prof. Haritha K Sivaraman

Mrs. Haritha K Sivaraman holds bachelor's degree in Electronics and CommunicationEngineering from Sahrdaya College of Engineering Technology under Calicut University and M.E in VLSI Design from PSNA College of Engineering under Anna University. She is pursuing her Ph.D in Wireless Sensor networks under VTU, Belagavi. Attended various workshops, conferences and faculty development programs on VLSI & LABview, MATLAB, LATEX. Has got 11 years of teaching experience. Has published papers in 02 national conferences and journals.

TITLE: System modeling using Simulink

Objectives

- Simulation of the trigonometric functions and display of signals.
- Implementations of analog and digital systems using Simulink.
- Implement digital logic circuits using Simulink and display the output

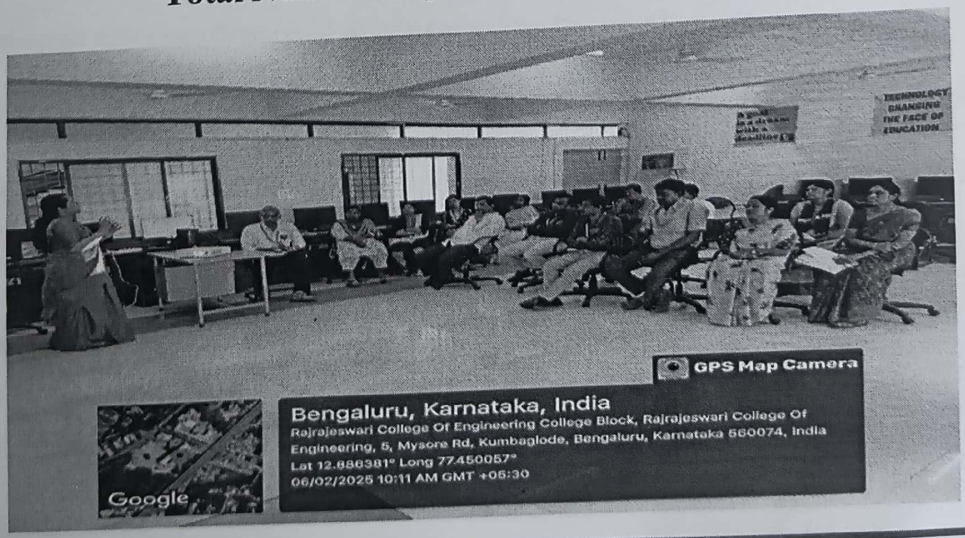
The following mentioned titles are discussed & addressed by the speaker which are:

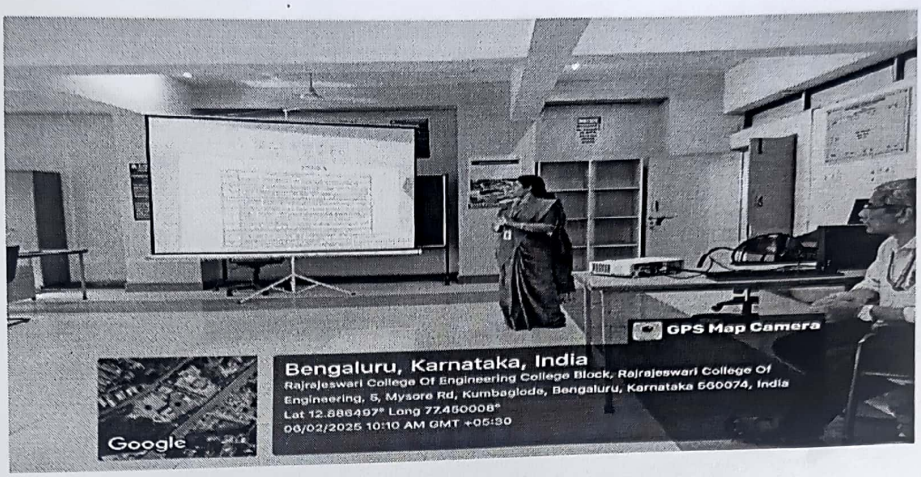
- Application of Matlab using simulink was discussed.
- Simulation of the trigonometric functions and display of signals.
- Implement digital logic circuits using Simulink and display the output.

Outcomes:

- Trigonometric functions were simulated.
- Digital logic circuits using Simulink were implemented.

Total Number of participants: 17





5th Day ABOUT SPEAKER

Prof. K Lokesha
Mr. Lokesha K holds bachelor's degree in Electronics and Communication Engineering from P.E.S.Institute of Technology under Bangalore University and M.Tech in VLSI design and Embedded Systems from VTU. Guided various UG / PG student projects on Analog design and low power VLSI design. Attended various workshops, conferences and faculty development programs on VLSI design.

TITLE: VLSI Design and Testing Lab

Objectives

- Design, model, simulate and verify digital circuits.
- Perform ASIC design flow and understand the process of synthesis, synthesis constraints and evaluating the synthesis reports to obtain optimum gate level netlist.

The following mentioned titles are discussed & addressed by the speaker are:

- The speaker discussed about the Design, model, simulation and verification of digital circuits.
- Also explained to perform ASIC design flow and understand the process of synthesis, synthesis constraints and evaluating the synthesis reports to obtain optimum gate level netlist.

Outcomes:

- Designed, simulated and verified digital circuits.

Total Number of participants: 17

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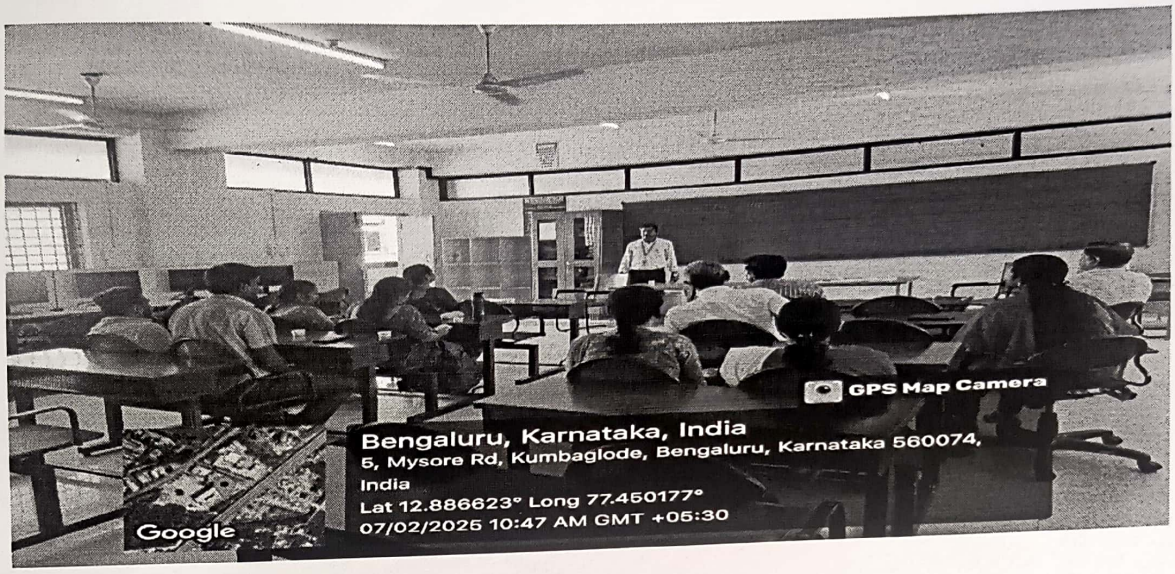

PROFESSOR & HEAD
DEPT. OF ELECTRONICS & COMMUNICATION ENG.
RAJARAJESWARI COLLEGE OF ENGG.
BENGALURU - 560 074

Campus:#14,RamohalliCross,Kumbalgodu,MysoreRoad,Bengaluru–560074

Phone:+91-80-28437124/28437375 EmailId:elizebeth@rrce.org Website:https://www.rrce.org