Webinar

On

"VLSI – Standard Cell Design"

- **Date:** 24th May 2020
- **Resource Person:Mr.G.Prashanth Reddy** Team lead, Intel India, Bangalore
- **Platform:**Zoom Meeting, ID: 87541324699
- **Objective :**
  - To enlight the students with basic VLSI Standard Cell Design
  - To provide a better understanding of Cell Design & Issues related as per the need of industry
- **No. of participants:** 113
- **Target Participants:** Students of 4th and 6th semesterof ECE department.
- **Outcome of the event:** The Students gained knowledge on
  - Basics of VLSI& Cell Designs.
  - Timing Analysis of Memory Cells, Latency, Pipelining.
  - Obtained a practical exposure on VLSI Design.
  - Various Job Opportunities VLSI Industries.

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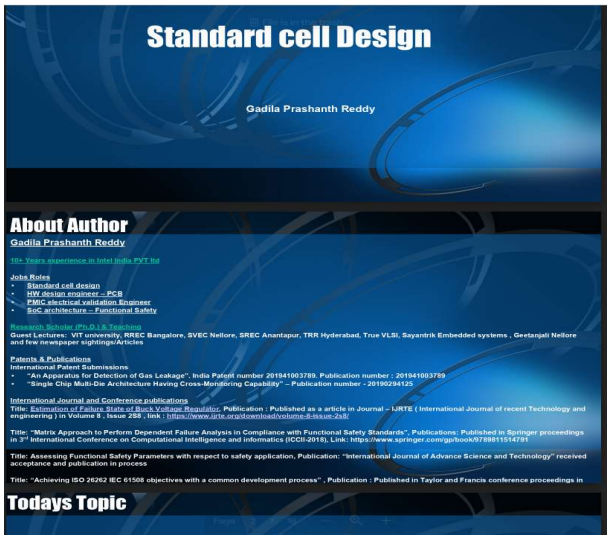

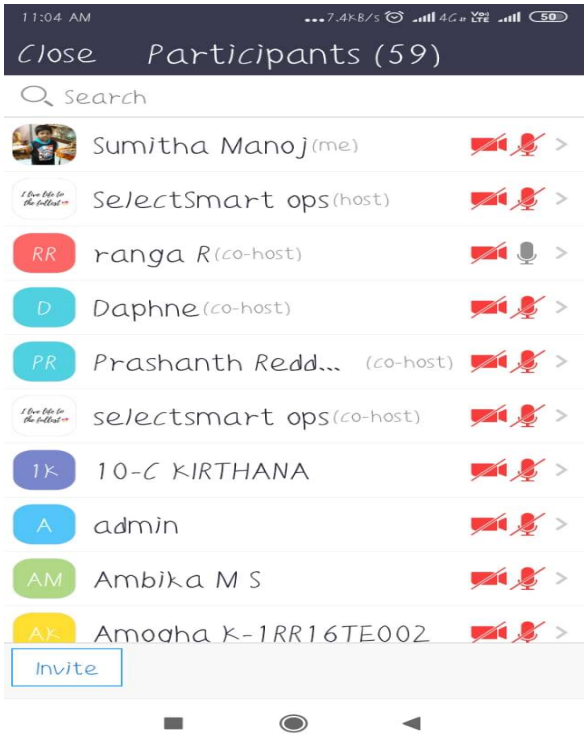

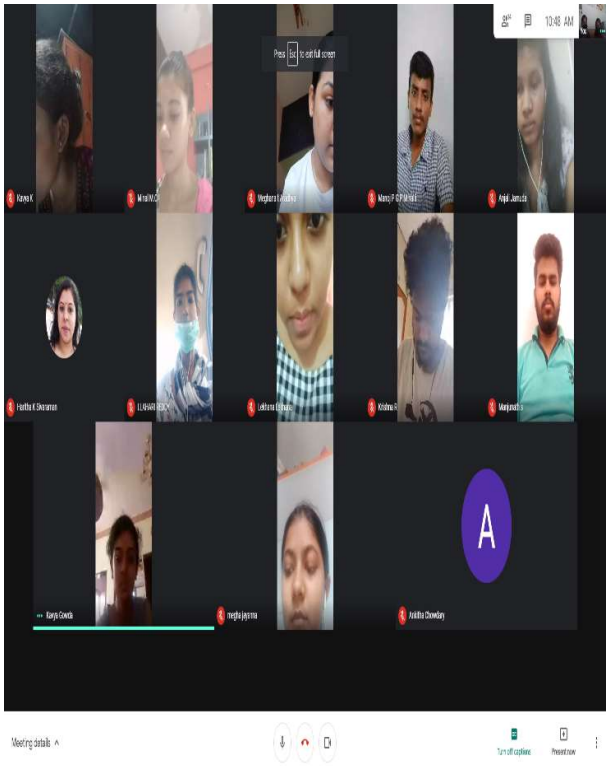

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