1.3.2. Average percentage of courses that include experiential learning through project work/field work/internship during last five years

ACADEMIC YEAR 2016-2021

COMPUTER SCIENCE AND ENGINEERING

Document contains list and syllabus of courses that includes experiential learning through project works and internships

Documents Enclosed

Sl.No	Particulars	Page No
1	List of course that include experiential learning	
2	Syllabus of mapped course	
3	List of project work	
4	Project work completion certificates	
5	List of internships undertaken by students	
6	Internship completion certificates	

List of course that include Experiential Learning

SL NO	COURSE CODE	COURSE NAME
1	15CS33	Data Structures and Applications
2	15CS34	Computer Organization
3	15CS35	Unix and Shell Programming
4	15CS36	Discrete Mathematical Structures
5	15CSL37	Analog and Digital Electronics Laboratory
6	15CSL38	Data Structures Laboratory
7	15MAT41	Engineering Mathematics - IV
8	15CS 42	Software Engineering
9	15CS43	Design and Analysis of Algorithms
10	15CS 44	Microprocessors and Microcontrollers
11	15CS45	Object Oriented Concepts
12	15CS46	Data Communication
13	15CSL47	Design and Analysis of Algorithm Laboratory
14	15CSL48	Microprocessors Laboratory
15	15MAT31	Engineering Mathematics - III
16	15CS51	Management and Entrepreneurship for IT Industry
17	15CS52	Computer Networks
18	15CS53	Database Management System
19	15CS54	Automata theory and Computability
20	15CSL57	Computer Network Laboratory
21	15CSL58	DBMS Laboratory with mini project
22	15CS61	Cryptography, Network Security and Cyber Law
23	15CS62	Computer Graphics and Visualization
24	15CS63	System Software and Compiler Design
25	15CS64	Operating Systems
26	15CSL67	System Software and Operating System Laboratory
27	15CSL68	Computer Graphics Laboratory with mini project
28	15CS71	Web Technology and its applications
29	15CS72	Advanced Computer Architectures
30	15CS73	Machine Learning
31	15CSL76	Machine Learning Laboratory
32	15CSL77	Web Technology Laboratory with mini project

33	15CSP78	Project Phase 1 + Seminar
34	15CS81	Internet of Things and Applications
35	15CS82	Big Data Analytics
36	15CS84	Internship / Professional Practice
37	15CSP85	Project work phase II
38	15CSS86	Seminar
39	17MAT31	Engineering Mathematics - III
40	17CS32	Analog and Digital Electronics
41	17CS33	Data Structures and Applications
42	17CS34	Computer Organization
43	17CS35	Unix and Shell Programming
44	17CS36	Discrete Mathematical Structures
45	17CSL37	Analog and Digital Electronics Laboratory
46	17CSL38	Data Structures Laboratory
47	17MAT41	Engineering Mathematics - IV
48	17CS42	Object Oriented Concepts
49	17CS43	Design and Analysis of Algorithms
50	17CS44	Microprocessors and Microcontrollers
51	17CS45	Software Engineering
52	17CS46	Data Communication
53	17CSL47	Design and Analysis of Algorithm Laboratory
54	17CSL48	Microprocessors Laboratory
55	17CS51	Management and Entrepreneurship for IT Industry
56	17CS52	Computer Networks
57	17CS53	Database Management System
58	17CS54	Automata theory and Computability
59	17CSL57	Computer Network Laboratory
60	17CSL58	DBMS Laboratory with mini project
61	17CS61	Cryptography, Network Security and Cyber Law
62	17CS62	Computer Graphics and Visualization
63	17CS63	System Software and Compiler Design
64	17CS64	Operating Systems
65	17CSL67	System Software and Operating System Laboratory
66	17CSL68	Computer Graphics Laboratory with mini project
67	17CS71	Web Technology and its applications
68	17CS72	Advanced Computer Architectures
69	17CS73	Machine Learning
70	17CSL76	Machine Learning Laboratory
71	17CSL77	Web Technology Laboratory with mini project

72	17CSP78	Project Work Phase–I + Project work Seminar
73	17CS81	Internet of Things and Applications
74	17CS82	Big Data Analytics
75	17CS84	Internship/ Professional Practice
76	17CSP85	Project Work-II

Syllabus of mapped course

DATA STRUCTURES AND APPLICATIONS

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - III

Subject Code	15CS33	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS - 04

Course objectives: This course will enable the students to

- Explain fundamentals of data structures and their applications essential for programming/problem solving
- Illustrate linear representation of data structures: Stack, Queues, Lists
- Illustrate linear representation of data structures: Trees, Graphs
- Demonstrate sorting and searching algorithms
- Find suitable data structure during application development/Problem Solving

Module -1	Teaching Hours
Introduction: Data Structures, Classifications (Primitive & Non Primitive), Data structure Operations, Review of Arrays, Structures, Self-Referential Structures, and Unions. Pointers and Dynamic Memory Allocation Functions. Representation of Linear Arrays in Memory, Dynamically allocated arrays, **Array Operations:** Traversing, inserting, deleting, searching, and sorting. Multidimensional Arrays, Polynomials and Sparse Matrices. **Strings:** Basic Terminology, Storing, Operations and Pattern Matching algorithms. Programming Examples. **Text 1: Ch 1:** 1.2, **Ch2:** 2.2 -2.7 **Text 2: Ch 1:** 1.1 -1.4, **Ch 3:** 3.1-3.3,3.5,3.7, **Ch 4:** 4.1-4.9,4.14 **Ref 3: Ch 1:** 1.4	10 Hours
Module -2	
Stacks and Queues *Stacks:* Definition, Stack Operations, Array Representation of Stacks, Stacks using Dynamic Arrays, Stack Applications: Polish notation, Infix to postfix conversion, evaluation of postfix expression, **Recursion** - Factorial, GCD, Fibonacci Sequence, Tower of Hanoi, Ackerman's function. *Queues:* Definition, Array Representation, Queue Operations, Circular Queues, Circular queues using Dynamic arrays, Dequeues, Priority Queues, A Mazing Problem. Multiple Stacks and Queues. Programming Examples. **Text 1: Ch3:** 3.1 -3.7 **Text 2: Ch6:** 6.1 -6.3, 6.5, 6.7-6.10, 6.12, 6.13	10 Hours
Module – 3	

Linked Lists: Definition, Representation of linked lists in Memory, Memory allocation; Garbage Collection. Linked list operations: Traversing, Searching, Insertion, and Deletion. Doubly Linked lists, Circular linked lists, and header linked lists. Linked Stacks and Queues. Applications of Linked lists – Polynomials, Sparse matrix representation. Programming Examples **Text 1: Ch4: 4.1 -4.8 except 4.6** **Text 2: Ch5: 5.1 – 5.10**	**10 Hours**
Module-4	
Trees: Terminology, Binary Trees, Properties of Binary trees, Array and linked Representation of Binary Trees, Binary Tree Traversals - Inorder, postorder, preorder; Additional Binary tree operations. Threaded binary trees, Binary Search Trees – Definition, Insertion, Deletion, Traversal, Searching, Application of Trees-Evaluation of Expression, Programming Examples **Text 1: Ch5: 5.1 –5.5, 5.7** **Text 2: Ch7: 7.1 – 7.9**	**10 Hours**
Module-5	
Graphs: Definitions, Terminologies, Matrix and Adjacency List Representation Of Graphs, Elementary Graph operations, Traversal methods: Breadth First Search and Depth First Search. *Sorting and Searching*: Insertion Sort, Radix sort, Address Calculation Sort. *Hashing*: Hash Table organizations, Hashing Functions, Static and Dynamic Hashing. *Files and Their Organization:* Data Hierarchy, File Attributes, Text Files and Binary Files, Basic File Operations, File Organizations and Indexing **Text 1: Ch6: 6.1 –6.2, Ch 7:7.2, Ch 8:8.1-8.3** **Text 2: Ch8: 8.1 – 8.7, Ch 9:9.1-9.3,9.7,9.9** **Reference 2: Ch 16: 16.1 - 16.7**	**10 Hours**
Course outcomes: After studying this course, students will be able to: • Use different types of data structures, operations and algorithms • Apply searching and sorting operations on files • Use stack, Queue, Lists, Trees and Graphs in problem solving • Implement all data structures in a high-level language for problem solving.	
Graduate Attributes (as per NBA) 1. Engineering Knowledge 2. Design/Development of Solutions 3. Conduct Investigations of Complex Problems 4. Problem Analysis for suitability of data structures.	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	

COMPUTER ORGANIZATION

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - III

Subject Code	15CS34	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives:

This course will enable the students to

- Explain the basic sub systems of a computer, their organization, structure and operation.
- Illustrate the concept of programs as sequences of machine instructions.
- Demonstrate different ways of communicating with I/O devices and standard I/O interfaces.
- Describe memory hierarchy and concept of virtual memory.
- Describe arithmetic and logical operations with integer and floating-point operands.
- Illustrate organization of a simple processor, pipelined processor and other computing systems.

Module -1	Teaching Hours
Basic Structure of Computers: Basic Operational Concepts, Bus Structures, Performance – Processor Clock, Basic Performance Equation, Clock Rate, Performance Measurement Machine Instructions and Programs: Memory Location and Addresses, Memory Operations, Instructions and Instruction Sequencing, Addressing Modes, Assembly Language, Basic Input and Output Operations, Stacks and Queues, Subroutines, Additional Instructions, Encoding of Machine Instructions	10Hours
Module -2	
Input/Output Organization: Accessing I/O Devices, Interrupts – Interrupt Hardware, Enabling and Disabling Interrupts, Handling Multiple Devices, Controlling Device Requests, Exceptions, Direct Memory Access, Buses Interface Circuits, Standard I/O Interfaces – PCI Bus, SCSI Bus, USB.	10 Hours
Module – 3	
Memory System: Basic Concepts, Semiconductor RAM Memories, Read Only Memories, Speed, Size, and Cost, Cache Memories – Mapping Functions, Replacement Algorithms, Performance Considerations, Virtual Memories, Secondary Storage.	10 Hours
Module-4	
Arithmetic: Numbers, Arithmetic Operations and Characters, Addition and Subtraction of Signed Numbers, Design of Fast Adders, Multiplication of Positive Numbers, Signed Operand Multiplication, Fast Multiplication, Integer Division, Floating-point Numbers and Operations.	10 Hours
Module-5	

Basic Processing Unit: Some Fundamental Concepts, Execution of a Complete Instruction, Multiple Bus Organization, Hard-wired Control, Micro programmed Control. Pipelining, Embedded Systems and Large Computer Systems: Basic Concepts of pipelining, Examples of Embedded Systems, Processor chips for embedded applications, Simple Microcontroller, The structure of General-Purpose Multiprocessors.	10 Hours

Course outcomes: After studying this course, students will be able to:

- Explain the basic organization of a computer system.
- Demonstrate functioning of different sub systems, such as processor, Input/output,and memory.
- Illustrate hardwired control and micro programmed control. pipelining, embedded and other computing systems.
- Design and analyse simple arithmetic and logical units.

Graduate Attributes (as per NBA)

1. Engineering Knowledge
2. Problem Analysis
3. Life-Long Learning

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Carl Hamacher, Zvonko Vranesic, Safwat Zaky: Computer Organization, 5th Edition, Tata McGraw Hill, 2002. (Listed topics only from Chapters 1, 2, 4, 5, 6, 7, 8, 9 and12)

Reference Books:

1. William Stallings: Computer Organization & Architecture, 9th Edition, Pearson, 2015.

DATA STRUCTURES LABORATORY

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - III

Laboratory Code	15CSL38	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS - 02

Course objectives:
This laboratory course enable students to get practical experience in design, develop, implement, analyze and evaluation/testing of

- Asymptotic performance of algorithms.
- Linear data structures and their applications such as stacks, queues and lists
- Non-Linear data structures and their applications such as trees and graphs
- Sorting and searching algorithms

Description: (if any)

Implement all the experiments in C Language under Linux / Windows environment.

Laboratory Experiments:

1. Design, Develop and Implement a menu driven Program in C for the following Array operations
   a. Creating an Array of N Integer Elements
   b. Display of Array Elements with Suitable Headings
   c. Inserting an Element (ELEM) at a given valid Position (POS)
   d. Deleting an Element at a given valid Position(POS)
   e. Exit.

   Support the program with functions for each of the above operations.

2. Design, Develop and Implement a Program in C for the following operationson Strings
   a. Read a main String (STR), a Pattern String (PAT) and a Replace String (REP)
   b. Perform Pattern Matching Operation: Find and Replace all occurrences of PAT in STR with REP if PAT exists in STR. Report suitable messages in case PAT does not exist in STR

   Support the program with functions for each of the above operations. Don't use Built-in functions.

3. Design, Develop and Implement a menu driven Program in C for the following operations on STACK of Integers (Array Implementation of Stack with maximum size MAX)
   a. *Push* an Element on to Stack
   b. *Pop* an Element from Stack
   c. Demonstrate how Stack can be used to check *Palindrome*
   d. Demonstrate *Overflow* and *Underflow* situations on Stack
   e. Display the status of Stack

f. Exit

Support the program with appropriate functions for each of the above operations

4. Design, Develop and Implement a Program in C for converting an Infix Expression to Postfix Expression. Program should support for both parenthesized and free parenthesized expressions with the operators: +, -, *, /, %(Remainder), ^(Power) and alphanumeric operands.

5. Design, Develop and Implement a Program in C for the following Stack Applications
   a. Evaluation of **Suffix expression** with single digit operands and operators: +, -, *, /, %, ^
   b. Solving **Tower of Hanoi** problem with n disks

6. Design, Develop and Implement a menu driven Program in C for the following operations on **Circular QUEUE** of Characters (Array Implementation of Queue with maximum size **MAX**)
   a. Insert an Element on to Circular QUEUE
   b. Delete an Element from Circular QUEUE
   c. Demonstrate *Overflow* and *Underflow* situations on Circular QUEUE
   d. Display the status of Circular QUEUE
   e. Exit

   Support the program with appropriate functions for each of the above operations

Continued:

7. Design, Develop and Implement a menu driven Program in C for the following operations on **Singly Linked List (SLL)** of Student Data with the fields: *USN, Name, Branch, Sem, PhNo*
   a. Create a **SLL** of N Students Data by using *front insertion*.
   b. Display the status of SLL and count the number of nodes in it
   c. Perform Insertion / Deletion at End of SLL
   d. Perform Insertion / Deletion at Front of **SLL(Demonstration of stack)**
   e. Exit

8. Design, Develop and Implement a menu driven Program in C for the following operations on **Doubly Linked List (DLL)** of Employee Data with the fields: *SSN, Name, Dept, Designation, Sal, PhNo*
   a. Create a **DLL** of N Employees Data by using *end insertion*.
   b. Display the status of **DLL** and count the number of nodes in it
   c. Perform Insertion and Deletion at End of **DLL**
   d. Perform Insertion and Deletion at Front of **DLL**
   e. Demonstrate how this **DLL** can be used as **Double Ended Queue**
   f. Exit

9. Design, Develop and Implement a Program in C for the following operations on **Singly Circular Linked List (SCLL)** with header nodes
   a. Represent and Evaluate a Polynomial $P(x,y,z) = 6x^2y^2z-4yz^5+3x^3yz+2xy^5z-2xyz^3$
   b. Find the sum of two polynomials **POLY1(x,y,z)** and **POLY2(x,y,z)** and store the result in **POLYSUM(x,y,z)**

   Support the program with appropriate functions for each of the above operations

10. Design, Develop and Implement a menu driven Program in C for the following operations on **Binary Search Tree (BST)** of Integers
    a. Create a BST of N Integers: 6, 9, 5, 2, 8, 15, 24, 14, 7, 8, 5, 2
    b. Traverse the BST in Inorder, Preorder and Post Order
    c. Search the BST for a given element (KEY) and report the appropriate message
    e. Exit

11. Design, Develop and Implement a Program in C for the following operations on **Graph(G)** of Cities
    a. Create a Graph of N cities using Adjacency Matrix.
    b. Print all the nodes reachable from a given starting node in a digraph using DFS/BFS method

12. Given a File of N employee records with a set K of Keys(4-digit) which uniquely determine the records in file F. Assume that file F is maintained in memory by a Hash Table(HT) of m memory locations with L as the set of memory addresses (2-digit) of locations in HT. Let the keys in K and addresses in L are Integers. Design and develop a Program in C that uses Hash function H: K →L as H(K)=K mod m (remainder method), and implement hashing technique to map a given key K to the address space L. Resolve the collision (if any) using linear probing.

Course outcomes:

On the completion of this laboratory course, the students will be able to:

- Analyse and Compare various linear and non-linear data structures
- Code, debug and demonstrate the working nature of different types of data structures and their applications
- Implement, analyse and evaluate the searching and sorting algorithms
- Choose the appropriate data structure for solving real world problems

Graduate Attributes (as per NBA)

1. Engineering Knowledge
2. Problem Analysis
3. Design/Development of Solutions
4. Modern Tool Usage

Conduction of Practical Examination:

1. All laboratory experiments (TWELVE nos) are to be included for practical examination.
2. Students are allowed to pick one experiment from the lot.
3. Strictly follow the instructions as printed on the cover page of answer script
4. Marks distribution: Procedure + Conduction + Viva:20 + 50 +10 (80)
5. **Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.**

SOFTWARE ENGINEERING

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – IV

Subject Code	15CS42	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Outline software engineering principles and activities involved in building large software programs.
- Identify ethical and professional issues and explain why they are of concern to software engineers.
- Describe the process of requirements gathering, requirements classification, requirements specification and requirements validation.
- Differentiate system models, use UML diagrams and apply design patterns.
- Discuss the distinctions between validation testing and defect testing.
- Recognize the importance of software maintenance and describe the intricacies involved in software evolution.
- Apply estimation techniques, schedule project activities and compute pricing.
- Identify software quality parameters and quantify software using measurements and metrics.
- List software quality standards and outline the practices involved.
- Recognize the need for agile software development, describe agile methods, apply agile practices and plan for agility.

Module 1	Teaching Hours
Introduction: Software Crisis, Need for Software Engineering, Professional Software Development, Software Engineering Ethics. Case Studies. **Software Processes**: Models: Waterfall Model (Sec 2.1.1), Incremental Model (Sec 2.1.2) and Spiral Model (Sec 2.1.3). Process activities. **Requirements Engineering**: Requirements Engineering Processes (**Chap 4**). Requirements Elicitation and Analysis (Sec 4.5). Functional and non-functional requirements (Sec 4.1). The software Requirements Document (Sec 4.2). Requirements Specification (Sec 4.3). Requirements validation (Sec 4.6). Requirements Management (Sec 4.7).	12 Hours
Module 2	
System Models: Context models (Sec 5.1). Interaction models (Sec 5.2). Structural models (Sec 5.3). Behavioral models (Sec 5.4). Model-driven engineering (Sec 5.5). **Design and Implementation**: Introduction to RUP (Sec 2.4), Design Principles (Chap 17). Object-Oriented design using the UML (Sec 7.1). Design patterns (Sec 7.2). Implementation issues (Sec 7.3). Open source development (Sec 7.4).	11 Hours
Module 3	
Software Testing: Development testing (Sec 8.1), Test-driven development (Sec 8.2), Release testing (Sec 8.3), User testing (Sec 8.4). Test Automation (**Page no 42, 70,212, 231,444,845**). **Software Evolution**: Evolution processes (Sec 9.1). Program evolution dynamics (Sec 9.2). Software maintenance (Sec 9.3). Legacy system management (Sec 9.4).	9 Hours

Module 4	
Project Planning: Software pricing (Sec 23.1). Plan-driven development (Sec 23.2). Project scheduling (Sec 23.3). Estimation techniques (Sec 23.5). **Quality management**: Software quality (Sec 24.1). Reviews and inspections (Sec 24.3). Software measurement and metrics (Sec 24.4). Software standards (Sec 24.2)	**10 Hours**
Module 5	
Agile Software Development: Coping with Change (Sec 2.3), The Agile Manifesto. Values and Principles. Agile methods: SCRUM (Ref "**The SCRUM Primer, Ver 2.0**") and Extreme Programming (Sec 3.3). Plan-driven and agile development (Sec 3.2). Agile project management (Sec 3.4), Scaling agile methods (Sec 3.5).	**8 Hours**

Course Outcomes: After studying this course, students will be able to:

- Design a software system, component, or process to meet desired needs within realistic constraints.
- Assess professional and ethical responsibility
- Function on multi-disciplinary teams
- Use the techniques, skills, and modern engineering tools necessary for engineering practice
- Analyze, design, implement, verify, validate, implement, apply, and maintain software systems or parts of software systems

Graduate Attributes

- Project Management and Finance
- Conduct Investigations of Complex Problems
- Modern Tool Usage
- Ethics

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Ian Sommerville: Software Engineering, 9th Edition, Pearson Education, 2012.

(Listed topics only from Chapters 1,2,3,4, 5, 7, 8, 9, 23, and 24)

2. The SCRUM Primer, Ver 2.0, http://www.goodagile.com/scrumprimer/scrumprimer20.pdf

Reference Books:

1. Roger S. Pressman: Software Engineering-A Practitioners approach, 7th Edition, Tata McGraw Hill.
2. Pankaj Jalote: An Integrated Approach to Software Engineering, Wiley India.

Web Reference for eBooks on Agile:

1. http://agilemanifesto.org/
2. http://www.jamesshore.com/Agile-Book/

DESIGN AND ANALYSIS OF ALGORITHMS

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – IV

Subject Code	15CS43	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Explain various computational problem solving techniques.
- Apply appropriate method to solve a given problem.
- Describe various methods of algorithm analysis.

Module 1	Teaching Hours
Introduction: What is an Algorithm? (T2:1.1), Algorithm Specification (T2:1.2), Analysis Framework (T1:2.1), **Performance Analysis**: Space complexity, Time complexity (T2:1.3). **Asymptotic Notations:** Big-Oh notation (O), Omega notation (Ω), Theta notation (Θ), and Little-oh notation (o), Mathematical analysis of Non-Recursive and recursive Algorithms with Examples (T1:2.2, 2.3, 2.4). **Important Problem Types:** Sorting, Searching, String processing, Graph Problems, Combinatorial Problems. **Fundamental Data Structures:** Stacks, Queues, Graphs, Trees, Sets and Dictionaries. (T1:1.3,1.4)	10 Hours
Module 2	
Divide and Conquer: General method, Binary search, Recurrence equation for divide and conquer, Finding the maximum and minimum (T2:3.1, 3.3, 3.4), Merge sort, Quick sort (T1:4.1, 4.2), Strassen's matrix multiplication (T2:3.8), Advantages and Disadvantages of divide and conquer. **Decrease and Conquer Approach:** Topological Sort. (T1:5.3)	10 Hours
Module 3	
Greedy Method: General method, Coin Change Problem, Knapsack Problem, Job sequencing with deadlines (T2:4.1, 4.3, 4.5). **Minimum cost spanning trees:** Prim's Algorithm, Kruskal's Algorithm (T1:9.1, 9.2). **Single source shortest paths:** Dijkstra's Algorithm (T1:9.3). **Optimal Tree problem:** Huffman Trees and Codes (T1:9.4). **Transform and Conquer Approach:** Heaps and Heap Sort (T1:6.4).	10 Hours
Module 4	
Dynamic Programming: General method with Examples, Multistage Graphs (T2:5.1, 5.2). **Transitive Closure:** Warshall's Algorithm, **All Pairs Shortest Paths:** Floyd's Algorithm, Optimal Binary Search Trees, Knapsack problem (T1:8.2, 8.3, 8.4), Bellman-Ford Algorithm (T2:5.4), Travelling Sales Person problem (T2:5.9), Reliability design (T2:5.8).	10 Hours
Module 5	
Backtracking: General method (T2:7.1), N-Queens problem (T1:12.1), Sum of subsets problem (T1:12.1), Graph coloring (T2:7.4), Hamiltonian cycles (T2:7.5). **Branch and Bound:** Assignment Problem, Travelling Sales Person problem (T1:12.2), **0/1 Knapsack problem** (T2:8.2, T1:12.2): LC Branch and Bound solution (T2:8.2), FIFO Branch and Bound solution (T2:8.2). **NP-Complete and NP-Hard problems:** Basic	10 Hours

concepts, non-deterministic algorithms, P, NP, NP-Complete, and NP-Hard classes (T2:11.1).	

Course Outcomes: After studying this course, students will be able to

- Describe computational solution to well known problems like searching, sorting etc.
- Estimate the computational complexity of different algorithms.
- Devise an algorithm using appropriate design strategies for problem solving.

Graduate Attributes

- Engineering Knowledge
- Problem Analysis
- Design/Development of Solutions
- Conduct Investigations of Complex Problems
- Life-Long Learning

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

T1. Introduction to the Design and Analysis of Algorithms, Anany Levitin., 2rd Edition, 2009. Pearson.
T2. Computer Algorithms/C++, Ellis Horowitz, Satraj Sahni and Rajasekaran, 2nd Edition, 2014, Universities Press.

Reference Books:

1. Introduction to Algorithms, Thomas H. Cormen, Charles E. Leiserson, Ronal L. Rivest, Clifford Stein, 3rd Edition, PHI
2. Design and Analysis of Algorithms , S. Sridhar, Oxford (Higher Education)

MICROPROCESSORS AND MICROCONTROLLERS

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – IV

Subject Code	15CS44	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Make familiar with importance and applications of microprocessors and microcontrollers
- Expose architecture of 8086 microprocessor and ARM processor
- Familiarize instruction set of ARM processor

Module 1	**Teaching Hours**
The x86 microprocessor: Brief history of the x86 family, Inside the 8088/86, Introduction to assembly programming, Introduction to Program Segments, The Stack, Flag register, x86 Addressing Modes. **Assembly language programming:** Directives & a Sample Program, Assemble, Link & Run a program, More Sample programs, Control Transfer Instructions, Data Types and Data Definition, Full Segment Definition, Flowcharts and Pseudo code. **Text book 1: Ch 1: 1.1 to 1.7, Ch 2: 2.1 to 2.7**	**10 Hours**
Module 2	
x86: Instructions sets description, **Arithmetic and logic instructions and programs:** Unsigned Addition and Subtraction, Unsigned Multiplication and Division, Logic Instructions, BCD and ASCII conversion, Rotate Instructions. **INT 21H and INT 10H Programming :** Bios INT 10H Programming , DOS Interrupt 21H. 8088/86 Interrupts, x86 PC and Interrupt Assignment. **Text book 1: Ch 3: 3.1 to 3.5, Ch 4: 4.1 , 4.2 Chapter 14: 14.1 and 14.2**	**10 Hours**
Module 3	
Signed Numbers and Strings: Signed number Arithmetic Operations, String operations. **Memory and Memory interfacing:** Memory address decoding, data integrity in RAM and ROM, 16-bit memory interfacing. **8255 I/O programming:** I/O addresses MAP of x86 PC's, programming and interfacing the 8255. **Text book 1: Ch 6: 6.1, 6.2, Ch 10: 10.2, 10.4, 10.5, Ch 11: 11.1 to 11.4**	**10 Hours**
Module 4	
Microprocessors versus Microcontrollers, **ARM Embedded Systems** :The RISC design philosophy, The ARM Design Philosophy, Embedded System Hardware, Embedded System Software, **ARM Processor Fundamentals** : Registers , Current Program Status Register , Pipeline, Exceptions, Interrupts, and the Vector Table , Core Extensions **Text book 2:Ch 1:1.1 to 1.4, Ch 2:2.1 to 2.5**	**10 Hours**
Module 5	
Introduction to the ARM Instruction Set : Data Processing Instructions , Branch Instructions, Software Interrupt Instructions, Program Status Register Instructions, Coprocessor Instructions, Loading Constants, Simple programming exercises **Text book 2: Ch 3:3.1 to 3.6 (Excluding 3.5.2)**	**10 Hours**

Course Outcomes: After studying this course, students will be able to

- Differentiate between microprocessors and microcontrollers
- Design and develop assembly language code to solve problems
- Gain the knowledge for interfacing various devices to x86 family and ARM processor
- Demonstrate design of interrupt routines for interfacing devices

Graduate Attributes

- Engineering Knowledge
- Problem Analysis
- Design/Development of Solutions

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Muhammad Ali Mazidi, Janice Gillispie Mazidi, Danny Causey, The x86 PC Assembly Language Design and Interfacing, 5th Edition, Pearson, 2013.
2. **ARM system developers guide**, Andrew N Sloss, Dominic Symes and Chris Wright, Elsevier,Morgan Kaufman publishers, 2008

Reference Books:

1. Douglas V. Hall: Microprocessors and Interfacing, Revised 2nd Edition, TMH, 2006.
2. K. Uday Kumar & B.S. Umashankar : Advanced Microprocessors & IBM-PC Assembly Language Programming, TMH 2003.
3. Ayala : The 8086 Microprocessor: programming and interfacing – 1st edition, Cengage Learning.
4. The Definitive Guide to the ARM Cortex-M3, by Joseph Yiu, 2nd Edition , Newnes, 2009.
5. The Insider's Guide to the ARM7 based microcontrollers, Hitex Ltd.,1st edition, 2005.
6. ARM System-on-Chip Architecture, Steve Furber, Second Edition, Pearson, 2015.
7. Architecture, Programming and Interfacing of Low power Processors- ARM7, Cortex-M and MSP430, Lyla B Das Cengage Learning, 1st Edition.

OBJECT ORIENTED CONCEPTS

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – IV

Subject Code	15CS45	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Learn fundamental features of object oriented language and JAVA
- Set up Java JDK environment to create, debug and run simple Java programs.
- Create multi-threaded programs and event handling mechanisms
- Introduce event driven Graphical User Interface (GUI) programming using applets and swings.

Module 1	Teaching Hours
Introduction to Object Oriented Concepts: A Review of structures, Procedure-Oriented Programming system, Object Oriented Programming System, Comparison of Object Oriented Language with C, Console I/O, variables and reference variables, Function Prototyping, Function Overloading. **Class and Objects:** Introduction, member functions and data, objects and functions, objects and arrays, Namespaces, Nested classes, Constructors, Destructors. **Text book 1: Ch 1: 1.1 to 1.9 Ch 2: 2.1 to 2.6 Ch 4: 4.1 to 4.2**	10 Hours
Module 2	
Introduction to Java: Java's magic: the Byte code, Java Development Kit (JDK); the Java Buzzwords, Object-oriented programming; Simple Java programs. Data types, variables and arrays, Operators, Control Statements. **Text book 2: Ch:1 Ch: 2 Ch:3 Ch:4 Ch:5**	10 Hours
Module 3	
Classes, Inheritance, Exceptions, Packages and Interfaces: Classes: Classes fundamentals; Declaring objects; Constructors, this keyword, garbage collection. **Inheritance:** inheritance basics, using super, creating multi level hierarchy, method overriding. **Exception handling:** Exception handling in Java. Packages, Access Protection, Importing Packages, Interfaces. **Text book 2: Ch:6 Ch: 8 Ch:9 Ch:10**	10 Hours
Module 4	
Multi Threaded Programming, Event Handling: Multi Threaded Programming: What are threads? How to make the classes threadable ; Extending threads; Implementing runnable; Synchronization, Changing state of the thread; Bounded buffer problems, read-write problem, producer consumer problems. **Event Handling:** Two event handling mechanisms, The delegation event model; Event classes; Sources of events; Event listener interfaces; Using the delegation event model; Adapter classes; Inner classes. **Text book 2: Ch 11: Ch: 22**	10 Hours
Module 5	
The Applet Class: Introduction, Two types of Applets; Applet basics; Applet Architecture; An Applet skeleton; Simple Applet display methods; Requesting repainting;	10 Hours

Using the Status Window, The HTML APPLET tag, Passing parameters to Applets, getDocumentBase() and getCodeBase(), ApletContext and showDocument(), The AudioClip Interface, The AppletStub Interface,Output to the Console. **Swings:** Swings: The origins of Swing, Two key Swing features, Components and Containers, The Swing Packages, A simple Swing Application, Create a Swing Applet, JLabel and ImageIcon, JTextField,The Swing Buttons, JTabbedpane, JScrollPane, JList, JComboBox, JTable. **Text book 2: Ch 21: Ch: 29 Ch: 30**	

Course Outcomes: After studying this course, students will be able to

- Explain the object-oriented concepts and JAVA.
- Develop computer programs to solve real world problems in Java.
- Develop simple GUI interfaces for a computer program to interact with users, and to understand the event-based GUI handling principles using Applets and swings.

Graduate Attributes

- Programming Knowledge
- Design/Development of Solutions
- Conduct Investigations of Complex Problems
- Life-Long Learning

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Sourav Sahay, Object Oriented Programming with C++ , 2nd Ed, Oxford University Press,2006.
   (Chapters 1, 2, 4)
2. Herbert Schildt, Java The Complete Reference, 7th Edition, Tata McGraw Hill, 2007.
   (Chapters 1, 2, 3, 4, 5, 6, 8, 9,10, 11, 21, 22, 29, 30)

Reference Book:

1. Mahesh Bhave and Sunil Patekar, "Programming with Java", First Edition, Pearson Education,2008, ISBN 9788131720806
2. Herbert Schildt, The Complete Reference C++, 4th Edition, Tata McGraw Hill, 2003.
3. Stanley B.Lippmann, Josee Lajoie, C++ Primer, 4th Edition, Pearson Education, 2005.
4. Rajkumar Buyya,S Thamarai selvi, xingchen chu, Object oriented Programming with java, Tata McGraw Hill education private limited.
5. Richard A Johnson, Introduction to Java Programming and OOAD, CENGAGE Learning.
6. E Balagurusamy, Programming with Java A primer, Tata McGraw Hill companies.

Note: Every institute shall organize a bridge organize on C++ either in the vacation or in the beginning of even semester.

DATA COMMUNICATION

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – IV

Subject Code	15CS46	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Comprehend the transmission technique of digital data between two or more computers and a computer network that allows computers to exchange data.
- Explain with the basics of data communication and various types of computer networks.
- Illustrate TCP/IP protocol suite and switching criteria.
- Demonstrate Medium Access Control protocols for reliable and noisy channels.
- Expose wireless and wired LANs along with IP version.

Contents	Teaching Hours
Module 1	
Introduction: Data Communications, Networks, Network Types, Internet History, Standards and Administration, **Networks Models**: Protocol Layering, TCP/IP Protocol suite, The OSI model, **Introduction to Physical Layer-1:** Data and Signals, Digital Signals, Transmission Impairment, Data Rate limits, Performance, **Digital Transmission**: Digital to digital conversion (Only Line coding: Polar, Bipolar and Manchester coding).	**10 Hours**
Module 2	
Physical Layer-2: Analog to digital conversion (only PCM), Transmission Modes, **Analog Transmission**: Digital to analog conversion, **Bandwidth Utilization**: Multiplexing and Spread Spectrum, **Switching**: Introduction, Circuit Switched Networks and Packet switching.	**10 Hours**
Module 3	
Error Detection and Correction: Introduction, Block coding, Cyclic codes, Checksum, Forward error correction, **Data link control**: DLC services, Data link layer protocols, HDLC, and Point to Point protocol (Framing, Transition phases only).	**10 Hours**
Module 4	
Media Access control: Random Access, Controlled Access and Channelization, **Wired LANs Ethernet**: Ethernet Protocol, Standard Ethernet, Fast Ethernet, Gigabit Ethernet and 10 Gigabit Ethernet, **Wireless LANs:** Introduction, IEEE 802.11 Project and Bluetooth.	**10 Hours**
Module 5	
Other wireless Networks: WIMAX, Cellular Telephony, Satellite networks, **Network layer Protocols** : Internet Protocol, ICMPv4,Mobile IP, **Next generation IP**: IPv6 addressing, The IPv6 Protocol, The ICMPv6 Protocol and Transition from IPv4 to IPv6.	**10 Hours**

Course Outcomes: After studying this course, students will be able to

- Illustrate basic computer network technology.
- Identify the different types of network topologies and protocols.
- Enumerate the layers of the OSI model and TCP/IP functions of each layer.
- Make out the different types of network devices and their functions within a network.

• Demonstrate the skills of subnetting and routing mechanisms.
Graduate Attributes
1. Engineering Knowledge 2. Design Development of solution(Partly) 3. Modern Tool Usage 4. Problem Analysis
Question paper pattern:
The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.
Text Book:
Behrouz A. Forouzan, Data Communications and Networking 5E, 5th Edition, Tata McGraw-Hill, 2013. (Chapters 1.1 to 1.5, 2.1 to 2.3, 3.1, 3.3 to 3.6, 4.1 to 4.3, 5.1, 6.1, 6.2, 8.1 to 8.3, 10.1 to 10.5, 11.1 to 11.4, 12.1 to 12.3, 13.1 to 13.5, 15.1 to 15.3, 16.1 to 16.3, 19.1 to 19.3, 22.1 to 22.4)
Reference Books:
1. Alberto Leon-Garcia and Indra Widjaja. Communication Networks - Fundamental Concepts and Key architectures, 2nd Edition Tata McGraw-Hill, 2004. 2. William Stallings. Data and Computer Communication, 8th Edition, Pearson Education, 2007. 3. Larry L. Peterson and Bruce S. Davie. Computer Networks – A Systems Approach, 4th Edition, Elsevier, 2007. 4. Nader F. Mir. Computer and Communication Networks, Pearson Education, 2007.

DESIGN AND ANALYSIS OF ALGORITHM LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – IV

Subject Code	15CSL47	IA Marks	20
Number of Lecture Hours/Week	01 I + 02 P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- Design and implement various algorithms in JAVA
- Employ various design strategies for problem solving.
- Measure and compare the performance of different algorithms.

Description

Design, develop, and implement the specified algorithms for the following problems using Java language under LINUX /Windows environment.Netbeans/Eclipse IDE tool can be used for development and demonstration.

Experiments

1	A	Create a Java class called ***Student***with the following details as variables within it. (i) USN (ii) Name (iii) Branch (iv) Phone Write a Java program to create n*Student* objects and print the USN, Name, Branch, and Phoneof these objects with suitable headings.
	B	Write a Java program to implement the Stack using arrays. Write Push(), Pop(), and Display() methods to demonstrate its working.
2	A	Design a superclass called ***Staff*** with details as StaffId, Name, Phone, Salary. Extend this class by writing three subclasses namely ***Teaching*** (domain, publications), ***Technical*** (skills), and ***Contract*** (period). Write a Java program to read and display at least 3 *staff* objects of all three categories.
	B	Write a Java class called ***Customer*** to store their name and date_of_birth. The date_of_birth format should be dd/mm/yyyy. Write methods to read customer data as <name, dd/mm/yyyy> and display as <name, dd, mm, yyyy> using StringTokenizer class considering the delimiter character as "/".
3	A	Write a Java program to read two integers *a* and*b*. Compute *a*/*b* and print, when *b* is not zero. Raise an exception when *b* is equal to zero.
	B	Write a Java program that implements a multi-thread application that has three threads. First thread generates a random integer for every 1 second, second thread computes the square of the number andprints, third thread will print the value of cube of the number.
4		Sort a given set of *n* integer elements using **Quick Sort** method and compute its time complexity. Run the program for varied values of *n*> 5000 and record the time taken to sort. Plot a graph of the time taken versus *n*on graph sheet. The elements can be read from a file or can be generated using the random number generator. Demonstrate using Java how the divide-and-conquer method works along with its time complexity analysis: worst case, average case and best case.

5	Sort a given set of ***n*** integer elements using **Merge Sort** method and compute its time complexity. Run the program for varied values of *n*> 5000, and record the time taken to sort. Plot a graph of the time taken versus ***n***on graph sheet. The elements can be read from a file or can be generated using the random number generator. Demonstrate using Java how the divide-and-conquer method works along with its time complexity analysis: worst case, average case and best case.
6	Implement in Java, the **0/1 Knapsack** problem using (a) Dynamic Programming method (b) Greedy method.
7	From a given vertex in a weighted connected graph, find shortest paths to other vertices using **Dijkstra's algorithm**. Write the program in Java.
8	Find Minimum Cost Spanning Tree of a given connected undirected graph using **Kruskal'salgorithm.** Use Union-Find algorithms in your program.
9	Find Minimum Cost Spanning Tree of a given connected undirected graph using **Prim's algorithm.**
10	Write Java programs to (a) Implement All-Pairs Shortest Paths problem using **Floyd's algorithm**. (b) Implement **Travelling Sales Person problem** using Dynamic programming.
11	Design and implement in Java to find a **subset** of a given set S = {Sl, S2,....Sn} of ***n*** positive integers whose SUM is equal to a given positive integer ***d***. For example, if S ={1, 2, 5, 6, 8} and ***d***= 9, there are two solutions {1,2,6}and {1,8}. Display a suitable message, if the given problem instance doesn't have a solution.
12	Design and implement in Java to find all **Hamiltonian Cycles** in a connected undirected Graph G of *n* vertices using backtracking principle.

Course Outcomes: The students should be able to:

- Design algorithms using appropriate design techniques (brute-force, greedy, dynamic programming, etc.)
- Implement a variety of algorithms such assorting, graph related, combinatorial, etc., in a high level language.
- Analyze and compare the performance of algorithms using language features.
- Apply and implement learned algorithm design techniques and data structuresto solve real-world problems.

Graduate Attributes

- Engineering Knowledge
- Problem Analysis
- Modern Tool Usage
- Conduct Investigations of Complex Problems
- Design/Development of Solutions

Conduction of Practical Examination:

All laboratory experiments (Twelve problems) are to be included for practical examination. Students are allowed to pick one experiment from the lot.
To generate the data set use random number generator function.
Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
Marks distribution: Procedure + Conduction + Viva: 20 + 50 + 10 (80). Change of experiment is allowed only once and marks allotted to the procedure.

MICROPROCESSOR AND MICROCONTROLLER LABORATORY

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – IV

Subject Code	15CSL48	IA Marks	20
Number of Lecture Hours/Week	01 I + 02 P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- To provide practical exposure to the students on microprocessors, design and coding knowledge on 80x86 family/ARM. To give the knowledge and practical exposure on connectivity and execute of interfacing devices with 8086/ARM kit like LED displays, Keyboards, DAC/ADC, and various other devices.

Description

Demonstration and Explanation hardware components and Faculty in-charge should explain 8086 architecture, pin diagram in one slot. The second slot, the Faculty in-charge should explain instruction set types/category etc. Students have to prepare a write-up on the same and include it in the Lab record and to be evaluated.

Laboratory Session-1: Write-up on Microprocessors, 8086 Functional block diagram, Pin diagram and description. The same information is also taught in theory class, this helps the students to understand better.

Laboratory Session-2: Write-up on Instruction group, Timing diagrams, etc. The same information is also taught in theory class, this helps the students to understand better.

Note: These TWO Laboratory sessions are used to fill the gap between theory classes and practical sessions. Both sessions are evaluated as lab experiments for 20 marks.

Experiments

- Develop and execute the following programs using 8086 Assembly Language. Any suitable assembler like MASM/TASM/8086 kit or any equivalent software may be used.
- Program should have suitable comments.
- The board layout and the circuit diagram of the interface are to be provided to the student during the examination.
- Software Required: Open source ARM Development platform, KEIL IDE and Proteus for simulation

SOFTWARE PROGRAMS: PART A

1. Design and develop an assembly language program to search a key element "X" in a list of 'n' 16-bit numbers. Adopt Binary search algorithm in your program for searching.
2. Design and develop an assembly program to sort a given set of 'n' 16-bit numbers in ascending order. Adopt Bubble Sort algorithm to sort given elements.
3. Develop an assembly language program to reverse a given string and verify whether it is a palindrome or not. Display the appropriate message.
4. Develop an assembly language program to compute nCr using recursive procedure. Assume that 'n' and 'r' are non-negative integers.

5. Design and develop an assembly language program to read the current time and Date from the system and display it in the standard format on the screen.
6. To write and simulate ARM assembly language programs for data transfer, arithmetic and logical operations (Demonstrate with the help of a suitable program)
7. To write and simulate C Programs for ARM microprocessor using KEIL (Demonstrate with the help of a suitable program)

Note : To use KEIL one may refer the book: Insider's Guide to the ARM7 based microcontrollers, Hitex Ltd.,1st edition, 2005

HARDWARE PROGRAMS: PART B

8. a. Design and develop an assembly program to demonstrate BCD Up-Down Counter (00-99) on the Logic Controller Interface.

   b. Design and develop an assembly program to read the status of two 8-bit inputs (X & Y) from the Logic Controller Interface and display X*Y.
9. Design and develop an assembly program to display messages "FIRE" and "HELP" alternately with flickering effects on a 7-segment display interface for a suitable period of time. Ensure a flashing rate that makes it easy to read both the messages (Examiner does not specify these delay values nor is it necessary for the student to compute these values).
10. Design and develop an assembly program to drive a Stepper Motor interface and rotate the motor in specified direction (clockwise or counter-clockwise) by N steps (Direction and N are specified by the examiner). Introduce suitable delay between successive steps. (Any arbitrary value for the delay may be assumed by the student).
11. Design and develop an assembly language program to
    a. Generate the Sine Wave using DAC interface (The output of the DAC is to be displayed on the CRO)
    b. Generate a Half Rectified Sine waveform using the DAC interface. (The output of the DAC is to be displayed on the CRO)
12. To interface LCD with ARM processor-- ARM7TDMI/LPC2148. Write and execute programs in C language for displaying text messages and numbers on LCD
13. To interface Stepper motor with ARM processor-- ARM7TDMI/LPC2148. Write a program to rotate stepper motor

Study Experiments:

1. Interfacing of temperature sensor with ARM freedom board (or any other ARM microprocessor board) and display temperature on LCD
2. To design ARM cortex based automatic number plate recognition system
3. To design ARM based power saving system

Course Outcomes: After studying this course, students will be able to

- Learn 80x86 instruction sets and gives the knowledge of how assembly language works.
- Design and implement programs written in 80x86 assembly language
- Know functioning of hardware devices and interfacing them to x86 family
- Choose processors for various kinds of applications.

Graduate Attributes

- Engineering Knowledge
- Problem Analysis
- Modern Tool Usage
- Conduct Investigations of Complex Problems
- Design/Development of Solutions

COMPUTER NETWORKS [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – V			
Subject Code	15CS52	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to

- Demonstration of application layer protocols
- Discuss transport layer services and understand UDP and TCP protocols
- Explain routers, IP and Routing Algorithms in network layer
- Disseminate the Wireless and Mobile Networks covering IEEE 802.11 Standard
- Illustrate concepts of Multimedia Networking, Security and Network Management

Module – 1	**Teaching Hours**
Application Layer: Principles of Network Applications: Network Application Architectures, Processes Communicating, Transport Services Available to Applications, Transport Services Provided by the Internet, Application-Layer Protocols. The Web and HTTP: Overview of HTTP, Non-persistent and Persistent Connections, HTTP Message Format, User-Server Interaction: Cookies, Web Caching, The Conditional GET. File Transfer: FTP Commands & Replies, Electronic Mail in the Internet: SMTP, Comparison with HTTP, Mail Message Format, Mail Access Protocols, DNS: The Internet's Directory Service: Services Provided by DNS, Overview of How DNS Works, DNS Records and Messages, Peer-to-Peer Applications: P2P File Distribution, Distributed Hash Tables, Socket Programming: creating Network Applications: Socket Programming with UDP, Socket Programming with TCP. **T1: Chap 2**	**10 Hours**
Module – 2	
Transport Layer : Introduction and Transport-Layer Services: Relationship Between Transport and Network Layers, Overview of the Transport Layer in the Internet, Multiplexing and Demultiplexing: Connectionless Transport: UDP,UDP Segment Structure, UDP Checksum, Principles of Reliable Data Transfer: Building a Reliable Data Transfer Protocol, Pipelined Reliable Data Transfer Protocols, Go-Back-N, Selective repeat, Connection-Oriented Transport TCP: The TCP Connection, TCP Segment Structure, Round-Trip Time Estimation and Timeout, Reliable Data Transfer, Flow Control, TCP Connection Management, Principles of Congestion Control: The Causes and the Costs of Congestion, Approaches to Congestion Control, Network-assisted congestion-control example, ATM ABR Congestion control, TCP Congestion Control: Fairness. **T1: Chap 3**	**10 Hours**
Module – 3	
The Network layer: What's Inside a Router?: Input Processing, Switching, Output Processing, Where Does Queuing Occur? Routing control plane, IPv6,A Brief foray into IP Security, Routing Algorithms: The Link-State (LS) Routing Algorithm, The Distance-Vector (DV) Routing Algorithm, Hierarchical Routing,	**10 Hours**

Routing in the Internet, Intra-AS Routing in the Internet: RIP, Intra-AS Routing in the Internet: OSPF, Inter/AS Routing: BGP, Broadcast Routing Algorithms and Multicast. **T1: Chap 4: 4.3-4.7**	
Module – 4	
Wireless and Mobile Networks: Cellular Internet Access: An Overview of Cellular Network Architecture, 3G Cellular Data Networks: Extending the Internet to Cellular subscribers, On to 4G:LTE,Mobility management: Principles, Addressing, Routing to a mobile node, Mobile IP, Managing mobility in cellular Networks, Routing calls to a Mobile user, Handoffs in GSM, Wireless and Mobility: Impact on Higher-layer protocols. **T1: Chap: 6 : 6.4-6.8**	**10 Hours**
Module – 5	
Multimedia Networking: Properties of video, properties of Audio, Types of multimedia Network Applications, Streaming stored video: UDP Streaming, HTTP Streaming, Adaptive streaming and DASH, content distribution Networks, case studies: : Netflix, You Tube and Kankan. **Network Support for Multimedia**: Dimensioning Best-Effort Networks, Providing Multiple Classes of Service, Diffserv, Per-Connection Quality-of-Service (QoS) Guarantees: Resource Reservation and Call Admission **T1: Chap: 7: 7.1,7.2,7.5**	**10 Hours**
Course outcomes: The students should be able to:	
• Explain principles of application layer protocols • Recognize transport layer services and infer UDP and TCP protocols • Classify routers, IP and Routing Algorithms in network layer • Understand the Wireless and Mobile Networks covering IEEE 802.11 Standard • Describe Multimedia Networking and Network Management	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. James F Kurose and Keith W Ross, Computer Networking, A Top-Down Approach, Sixth edition, Pearson,2017.	
Reference Books:	
1. Behrouz A Forouzan, Data and Communications and Networking, Fifth Edition, McGraw Hill, Indian Edition 2. Larry L Peterson and Brusce S Davie, Computer Networks, fifth edition, ELSEVIER 3. Andrew S Tanenbaum, Computer Networks, fifth edition, Pearson 4. Mayank Dave, Computer Networks, Second edition, Cengage Learning	

DATABASE MANAGEMENT SYSTEM
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – V

Subject Code	15CS53	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Provide a strong foundation in database concepts, technology, and practice.
- Practice SQL programming through a variety of database problems.
- Demonstrate the use of concurrency and transactions in database
- Design and build database applications for real world problems.

Module – 1	**Teaching Hours**
Introduction to Databases: Introduction, Characteristics of database approach, Advantages of using the DBMS approach, History of database applications. **Overview of Database Languages and Architectures**: Data Models, Schemas, and Instances. Three schema architecture and data independence, database languages, and interfaces, The Database System environment. **Conceptual Data Modelling using Entities and Relationships**: Entity types, Entity sets, attributes, roles, and structural constraints, Weak entity types, ER diagrams, examples, Specialization and Generalization. **Textbook 1:Ch 1.1 to 1.8, 2.1 to 2.6, 3.1 to 3.10**	**10 Hours**
Module – 2	
Relational Model: Relational Model Concepts, Relational Model Constraints and relational database schemas, Update operations, transactions, and dealing with constraint violations. **Relational Algebra**: Unary and Binary relational operations, additional relational operations (aggregate, grouping, etc.) Examples of Queries in relational algebra. **Mapping Conceptual Design into a Logical Design**: Relational Database Design using ER-to-Relational mapping. **SQL**: SQL data definition and data types, specifying constraints in SQL, retrieval queries in SQL, INSERT, DELETE, and UPDATE statements in SQL, Additional features of SQL. **Textbook 1: Ch4.1 to 4.5, 5.1 to 5.3, 6.1 to 6.5, 8.1; Textbook 2: 3.5**	**10 Hours**
Module – 3	
SQL : Advances Queries: More complex SQL retrieval queries, Specifying constraints as assertions and action triggers, Views in SQL, Schema change statements in SQL. **Database Application Development**: Accessing databases from applications, An introduction to JDBC, JDBC classes and interfaces, SQLJ, Stored procedures, Case study: The internet Bookshop. **Internet Applications**: The three-Tier application architecture, The presentation layer, The Middle Tier **Textbook 1: Ch7.1 to 7.4; Textbook 2: 6.1 to 6.6, 7.5 to 7.7.**	**10 Hours**
Module – 4	
Normalization: Database Design Theory – Introduction to Normalization using Functional and Multivalued Dependencies: Informal design guidelines for relation schema, Functional Dependencies, Normal Forms based on Primary Keys, Second and Third Normal Forms, Boyce-Codd Normal Form, Multivalued Dependency and Fourth Normal Form, Join Dependencies and Fifth Normal	**10 Hours**

Form. **Normalization Algorithms:** Inference Rules, Equivalence, and Minimal Cover, Properties of Relational Decompositions, Algorithms for Relational Database Schema Design, Nulls, Dangling tuples, and alternate Relational Designs, Further discussion of Multivalued dependencies and 4NF, Other dependencies and Normal Forms **Textbook 1: Ch14.1 to 14.7, 15.1 to 15.6**	
Module – 5	
Transaction Processing: Introduction to Transaction Processing, Transaction and System concepts, Desirable properties of Transactions, Characterizing schedules based on recoverability, Characterizing schedules based on Serializability, Transaction support in SQL. **Concurrency Control in Databases:** Two-phase locking techniques for Concurrency control, Concurrency control based on Timestamp ordering, Multiversion Concurrency control techniques, Validation Concurrency control techniques, Granularity of Data items and Multiple Granularity Locking. **Introduction to Database Recovery Protocols:** Recovery Concepts, NO-UNDO/REDO recovery based on Deferred update, Recovery techniques based on immediate update, Shadow paging, Database backup and recovery from catastrophic failures **Textbook 1: 20.1 to 20.6, 21.1 to 21.7, 22.1 to 22.4, 22.7.**	**10 Hours**
Course outcomes: The students should be able to:	
• Identify, analyze and define database objects, enforce integrity constraints on a database using RDBMS. • Use Structured Query Language (SQL) for database manipulation. • Design and build simple database systems • Develop application to interact with databases.	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. Fundamentals of Database Systems, Ramez Elmasri and Shamkant B. Navathe, 7th Edition, 2017, Pearson. 2. Database management systems, Ramakrishnan, and Gehrke, 3rd Edition, 2014, McGraw Hill	
Reference Books:	
1. Silberschatz Korth and Sudharshan, Database System Concepts, 6th Edition, Mc-GrawHill, 2013. 2. Coronel, Morris, and Rob, Database Principles Fundamentals of Design, Implementation and Management, Cengage Learning 2012.	

OBJECT ORIENTED MODELING AND DESIGN [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – V			
Subject Code	15CS551	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to

- Describe the concepts involved in Object-Oriented modelling and their benefits.
- Demonstrate concept of use-case model, sequence model and state chart model for a given problem.
- Explain the facets of the unified process approach to design and build a Software system.
- Translate the requirements into implementation for Object Oriented design.
- Choose an appropriate design pattern to facilitate development procedure.

Module – 1	Teaching Hours
Introduction, Modelling Concepts and Class Modelling: What is Object orientation? What is OO development? OO Themes; Evidence for usefulness of OO development; OO modelling history. Modelling as Design technique: Modelling; abstraction; The Three models. Class Modelling: Object and Class Concept; Link and associations concepts; Generalization and Inheritance; A sample class model; Navigation of class models; Advanced Class Modelling, Advanced object and class concepts; Association ends; N-ary associations; Aggregation; Abstract classes; Multiple inheritance; Metadata; Reification; Constraints; Derived Data; Packages. **Text Book-1: Ch 1, 2, 3 and 4**	8 Hours
Module – 2	
UseCase Modelling and Detailed Requirements: Overview; Detailed object-oriented Requirements definitions; System Processes-A use case/Scenario view; Identifying Input and outputs-The System sequence diagram; Identifying Object Behaviour-The state chart Diagram; Integrated Object-oriented Models. **Text Book-2:Chapter- 6:Page 210 to 250**	8 Hours
Module – 3	
Process Overview; System Conception and Domain Analysis: Process Overview: Development stages; Development life Cycle; System Conception: Devising a system concept; elaborating a concept; preparing a problem statement. Domain Analysis: Overview of analysis; Domain Class model: Domain state model; Domain interaction model; Iterating the analysis. **Text Book-1:Chapter- 10,11,and 12**	8 Hours
Module – 4	
Use case Realization :The Design Discipline within up iterations: Object Oriented Design-The Bridge between Requirements and Implementation; Design Classes and Design within Class Diagrams; Interaction Diagrams-Realizing Use Case and defining methods; Designing with Communication Diagrams; Updating the Design Class Diagram; Package Diagrams-Structuring the Major Components; Implementation Issues for Three-Layer Design. **Text Book-2: Chapter 8: page 292 to 346**	8 Hours

Module – 5	
Design Patterns: Introduction; what is a design pattern?, Describing design patterns, the catalogue of design patterns, Organizing the catalogue, How design patterns solve design problems, how to select a design patterns, how to use a design pattern; Creational patterns: prototype and singleton (only); structural patterns adaptor and proxy (only). **Text Book-3: Ch-1: 1.1, 1.3, 1.4, 1.5, 1.6, 1.7, 1.8,Ch-3,Ch-4.**	**8 Hours**
Course outcomes: The students should be able to:	
• Describe the concepts of object-oriented and basic class modelling. • Draw class diagrams, sequence diagrams and interaction diagrams to solve problems. • Choose and apply a befitting design pattern for the given problem.	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. Michael Blaha, James Rumbaugh: Object Oriented Modelling and Design with UML,2nd Edition, Pearson Education,2005 2. Satzinger, Jackson and Burd: Object-Oriented Analysis & Design with the Unified Process, Cengage Learning, 2005. 3. Erich Gamma, Richard Helm, Ralph Johnson and john Vlissides: Design Patterns – Elements of Reusable Object-Oriented Software, Pearson Education,2007.	
Reference Books:	
1. Grady Booch et. al.: Object-Oriented Analysis and Design with Applications,3rd Edition,Pearson Education,2007. 2. 2.Frank Buschmann, ReginaMeunier, Hans Rohnert, Peter Sommerlad, Michel Stal: Pattern –Oriented Software Architecture. A system of patterns , Volume 1, John Wiley and Sons.2007. 3. 3. Booch, Jacobson, Rambaugh : Object-Oriented Analysis and Design with Applications, 3rd edition, pearson, Reprint 2013	

INTRODUCTION TO SOFTWARE TESTING [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – V			
Subject Code	15CS552	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to

- Differentiate the various testing techniques.
- Analyze the problem and derive suitable test cases.
- Apply suitable technique for designing of flow graph.
- Explain the need for planning and monitoring a process.

Module – 1	**Teaching Hours**
Basics of Software Testing: Basic definitions, Software Quality , Requirements, Behaviour and Correctness, Correctness versus Reliability, Testing and Debugging, Test cases, Insights from a Venn diagram, Identifying test cases, Test-generation Strategies, Test Metrics, Error and fault taxonoonies , Levels of testing, Testing and Verification, Static Testing **Textbook 3: Ch 1:1.2 - 1.5, 3; Textbook 1: Ch 1**	8 Hours
Module – 2	
Problem Statements: Generalized pseudo code, the triangle problem, the NextDate function, the commission problem, the SATM (Simple Automatic Teller Machine) problem, the currency converter, Saturn windshield wiper **Functional Testing:** Boundary value analysis, Robustness testing, Worst-case testing, Robust Worst testing for triangle problem, NextDate problem and commission problem, Equivalence classes, Equivalence test cases for the triangle problem, NextDate function, and the commission problem, Guidelines and observations, Decision tables, Test cases for the triangle problem, NextDate function, and the commission problem, Guidelines and observations. **Textbook 1: Ch 2, 5, 6 & 7, Textbook 2: Ch 3**	8 Hours
Module – 3	
Fault Based Testing: Overview, Assumptions in fault based testing, Mutation analysis, Fault-based adequacy criteria, Variations on mutation analysis. **Structural Testing:** Overview, Statement testing, Branch testing, Condition testing, Path testing: DD paths, Test coverage metrics, Basis path testing, guidelines and observations, Data –Flow testing: Definition-Use testing, Slice-based testing, Guidelines and observations. **T2:Chapter 16, 12 T1:Chapter 9 & 10**	8 Hours
Module – 4	
Test Execution: Overview of test execution, from test case specification to test cases, Scaffolding, Generic versus specific scaffolding, Test oracles, Self-checks as oracles, Capture and replay **Process Framework** :Basic principles: Sensitivity, redundancy, restriction, partition, visibility, Feedback, the quality process, Planning and monitoring, Quality goals, Dependability properties ,Analysis Testing, Improving the process, Organizational factors. **Planning and Monitoring the Process:** Quality and process, Test and analysis strategies and plans, Risk planning, monitoring the process, Improving the	8 Hours

process, the quality team. **T2: Chapter 17, 20.**	
Module – 5	
Integration and Component-Based Software Testing: Overview, Integration testing strategies, Testing components and assemblies. System, Acceptance and Regression Testing: Overview, System testing, Acceptance testing, Usability, Regression testing, Regression test selection techniques, Test case prioritization and selective execution. **Levels of Testing, Integration Testing:** Traditional view of testing levels, Alternative life-cycle models, The SATM system, Separating integration and system testing, A closer look at the SATM system, Decomposition-based, call graph-based, Path-based integrations. **T2: Chapter 21 & 22, T1 : Chapter 12 & 13**	8 Hours
Course outcomes: The students should be able to:	
• Derive test cases for any given problem • Compare the different testing techniques • Classify the problem into suitable testing model • Apply the appropriate technique for the design of flow graph. • Create appropriate document for the software artefact.	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. Paul C. Jorgensen: Software Testing, A Craftsman's Approach, 3rd Edition, Auerbach Publications, 2008. 2. Mauro Pezze, Michal Young: Software Testing and Analysis – Process, Principles and Techniques, Wiley India, 2009. 3. Aditya P Mathur: Foundations of Software Testing, Pearson Education, 2008.	
Reference Books:	
1. Software testing Principles and Practices – Gopalaswamy Ramesh, Srinivasan Desikan, 2 nd Edition, Pearson, 2007. 2. Software Testing – Ron Patton, 2nd edition, Pearson Education, 2004. 3. The Craft of Software Testing – Brian Marrick, Pearson Education, 1995. 4. Anirban Basu, Software Quality Assurance, Testing and Metrics, PHI, 2015 5. Naresh Chauhan, Software Testing, Oxford University press.	

ADVANCED JAVA AND J2EE
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – V

Subject Code	15CS553	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Identify the need for advanced Java concepts like Enumerations and Collections
- Construct client-server applications using Java socket API
- Make use of JDBC to access database through Java Programs
- Adapt servlets to build server side programs
- Demonstrate the use of JavaBeans to develop component-based Java software

Module – 1	**Teaching Hours**
Enumerations, Autoboxing and Annotations(metadata): Enumerations, Enumeration fundamentals, the values() and valueOf() Methods, java enumerations are class types, enumerations Inherits Enum, example, type wrappers, Autoboxing, Autoboxing and Methods, Autoboxing/Unboxing occurs in Expressions, Autoboxing/Unboxing, Boolean and character values, Autoboxing/Unboxing helps prevent errors, A word of Warning. Annotations, Annotation basics, specifying retention policy, Obtaining Annotations at run time by use of reflection, Annotated element Interface, Using Default values, Marker Annotations, Single Member annotations, Built-In annotations.	**8 Hours**
Module – 2	
The collections and Framework: Collections Overview, Recent Changes to Collections, The Collection Interfaces, The Collection Classes, Accessing a collection Via an Iterator, Storing User Defined Classes in Collections, The Random Access Interface, Working With Maps, Comparators, The Collection Algorithms, Why Generic Collections?, The legacy Classes and Interfaces, Parting Thoughts on Collections.	**8 Hours**
Module – 3	
String Handling :The String Constructors, String Length, Special String Operations, String Literals, String Concatenation, String Concatenation with Other Data Types, String Conversion and toString( ) Character Extraction, charAt( ), getChars( ), getBytes( ) toCharArray(), String Comparison, equals( ) and equalsIgnoreCase( ), regionMatches( ) startsWith( ) and endsWith( ), equals( ) Versus == , compareTo( ) Searching Strings, Modifying a String, substring( ), concat( ), replace( ), trim( ), Data Conversion Using valueOf( ), Changing the Case of Characters Within a String, Additional String Methods, StringBuffer , StringBuffer Constructors, length( ) and capacity( ), ensureCapacity( ), setLength( ), charAt( ) and setCharAt( ), getChars( ),append( ), insert( ), reverse( ), delete( ) and deleteCharAt( ), replace( ), substring( ), Additional StringBuffer Methods, StringBuilder **Text Book 1: Ch 15**	**8 Hours**

Module – 4	
Background; The Life Cycle of a Servlet; Using Tomcat for Servlet Development; A simple Servlet; The Servlet API; The Javax.servlet Package; Reading Servlet Parameter; The Javax.servlet.http package; Handling HTTP Requests and Responses; Using Cookies; Session Tracking. Java Server Pages (JSP): JSP, JSP Tags, Tomcat, Request String, User Sessions, Cookies, Session Objects **Text Book 1: Ch 31 Text Book 2: Ch 11**	8 Hours
Module – 5	
The Concept of JDBC; JDBC Driver Types; JDBC Packages; A Brief Overview of the JDBC process; Database Connection; Associating the JDBC/ODBC Bridge with the Database; Statement Objects; ResultSet; Transaction Processing; Metadata, Data types; Exceptions. **Text Book 2: Ch 06**	8 Hours

Course outcomes: The students should be able to:

- Interpret the need for advanced Java concepts like enumerations and collections in developing modular and efficient programs
- Build client-server applications and TCP/IP socket programs
- Illustrate database access and details for managing information using the JDBC API
- Describe how servlets fit into Java-based web application architecture
- Develop reusable software components using Java Beans

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Herbert Schildt: JAVA the Complete Reference, 7th/9th Edition, Tata McGraw Hill, 2007.
2. Jim Keogh: J2EE-TheCompleteReference, McGraw Hill, 2007.

Reference Books:

1. Y. Daniel Liang: Introduction to JAVA Programming, 7thEdition, Pearson Education, 2007.
2. Stephanie Bodoff et al: The J2EE Tutorial, 2nd Edition, Pearson Education,2004.
3. Uttam K Roy, Advanced JAVA programming, Oxford University press, 2015.

COMPUTER NETWORK LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – V

Subject Code	15CSL57	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- Demonstrate operation of network and its management commands
- Simulate and demonstrate the performance of GSM and CDMA
- Implement data link layer and transport layer protocols.

Description (If any):

For the experiments below modify the topology and parameters set for the experiment and take multiple rounds of reading and analyze the results available in log files. Plot necessary graphs and conclude. Use NS2/NS3.

Lab Experiments:

PART A

1. Implement three nodes point – to – point network with duplex links between them. Set the queue size, vary the bandwidth and find the number of packets dropped.
2. Implement transmission of ping messages/trace route over a network topology consisting of 6 nodes and find the number of packets dropped due to congestion.
3. Implement an Ethernet LAN using n nodes and set multiple traffic nodes and plot congestion window for different source / destination.
4. Implement simple ESS and with transmitting nodes in wire-less LAN by simulation and determine the performance with respect to transmission of packets.
5. Implement and study the performance of GSM on NS2/NS3 (Using MAC layer) or equivalent environment.
6. Implement and study the performance of CDMA on NS2/NS3 (Using stack called Call net) or equivalent environment.

PART B

Implement the following in Java:

7. Write a program for error detecting code using CRC-CCITT (16- bits).
8. Write a program to find the shortest path between vertices using bellman-ford algorithm.
9. Using TCP/IP sockets, write a client – server program to make the client send the file name and to make the server send back the contents of the requested file if present.
10. Write a program on datagram socket for client/server to display the messages on client side, typed at the server side.
11. Write a program for simple RSA algorithm to encrypt and decrypt the data.
12. Write a program for congestion control using leaky bucket algorithm.

Study Experiment / Project:

NIL

Course outcomes: The students should be able to:

- Analyze and Compare various networking protocols.
- Demonstrate the working of different concepts of networking.

- Implement, analyze and evaluate networking protocols in NS2 / NS3

Conduction of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Students are allowed to pick one experiment from part A and part B with lot.
3. Strictly follow the instructions as printed on the cover page of answer script
4. Marks distribution: Procedure + Conduction + Viva: 80

   Part A: 10+25+5 =40

   Part B: 10+25+5 =40

5. Change of experiment is allowed only once and marks allotted to the procedure part to be made zero

DBMS LABORATORY WITH MINI PROJECT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – V

Subject Code	15CSL58	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- Foundation knowledge in database concepts, technology and practice to groom students into well-informed database application developers.
- Strong practice in SQL programming through a variety of database problems.
- Develop database applications using front-end tools and back-end DBMS.

Description (If any):

PART-A: SQL Programming (Max. Exam Mks. 50)

- Design, develop, and implement the specified queries for the following problems using Oracle, MySQL, MS SQL Server, or any other DBMS under LINUX/Windows environment.
- Create Schema and insert at least 5 records for each table. Add appropriate database constraints.

PART-B: Mini Project (Max. Exam Mks. 30)

- Use Java, C#, PHP, Python, or any other similar front-end tool. All applications must be demonstrated on desktop/laptop as a stand-alone or web based application (Mobile apps on Android/IOS are not permitted.)

Lab Experiments:

Part A: SQL Programming

1

Consider the following schema for a Library Database:
BOOK(Book_id, Title, Publisher_Name, Pub_Year)
BOOK_AUTHORS(Book_id, Author_Name)
PUBLISHER(Name, Address, Phone)
BOOK_COPIES(Book_id, Branch_id, No-of_Copies)
BOOK_LENDING(Book_id, Branch_id, Card_No, Date_Out, Due_Date)
LIBRARY_BRANCH(Branch_id, Branch_Name, Address)
Write SQL queries to

1. Retrieve details of all books in the library – id, title, name of publisher, authors, number of copies in each branch, etc.
2. Get the particulars of borrowers who have borrowed more than 3 books, but from Jan 2017 to Jun 2017.
3. Delete a book in BOOK table. Update the contents of other tables to reflect this data manipulation operation.
4. Partition the BOOK table based on year of publication. Demonstrate its working with a simple query.
5. Create a view of all books and its number of copies that are currently available in the Library.

2

Consider the following schema for Order Database:
SALESMAN(Salesman_id, Name, City, Commission)
CUSTOMER(Customer_id, Cust_Name, City, Grade, Salesman_id)
ORDERS(Ord_No, Purchase_Amt, Ord_Date, Customer_id, Salesman_id)
Write SQL queries to

1. Count the customers with grades above Bangalore's average.

	2. Find the name and numbers of all salesman who had more than one customer. 3. List all the salesman and indicate those who have and don't have customers in their cities (Use UNION operation.) 4. Create a view that finds the salesman who has the customer with the highest order of a day. 5. Demonstrate the DELETE operation by removing salesman with id 1000. All his orders must also be deleted.
3	Consider the schema for Movie Database: ACTOR(Act_id, Act_Name, Act_Gender) DIRECTOR(Dir_id, Dir_Name, Dir_Phone) MOVIES(Mov_id, Mov_Title, Mov_Year, Mov_Lang, Dir_id) MOVIE_CAST(Act_id, Mov_id, Role) RATING(Mov_id, Rev_Stars) Write SQL queries to 1. List the titles of all movies directed by 'Hitchcock'. 2. Find the movie names where one or more actors acted in two or more movies. 3. List all actors who acted in a movie before 2000 and also in a movie after 2015 (use JOIN operation). 4. Find the title of movies and number of stars for each movie that has at least one rating and find the highest number of stars that movie received. Sort the result by movie title. 5. Update rating of all movies directed by 'Steven Spielberg' to 5.
4	Consider the schema for College Database: STUDENT(USN, SName, Address, Phone, Gender) SEMSEC(SSID, Sem, Sec) CLASS(USN, SSID) SUBJECT(Subcode, Title, Sem, Credits) IAMARKS(USN, Subcode, SSID, Test1, Test2, Test3, FinalIA) Write SQL queries to 1. List all the student details studying in fourth semester 'C' section. 2. Compute the total number of male and female students in each semester and in each section. 3. Create a view of Test1 marks of student USN '1BI15CS101' in all subjects. 4. Calculate the FinalIA (average of best two test marks) and update the corresponding table for all students. 5. Categorize students based on the following criterion: If FinalIA = 17 to 20 then CAT = 'Outstanding' If FinalIA = 12 to 16 then CAT = 'Average' If FinalIA< 12 then CAT = 'Weak' Give these details only for 8th semester A, B, and C section students.
5	Consider the schema for Company Database: EMPLOYEE(SSN, Name, Address, Sex, Salary, SuperSSN, DNo) DEPARTMENT(DNo, DName, MgrSSN, MgrStartDate) DLOCATION(DNo,DLoc) PROJECT(PNo, PName, PLocation, DNo) WORKS_ON(SSN, PNo, Hours) Write SQL queries to 1. Make a list of all project numbers for projects that involve an employee whose last name is 'Scott', either as a worker or as a manager of the department that controls the project.

2. Show the resulting salaries if every employee working on the 'IoT' project is given a 10 percent raise.
3. Find the sum of the salaries of all employees of the 'Accounts' department, as well as the maximum salary, the minimum salary, and the average salary in this department
4. Retrieve the name of each employee who works on all the projects controlledby department number 5 (use NOT EXISTS operator).
5. For each department that has more than five employees, retrieve the department number and the number of its employees who are making more than Rs. 6,00,000.

Part B: Mini project

- For any problem selected, write the ER Diagram, apply ER-mapping rules, normalize the relations, and follow the application development process.
- Make sure that the application should have five or more tables, at least one trigger and one stored procedure, using suitable frontend tool.
- Indicative areas include: health care, education, industry, transport, supply chain, etc.

Course outcomes: The students should be able to:

- Create, Update and query on the database.
- Demonstrate the working of different concepts of DBMS
- Implement, analyze and evaluate the project developed for an application.

Conduction of Practical Examination:

1. All laboratory experiments from part A are to be included for practical examination.
2. Mini project has to be evaluated for 30 Marks.
3. Report should be prepared in a standard format prescribed for project work.
4. Students are allowed to pick one experiment from the lot.
5. Strictly follow the instructions as printed on the cover page of answer script.
6. Marks distribution:
   a) Part A: Procedure + Conduction + Viva:10 + 35 +5 =50 Marks
   b) Part B: Demonstration + Report + Viva voce = 15+10+05 = 30 Marks
7. Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.

CRYPTOGRAPHY, NETWORK SECURITY AND CYBER LAW [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – VI			
Subject Code	15CS61	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to

- Explain the concepts of Cyber security
- Illustrate key management issues and solutions.
- Familiarize with Cryptography and very essential algorithms
- Introduce cyber Law and ethics to be followed.

Module – 1	**Teaching Hours**
Introduction - Cyber Attacks, Defence Strategies and Techniques, Guiding Principles, Mathematical Background for Cryptography - Modulo Arithmetic's, The Greatest Comma Divisor, Useful Algebraic Structures, Chinese Remainder Theorem, Basics of Cryptography - Preliminaries, Elementary Substitution Ciphers, Elementary Transport Ciphers, Other Cipher Properties, Secret Key Cryptography – Product Ciphers, DES Construction.	**10 Hours**
Module – 2	
Public Key Cryptography and RSA – RSA Operations, Why Does RSA Work?, Performance, Applications, Practical Issues, Public Key Cryptography Standard (PKCS), Cryptographic Hash - Introduction, Properties, Construction, Applications and Performance, The Birthday Attack, Discrete Logarithm and its Applications - Introduction, Diffie-Hellman Key Exchange, Other Applications.	**10 Hours**
Module – 3	
Key Management - Introduction, Digital Certificates, Public Key Infrastructure, Identity–based Encryption, Authentication–I - One way Authentication, Mutual Authentication, Dictionary Attacks, Authentication – II – Centalised Authentication, The Needham-Schroeder Protocol, Kerberos, Biometrics, IPSec-Security at the Network Layer – Security at Different layers: Pros and Cons, IPSec in Action, Internet Key Exchange (IKE) Protocol, Security Policy and IPSEC, Virtual Private Networks, Security at the Transport Layer - Introduction, SSL Handshake Protocol, SSL Record Layer Protocol, OpenSSL.	**10 Hours**
Module – 4	
IEEE 802.11 Wireless LAN Security - Background, Authentication, Confidentiality and Integrity, Viruses, Worms, and Other Malware, Firewalls – Basics, Practical Issues, Intrusion Prevention and Detection - Introduction, Prevention Versus Detection, Types of Instruction Detection Systems, DDoS Attacks Prevention/Detection, Web Service Security – Motivation, Technologies for Web Services, WS- Security, SAML, Other Standards.	**10 Hours**
Module – 5	
IT act aim and objectives, Scope of the act, Major Concepts, Important provisions, Attribution, acknowledgement, and dispatch of electronic records, Secure electronic records and secure digital signatures, Regulation of certifying authorities: Appointment of Controller and Other officers, Digital Signature certificates, Duties of Subscribers, Penalties and adjudication, The cyber	**10 Hours**

regulations appellate tribunal, Offences, Network service providers not to be liable in certain cases, Miscellaneous Provisions.	

Course outcomes: The students should be able to:

- Discuss cryptography and its need to various applications
- Design and develop simple cryptography algorithms
- Understand cyber security and need cyber Law

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Cryptography, Network Security and Cyber Laws – Bernard Menezes, Cengage Learning, 2010 edition (Chapters-1,3,4,5,6,7,8,9,10,11,12,13,14,15,19(19.1-19.5),21(21.1-21.2),22(22.1-22.4),25

Reference Books:

1. Cryptography and Network Security- Behrouz A Forouzan, Debdeep Mukhopadhyay, Mc-GrawHill, 3rd Edition, 2015
2. Cryptography and Network Security- William Stallings, Pearson Education, 7th Edition
3. Cyber Law simplified- Vivek Sood, Mc-GrawHill, 11th reprint , 2013
4. Cyber security and Cyber Laws, Alfred Basta, Nadine Basta, Mary brown, ravindra kumar, Cengage learning

DATA MINING AND DATA WAREHOUSING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VI

Subject Code	15CS651	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Define multi-dimensional data models.
- Explain rules related to association, classification and clustering analysis.
- Compare and contrast between different classification and clustering algorithms

Module – 1	**Teaching Hours**
Data Warehousing & modeling: Basic Concepts: Data Warehousing: A multitier Architecture, Data warehouse models: Enterprise warehouse, Data mart and virtual warehouse, Extraction, Transformation and loading, Data Cube: A multidimensional data model, Stars, Snowflakes and Fact constellations: Schemas for multidimensional Data models, Dimensions: The role of concept Hierarchies, Measures: Their Categorization and computation, Typical OLAP Operations.	8 Hours
Module – 2	
Data warehouse implementation& Data mining: Efficient Data Cube computation: An overview, Indexing OLAP Data: Bitmap index and join index, Efficient processing of OLAP Queries, OLAP server Architecture ROLAP versus MOLAP Versus HOLAP. : Introduction: What is data mining, Challenges, Data Mining Tasks, Data: Types of Data, Data Quality, Data Preprocessing, Measures of Similarity and Dissimilarity,	8 Hours
Module – 3	
Association Analysis: Association Analysis: Problem Definition, Frequent Item set Generation, Rule generation, Alternative Methods for Generating Frequent Item sets, FP-Growth Algorithm, Evaluation of Association Patterns.	8 Hours
Module – 4	
Classification : Decision Trees Induction, Method for Comparing Classifiers, Rule Based Classifiers, Nearest Neighbor Classifiers, Bayesian Classifiers.	8 Hours
Module – 5	
Clustering Analysis: Overview, K-Means, Agglomerative Hierarchical Clustering, DBSCAN, Cluster Evaluation, Density-Based Clustering, Graph-Based Clustering, Scalable Clustering Algorithms.	8 Hours

Course outcomes: The students should be able to:

- Identify data mining problems and implement the data warehouse
- Write association rules for a given data pattern.
- Choose between classification and clustering solution.

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.

The students will have to answer FIVE full questions, selecting ONE full question from each module.
Text Books:
1. Pang-Ning Tan, Michael Steinbach, Vipin Kumar: Introduction to Data Mining, Pearson, First impression,2014. 2. Jiawei Han, Micheline Kamber, Jian Pei: Data Mining -Concepts and Techniques, 3rd Edition, Morgan Kaufmann Publisher, 2012.
Reference Books:
1. Sam Anahory, Dennis Murray: Data Warehousing in the Real World, Pearson,Tenth Impression,2012. 2. Michael J.Berry,Gordon.S Linoff: Mastering Data Mining , Wiley Edition, second edition,2012.

WEB TECHNOLOGY AND ITS APPLICATIONS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CS71	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course Objectives: This course will enable students to

- Illustrate the Semantic Structure of HTML and CSS
- Compose forms and tables using HTML and CSS
- Design Client-Side programs using JavaScript and Server-Side programs using PHP
- Infer Object Oriented Programming capabilities of PHP
- Examine JavaScript frameworks such as jQuery and Backbone

Module – 1	**Teaching Hours**
Introduction to HTML, What is HTML and Where did it come from?, HTML Syntax, Semantic Markup, Structure of HTML Documents, Quick Tour of HTML Elements, HTML5 Semantic Structure Elements, Introduction to CSS, What is CSS, CSS Syntax, Location of Styles, Selectors, The Cascade: How Styles Interact, The Box Model, CSS Text Styling.	**10 Hours**
Module – 2	
HTML Tables and Forms, Introducing Tables, Styling Tables, Introducing Forms, Form Control Elements, Table and Form Accessibility, Microformats, Advanced CSS: Layout, Normal Flow, Positioning Elements, Floating Elements, Constructing Multicolumn Layouts, Approaches to CSS Layout, Responsive Design, CSS Frameworks.	**10 Hours**
Module – 3	
JavaScript: Client-Side Scripting, What is JavaScript and What can it do?, JavaScript Design Principles, Where does JavaScript Go?, Syntax, JavaScript Objects, The Document Object Model (DOM), JavaScript Events, Forms, Introduction to Server-Side Development with PHP, What is Server-Side Development, A Web Server's Responsibilities, Quick Tour of PHP, Program Control, Functions	**10 Hours**
Module – 4	
PHP Arrays and Superglobals, Arrays, $_GET and $_POST Superglobal Arrays, $_SERVER Array, $_Files Array, Reading/Writing Files, PHP Classes and Objects, Object-Oriented Overview, Classes and Objects in PHP, Object Oriented Design, Error Handling and Validation, What are Errors and Exceptions?, PHP Error Reporting, PHP Error and Exception Handling	**10 Hours**
Module – 5	
Managing State, The Problem of State in Web Applications, Passing Information via Query Strings, Passing Information via the URL Path, Cookies, Serialization, Session State, HTML5 Web Storage, Caching, Advanced JavaScript and jQuery, JavaScript Pseudo-Classes, jQuery Foundations, AJAX, Asynchronous File Transmission, Animation, Backbone MVC Frameworks, XML Processing and Web Services, XML Processing, JSON, Overview of Web Services.	**10 Hours**

Course Outcomes:After studying this course, students will be able to

- Adapt HTML and CSS syntax and semantics to build web pages.

- Construct and visually format tables and forms using HTML and CSS.
- Develop Client-Side Scripts using JavaScript and Server-Side Scripts using PHP to generate and display the contents dynamically.
- Appraise the principles of object oriented development using PHP
- Inspect JavaScript frameworks like jQuery and Backbone which facilitates developer to focus on core features.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Randy Connolly, Ricardo Hoar, **"Fundamentals of Web Development"**, 1st Edition, Pearson Education India. (**ISBN**:978-9332575271)

Reference Books:

1) Robin Nixon, **"Learning PHP, MySQL &JavaScript with jQuery, CSS and HTML5"**, 4th Edition, O'Reilly Publications, 2015. (**ISBN**:978-9352130153)
2) Luke Welling, Laura Thomson, **"PHP and MySQL Web Development"**, 5th Edition, Pearson Education, 2016. (**ISBN**:978-93332582736)
3) Nicholas C Zakas, **"Professional JavaScript for Web Developers"**, 3rd Edition, Wrox/Wiley India, 2012. (**ISBN**:978-8126535088)
4) David Sawyer Mcfarland, **"JavaScript & jQuery: The Missing Manual"**, 1st Edition, O'Reilly/Shroff Publishers & Distributors Pvt Ltd, 2014 (**ISBN**:978-9351108078)
5) Zak Ruvalcaba Anne Boehm, **"Murach's HTML5 and CSS3"**, 3rd Edition, Murachs/Shroff Publishers & Distributors Pvt Ltd, 2016. (**ISBN**:978-9352133246)

MACHINE LEARNING

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CS73	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course Objectives: This course will enable students to

- Define machine learning and problems relevant to machine learning.
- Differentiate supervised, unsupervised and reinforcement learning
- Apply neural networks, Bayes classifier and k nearest neighbor, for problems appear in machine learning.
- Perform statistical analysis of machine learning techniques.

Module – 1	**Teaching Hours**
Introduction: Well posed learning problems, Designing a Learning system, Perspective and Issues in Machine Learning. **Concept Learning:** Concept learning task, Concept learning as search, Find-S algorithm, Version space, Candidate Elimination algorithm, Inductive Bias. **Text Book1, Sections: 1.1 – 1.3, 2.1-2.5, 2.7**	**10 Hours**
Module – 2	
Decision Tree Learning: Decision tree representation, Appropriate problems for decision tree learning, Basic decision tree learning algorithm, hypothesis space search in decision tree learning, Inductive bias in decision tree learning, Issues in decision tree learning. **Text Book1, Sections: 3.1-3.7**	**10 Hours**
Module – 3	
Artificial Neural Networks: Introduction, Neural Network representation, Appropriate problems, Perceptrons, Backpropagation algorithm. **Text book 1, Sections: 4.1 – 4.6**	**08 Hours**
Module – 4	
Bayesian Learning: Introduction, Bayes theorem, Bayes theorem and concept learning, ML and LS error hypothesis, ML for predicting probabilities, MDL principle, Naive Bayes classifier, Bayesian belief networks, EM algorithm. **Text book 1, Sections: 6.1 – 6.6, 6.9, 6.11, 6.12**	**10 Hours**
Module – 5	
Evaluating Hypothesis: Motivation, Estimating hypothesis accuracy, Basics of sampling theorem, General approach for deriving confidence intervals, Difference in error of two hypothesis, Comparing learning algorithms. **Instance Based Learning:** Introduction, k-nearest neighbor learning, locally weighted regression, radial basis function, cased-based reasoning. **Reinforcement Learning:** Introduction, Learning Task, Q Learning **Text book 1, Sections: 5.1-5.6, 8.1-8.5, 13.1-13.3**	**12 Hours**

Course Outcomes: After studying this course, students will be able to

- Identify the problems for machine learning. And select the either supervised,

unsupervised or reinforcement learning. • Explain theory of probability and statistics related to machine learning • Investigate concept learning, ANN, Bayes classifier, k nearest neighbor, Q.
Question paper pattern:
The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.
Text Books:
1. Tom M. Mitchell, Machine Learning, India Edition 2013, McGraw Hill Education.
Reference Books:
1. Trevor Hastie, Robert Tibshirani, Jerome Friedman, h The Elements of Statistical Learning, 2nd edition, springer series in statistics. 2. Ethem Alpaydin, Introduction to machine learning, second edition, MIT press.

NATURAL LANGUAGE PROCESSING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CS741	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Learn the techniques in natural language processing.
- Be familiar with the natural language generation.
- Be exposed to Text Mining.
- Understand the information retrieval techniques

Module – 1	**Teaching Hours**
Overview and language modeling: Overview: Origins and challenges of NLP-Language and Grammar-Processing Indian Languages- NLP Applications-Information Retrieval. Language Modeling: Various Grammar- based Language Models-Statistical Language Model.	**8 Hours**
Module – 2	
Word level and syntactic analysis: Word Level Analysis: Regular Expressions-Finite-State Automata-Morphological Parsing-Spelling Error Detection and correction-Words and Word classes-Part-of Speech Tagging. Syntactic Analysis: Context-free Grammar-Constituency- Parsing-Probabilistic Parsing.	**8 Hours**
Module – 3	
Extracting Relations from Text: From Word Sequences to Dependency Paths: Introduction, Subsequence Kernels for Relation Extraction, A Dependency-Path Kernel for Relation Extraction and Experimental Evaluation. **Mining Diagnostic Text Reports by Learning to Annotate Knowledge Roles:** Introduction, Domain Knowledge and Knowledge Roles, Frame Semantics and Semantic Role Labeling, Learning to Annotate Cases with Knowledge Roles and Evaluations. **A Case Study in Natural Language Based Web Search:** InFact System Overview, The GlobalSecurity.org Experience.	**8 Hours**
Module – 4	
Evaluating Self-Explanations in iSTART: Word Matching, Latent Semantic Analysis, and Topic Models: Introduction, iSTART: Feedback Systems, iSTART: Evaluation of Feedback Systems. **Textual Signatures: Identifying Text-Types Using Latent Semantic Analysis to Measure the Cohesion of Text Structures:** Introduction, Cohesion, Coh-Metrix, Approaches to Analyzing Texts, Latent Semantic Analysis, Predictions, Results of Experiments. **Automatic Document Separation: A Combination of Probabilistic Classification and Finite-State Sequence Modeling:** Introduction, Related Work, Data Preparation, Document Separation as a Sequence Mapping Problem, Results. **Evolving Explanatory Novel Patterns for Semantically-Based Text Mining:** Related Work, A Semantically Guided Model for Effective Text Mining.	**8 Hours**

Module – 5	
INFORMATION RETRIEVAL AND LEXICAL RESOURCES: Information Retrieval: Design features of Information Retrieval Systems-Classical, Non classical, Alternative Models of Information Retrieval – valuation Lexical Resources: World Net-Frame Net- Stemmers-POS Tagger- Research Corpora.	**8 Hours**

Course outcomes: The students should be able to:

- Analyze the natural language text.
- Generate the natural language.
- Do Text mining
- Apply information retrieval techniques.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Tanveer Siddiqui, U.S. Tiwary, "Natural Language Processing and Information Retrieval", Oxford University Press, 2008.
2. Anne Kao and Stephen R. Poteet (Eds), "Natural LanguageProcessing and Text Mining", Springer-Verlag London Limited 2007.

Reference Books:

1. Daniel Jurafsky and James H Martin, "Speech and Language Processing: Anintroduction to Natural Language Processing, Computational Linguistics and SpeechRecognition", 2nd Edition, Prentice Hall, 2008.
2. James Allen, "Natural Language Understanding", 2nd edition, Benjamin/Cummingspublishing company, 1995.
3. Gerald J. Kowalski and Mark.T. Maybury, "Information Storage and Retrieval systems", Kluwer academic Publishers, 2000.

INFORMATION AND NETWORK SECURITY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CS743	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Analyze the cryptographic processes.
- Summarize the digital security process.
- Indicate the location of a security process in the given system

Module – 1	**Teaching Hours**
Introduction. How to Speak Crypto. Classic Crypto. Simple Substitution Cipher. Cryptanalysis of a Simple Substitution. Definition of Secure. Double Transposition Cipher. One-time Pad. Project VENONA. Codebook Cipher. Ciphers of the Election of 1876. Modern Crypto History. Taxonomy of Cryptography. Taxonomy of Cryptanalysis.	**8 Hours**
Module – 2.	
What is a Hash Function? The Birthday Problem Non-cryptographic Hashes. Tiger Hash. HMAC. Uses of Hash Functions. Online Bids. Spam Reduction. Other Crypto-Related Topics. Secret Sharing. Key Escrow. Random Numbers. Texas Hold 'em Poker. Generating Random Bits. Information Hiding.	**8 Hours**
Module – 3	
Random number generation Providing freshness Fundamentals of entity authentication Passwords Dynamic password schemes Zero-knowledge mechanisms Further reading Cryptographic Protocols Protocol basics From objectives to a protocol Analysing a simple protocol Authentication and key establishment protocols	**8 Hours**
Module – 4	
Key management fundamentals Key lengths and lifetimes Key generation Key establishment Key storage Key usage Governing key management Public-Key Management Certification of public keys The certificate lifecycle Public-key management models Alternative approaches	**8 Hours**
Module – 5	
Cryptographic Applications Cryptography on the Internet Cryptography for wireless local area networks Cryptography for mobile telecommunications Cryptography for secure payment card transactions Cryptography for video broadcasting Cryptography for identity cards Cryptography for home users	**8 Hours**

Course outcomes: The students should be able to:

- Analyze the Digitals security lapses
- Illustrate the need of key management

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

COMPUTER VISION AND ROBOTICS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CS752	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Review image processing techniques for computer vision
- Explain shape and region analysis
- Illustrate Hough Transform and its applications to detect lines, circles, ellipses
- Contrast three-dimensional image analysis techniques, motion analysis and applications of computer vision algorithms

Module – 1	**Teaching Hours**
CAMERAS: Pinhole Cameras, **Radiometry – Measuring Light**: Light in Space, Light Surfaces, Important Special Cases, **Sources, Shadows, And Shading**: Qualitative Radiometry, Sources and Their Effects, Local Shading Models, Application: Photometric Stereo, Interreflections: Global Shading Models, **Color**: The Physics of Color, Human Color Perception, Representing Color, A Model for Image Color, Surface Color from Image Color.	**8 Hours**
Module – 2	
Linear Filters: Linear Filters and Convolution, Shift Invariant Linear Systems, Spatial Frequency and Fourier Transforms, Sampling and Aliasing, Filters as Templates, **Edge Detection**: Noise, Estimating Derivatives, Detecting Edges, **Texture**: Representing Texture, Analysis (and Synthesis) Using Oriented Pyramids, Application: Synthesis by Sampling Local Models, Shape from Texture.	**8 Hours**
Module – 3	
The Geometry of Multiple Views: Two Views, **Stereopsis**: Reconstruction, Human Stereopsis, Binocular Fusion, Using More Cameras, **Segmentation by Clustering**: What Is Segmentation?, Human Vision: Grouping and Getstalt, Applications: Shot Boundary Detection and Background Subtraction, Image Segmentation by Clustering Pixels, Segmentation by Graph-Theoretic Clustering,	**8 Hours**
Module – 4	
Segmentation by Fitting a Model: The Hough Transform, Fitting Lines, Fitting Curves, Fitting as a Probabilistic Inference Problem, Robustness, **Segmentation and Fitting Using Probabilistic Methods**: Missing Data Problems, Fitting, and Segmentation, The EM Algorithm in Practice, **Tracking With Linear Dynamic Models**: Tracking as an Abstract Inference Problem, Linear Dynamic Models, Kalman Filtering, Data Association, Applications and Examples.	**8 Hours**
Module – 5	
Geometric Camera Models: Elements of Analytical Euclidean Geometry, Camera Parameters and the Perspective Projection, Affine Cameras and Affine Projection Equations, **Geometric Camera Calibration**: Least-Squares Parameter Estimation, A Linear Approach to Camera Calibration, Taking Radial Distortion into Account, Analytical Photogrammetry, An Application: Mobile Robot Localization, **Model- Based Vision**: Initial Assumptions, Obtaining	**8 Hours**

Hypotheses by Pose Consistency, Obtaining Hypotheses by pose Clustering, Obtaining Hypotheses Using Invariants, Verification, Application: Registration In Medical Imaging Systems, Curved Surfaces and Alignment.	

Course outcomes: The students should be able to:

- Implement fundamental image processing techniques required for computer vision
- Perform shape analysis
- Implement boundary tracking techniques
- Apply chain codes and other region descriptors
- Apply Hough Transform for line, circle, and ellipse detections.
- Apply 3D vision techniques.
- Implement motion related techniques.
- Develop applications using computer vision techniques.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. David A. Forsyth and Jean Ponce: Computer Vision – A Modern Approach, PHI Learning (Indian Edition), 2009.

Reference Books:

2. E. R. Davies: Computer and Machine Vision – Theory, Algorithms and Practicalities, Elsevier (Academic Press), 4th edition, 2013.

DIGITAL IMAGE PROCESSING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CS753	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Define the fundamental concepts in image processing
- Evaluate techniques followed in image enhancements
- Illustrate image segmentation and compression algorithms

Module – 1	**Teaching Hours**
Introduction Fundamental Steps in Digital Image Processing, Components of an Image Processing System, Sampling and Quantization, Representing Digital Images (Data structure), Some Basic Relationships Between Pixels- Neighbors and Connectivity of pixels in image, Applications of Image Processing: Medical imaging, Robot vision, Character recognition, Remote Sensing.	8 Hours
Module – 2	
Image Enhancement In The Spatial Domain: Some Basic Gray Level Transformations, Histogram Processing, Enhancement Using Arithmetic/Logic Operations, Basics of Spatial Filtering, Smoothing Spatial Filters, Sharpening Spatial Filters, Combining Spatial Enhancement Methods.	8 Hours
Module – 3	
Image Enhancement In Frequency Domain: Introduction, Fourier Transform, Discrete Fourier Transform (DFT), properties of DFT , Discrete Cosine Transform (DCT), Image filtering in frequency domain.	8 Hours
Module – 4	
Image Segmentation: Introduction, Detection of isolated points, line detection, Edge detection, Edge linking, Region based segmentation- Region growing, split and merge technique, local processing, regional processing, Hough transform, Segmentation using Threshold.	8 Hours
Module – 5	
Image Compression: Introduction, coding Redundancy , Inter-pixel redundancy, image compression model, Lossy and Lossless compression, Huffman Coding, Arithmetic Coding, LZW coding, Transform Coding, Sub-image size selection, blocking, DCT implementation using FFT, Run length coding.	8 Hours

Course outcomes: The students should be able to:

- Explain fundamentals of image processing
- Compare transformation algorithms
- Contrast enhancement, segmentation and compression techniques

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Rafael C G., Woods R E. and Eddins S L, Digital Image Processing, Prentice Hall, 3rd edition, 2008.

Reference Books:

1. Milan Sonka, "Image Processing, analysis and Machine Vision", Thomson Press India Ltd, Fourth Edition.
2. Fundamentals of Digital Image Processing- Anil K. Jain, 2nd Edition, Prentice Hall of India.
3. S. Sridhar , Digital Image Processing, Oxford University Press, 2nd Ed, 2016.

STORAGE AREA NETWORKS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CS754	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Evaluate storage architectures,
- Define backup, recovery, disaster recovery, business continuity, and replication
- Examine emerging technologies including IP-SAN
- Understand logical and physical components of a storage infrastructure
- Identify components of managing and monitoring the data center
- Define information security and identify different storage virtualization technologies

Module – 1	**Teaching Hours**
Storage System Introduction to Information Storage: Evolution of Storage Architecture, Data Center Infrastructure, Virtualization and Cloud Computing. Data Center Environment: Application, Host (Compute), Connectivity, Storage. Data Protection: RAID: RAID Implementation Methods, RAID Techniques, RAID Levels, RAID Impact on Disk Performance. Intelligent Storage Systems: Components of Intelligent Storage System, Storage Provisioning. **Text Book-1 Ch1: 1.2 to 1.4, Ch2: 2.1, 2.3 to 2.5, Ch3: 3.1, 3.3 to 3.5, Ch4: 4.1 and 4.2**	**8 Hours**
Module – 2	
Storage Networking Technologies Fibre Channel Storage Area Networks: Components of FC SAN, FC connectivity, Fibre Channel Architecture, Zoning, FC SAN Topologies, Virtualization in SAN. IP SAN and FCoE: iSCSI, FCIP, FCoE. Network Attached Storage: Components of NAS, NAS I/O Operation, NAS File-Sharing Protocols, File-Level Virtualization, Object-Based Storage and Unified Storage: Object-Based Storage Devices, Content-Addressed Storage, Unified Storage. **Text Book-1 Ch5: 5.3, 5.4, 5.6, 5.9 to 5.11, Ch6: 6.1 to 6.3, Ch7: 7.4, 7.5, 7.7 and 7.9 Ch8: 8.1, 8.2 and 8.4**	**8 Hours**
Module – 3	
Backup, Archive and Replication Introduction to Business Continuity: Information Availability, BC Terminology, BC Planning Lifecycle, Failure Analysis, BC Technology Solutions. Backup and Archive: Backup Methods, Backup Topologies, Backup Targets, Data Deduplication for Backup, Backup in Virtualized Environments, Data Archive. Local Replication: Replication Terminology, Uses of Local Replicas, Local Replication Technologies, Local Replication in a Virtualized Environment. Remote Replication: Remote Replication Technologies, Three-Site Replication, Remote Replication and	**8 Hours**

Migration in a Virtualized Environment. **Text Book-1 Ch10: 10.5, 10.8, 10.10 to 10.13, Ch11: 11.1, 11.2, 11.4 and 11.8, Ch12: 12.2, 12.3 and 12.5**	
Module – 4	
Cloud Computing and Virtualization Cloud Enabling Technologies, Characteristics of Cloud Computing, Benefits of Cloud Computing, Cloud Service Models, Cloud Deployment Models, Cloud Computing Infrastructure, Cloud Challenges and Cloud Adoption Considerations. Virtualization Appliances: Black Box Virtualization, In-Band Virtualization Appliances, Out-of-Band Virtualization Appliances, High Availability for Virtualization Appliances, Appliances for Mass Consumption. Storage Automation and Virtualization: Policy-Based Storage Management, Application-Aware Storage Virtualization, Virtualization-Aware Applications. **Text Book-1 Ch13: 13.1 to 13.8. Text Book-2 Ch9: 9.1 to 9.5 Ch13: 13.1 to 13.3**	**8 Hours**
Module – 5	
Securing and Managing Storage Infrastructure Securing and Storage Infrastructure: Information Security Framework, Risk Triad, Storage Security Domains, Security Implementations in Storage Networking, Securing Storage Infrastructure in Virtualized and Cloud Environments. **Managing the Storage Infrastructure** Monitoring the Storage Infrastructure, Storage Infrastructure Management activities, Storage Infrastructure Management Challenges, Information Lifecycle management, Storage Tiering. **Text Book-1 Ch14: 14.1 to 14.5, Ch15: 15.1 to 15.3, 15.5 and 15.6**	**8 Hours**
Course outcomes: The students should be able to:	
• Identify key challenges in managing information and analyze different storage networking technologies and virtualization • Explain components and the implementation of NAS • Describe CAS architecture and types of archives and forms of virtualization • Illustrate the storage infrastructure and management activities	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books:	
1. Information Storage and Management,Author :EMC Education Services, Publisher: Wiley ISBN: 9781118094839 2. Storage Virtualization, Author: Clark Tom, Publisher: Addison Wesley Publishing Company ISBN : 9780321262516	
Reference Books:	
NIL	

MACHINE LEARNING LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CSL76	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

1. Make use of Data sets in implementing the machine learning algorithms
2. Implement the machine learning concepts and algorithms in any suitable language of choice.

Description (If any):

1. The programs can be implemented in either JAVA or Python.
2. For Problems 1 to 6 and 10, programs are to be developed without using the built-in classes or APIs of Java/Python.
3. Data sets can be taken from standard repositories (https://archive.ics.uci.edu/ml/datasets.html) or constructed by the students.

Lab Experiments:

1. Implement and demonstratethe **FIND-Salgorithm** for finding the most specific hypothesis based on a given set of training data samples. Read the training data from a .CSV file.
2. For a given set of training data examples stored in a .CSV file, implement and demonstrate the **Candidate-Elimination algorithm**to output a description of the set of all hypotheses consistent with the training examples.
3. Write a program to demonstrate the working of the decision tree based **ID3 algorithm**. Use an appropriate data set for building the decision tree and apply this knowledge toclassify a new sample.
4. Build an Artificial Neural Network by implementing the **Backpropagation algorithm** and test the same using appropriate data sets.
5. Write a program to implement the **naïve Bayesian classifier** for a sample training data set stored as a .CSV file. Compute the accuracy of the classifier, considering few test data sets.
6. Assuming a set of documents that need to be classified, use the **naïve Bayesian Classifier** model to perform this task. Built-in Java classes/API can be used to write the program. Calculate the accuracy, precision, and recall for your data set.
7. Write a program to construct a**Bayesian network** considering medical data. Use this model to demonstrate the diagnosis of heart patients using standard Heart Disease Data Set. You can use Java/Python ML library classes/API.
8. Apply **EM algorithm** to cluster a set of data stored in a .CSV file. Use the same data set for clustering using ***k*-Means algorithm**. Compare the results of these two algorithms and comment on the quality of clustering. You can add Java/Python ML library classes/API in the program.
9. Write a program to implement ***k*-Nearest Neighbour algorithm** to classify the iris data set. Print both correct and wrong predictions. Java/Python ML library classes can be used for this problem.
10. Implement the non-parametric **Locally Weighted Regressionalgorithm** in order to fit data points. Select appropriate data set for your experiment and draw graphs.

Study Experiment / Project:
NIL
Course outcomes: The students should be able to:
1. Understand the implementation procedures for the machine learning algorithms. 2. Design Java/Python programs for various Learning algorithms. 3. Applyappropriate data sets to the Machine Learning algorithms. 4. Identify and apply Machine Learning algorithms to solve real world problems.
Conduction of Practical Examination:
• All laboratory experiments are to be included for practical examination. • Students are allowed to pick one experiment from the lot. • Strictly follow the instructions as printed on the cover page of answer script • Marks distribution: Procedure + Conduction + Viva: 20 + 50 +10 (80) **Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.**

WEB TECHNOLOGY LABORATORY WITH MINI PROJECT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VII

Subject Code	15CSL77	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

1. Design and develop static and dynamic web pages.
2. Familiarize with Client-Side Programming, Server-Side Programming, Active server Pages.
3. Learn Database Connectivity to web applications.

Description (If any):

NIL

Lab Experiments:

PART A

1. Write a JavaScript to design a simple calculator to perform the following operations: sum, product, difference and quotient.
2. Write a JavaScript that calculates the squares and cubes of the numbers from 0 to 10 and outputs HTML text that displays the resulting values in an HTML table format.
3. Write a JavaScript code that displays text "TEXT-GROWING" with increasing font size in the interval of 100ms in RED COLOR, when the font size reaches 50pt it displays "TEXT-SHRINKING" in BLUE color. Then the font size decreases to 5pt.
4. Develop and demonstrate a HTML5 file that includes JavaScript script that uses functions for the following problems:
   a. Parameter: A string
   b. Output: The position in the string of the left-most vowel
   c. Parameter: A number
   d. Output: The number with its digits in the reverse order
5. Design an XML document to store information about a student in an engineering college affiliated to VTU. The information must include USN, Name, and Name of the College, Branch, Year of Joining, and email id. Make up sample data for 3 students. Create a CSS style sheet and use it to display the document.
6. Write a PHP program to keep track of the number of visitors visiting the web page and to display this count of visitors, with proper headings.
7. Write a PHP program to display a digital clock which displays the current time of the server.
8. Write the PHP programs to do the following:
   a. Implement simple calculator operations.
   b. Find the transpose of a matrix.
   c. Multiplication of two matrices.
   d. Addition of two matrices.

9. Write a PHP program named states.py that declares a variable states with value "Mississippi Alabama Texas Massachusetts Kansas". write a PHP program that does the following:
    a. Search for a word in variable states that ends in xas. Store this word in element 0 of a list named statesList.
    b. Search for a word in states that begins with k and ends in s. Perform a case-insensitive comparison. [Note: Passing re.Ias a second parameter to method compile performs a case-insensitive comparison.] Store this word in element1 of statesList.
    c. Search for a word in states that begins with M and ends in s. Store this word in element 2 of the list.
    d. Search for a word in states that ends in a. Store this word in element 3 of the list.
10. Write a PHP program to sort the student records which are stored in the database using selection sort.

Study Experiment / Project:

Develop a web application project using the languages and concepts learnt in the theory and exercises listed in part A with a good look and feel effects. You can use any web technologies and frameworks and databases.

Note:

1. In the examination each student picks one question from part A.

2. A team of two or three students must develop the mini project. However during the examination, each student must demonstrate the project individually.

3. The team must submit a brief project report (15-20 pages) that must include the following
    a. Introduction
    b. Requirement Analysis
    c. Software Requirement Specification
    d. Analysis and Design
    e. Implementation
    f. Testing

Course outcomes: The students should be able to:

- Design and develop dynamic web pages with good aesthetic sense of designing and latest technical know-how's.
- Have a good understanding of Web Application Terminologies, Internet Tools other web services.
- Learn how to link and publish web sites

Conduction of Practical Examination:

1. All laboratory experiments from part A are to be included for practical examination.

INTERNET OF THINGS TECHNOLOGY [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – VIII			
Subject Code	15CS81	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course Objectives: This course will enable students to

- Assess the genesis and impact of IoT applications, architectures in real world.
- Illustrate diverse methods of deploying smart objects and connect them to network.
- Compare different Application protocols for IoT.
- Infer the role of Data Analytics and Security in IoT.
- Identifysensor technologies for sensing real world entities and understand the role of IoT in various domains of Industry.

Module – 1	**Teaching Hours**
What is IoT, Genesis of IoT, IoT and Digitization, IoT Impact, Convergence of IT and IoT, IoT Challenges, IoT Network Architecture and Design, Drivers Behind New Network Architectures, Comparing IoT Architectures, A Simplified IoT Architecture, The Core IoT Functional Stack, IoT Data Management and Compute Stack.	10 Hours
Module – 2	
Smart Objects: The "Things" in IoT, Sensors, Actuat ors, and Smart Objects, Sensor Networks, Connecting Smart Objects, Communications Criteria, IoT Access Technologies.	10 Hours
Module – 3	
IP as the IoT Network Layer, The Business Case for IP, The need for Optimization, Optimizing IP for IoT, Profiles and Compliances, Application Protocols for IoT, The Transport Layer, IoT Application Transport Methods.	10 Hours
Module – 4	
Data and Analytics for IoT, An Introduction to Data Analytics for IoT, Machine Learning, Big Data Analytics Tools and Technology, Edge Streaming Analytics, Network Analytics, Securing IoT, A Brief History of OT Security, Common Challenges in OT Security, How IT and OT Security Practices and Systems Vary, Formal Risk Analysis Structures: OCTAVE and FAIR, The Phased Application of Security in an Operational Environment	10 Hours
Module – 5	
IoT Physical Devices and Endpoints - Arduino UNO: Introduction to Arduino, Arduino UNO, Installing the Software, Fundamentals of Arduino Programming. IoT Physical Devices and Endpoints - RaspberryPi: Introduction to RaspberryPi, About the RaspberryPi Board: Hardware Layout, Operating Systems on RaspberryPi, Configuring RaspberryPi, Programming RaspberryPi with Python, Wireless Temperature Monitoring System Using Pi, DS18B20 Temperature Sensor, Connecting Raspberry Pi via SSH, Accessing Temperature from DS18B20 sensors, Remote access to RaspberryPi, Smart and Connected Cities, An IoT Strategy for Smarter Cities, Smart City IoT Architecture,	10 Hours

Smart City Security Architecture, Smart City Use-Case Examples.	
Course Outcomes: After studying this course, students will be able to	
• Interpret the impact and challenges posed by IoT networks leading to new architectural models. • Compare and contrast the deployment of smart objects and the technologies to connect them to network. • Appraise the role of IoT protocols for efficient network communication. • Elaborate the need for Data Analytics and Security in IoT. • Illustrate different sensor technologies for sensing real world entities and identify the applications of IoT in Industry.	
Question paper pattern:	
The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books:	
1. David Hanes, Gonzalo Salgueiro, Patrick Grossetete, Robert Barton, Jerome Henry, **"IoT Fundamentals: Networking Technologies, Protocols, and Use Cases for the Internet of Things:"**, 1st Edition, Pearson Education (Cisco Press Indian Reprint). (ISBN: 978-9386873743) 2. Srinivasa K G, **"Internet of Things:"**, CENGAGE Learning India, 2017	
Reference Books:	
1. Vijay Madisetti and Arshdeep Bahga, **"Internet of Things (A Hand: -on-Approach)"**, 1st Edition, VPT, 2014. (ISBN: 978-8173719547) 2. Raj Kamal, **"Internet of Things: Architecture and Design Principles"**, 1st Edition, McGraw Hill Education, 2017. (ISBN: 978-9352605224)	

BIG DATA ANALYTICS

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VIII

Subject Code	15CS82	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Understand Hadoop Distributed File system and examine MapReduce Programming
- Explore Hadoop tools and manage Hadoop with Ambari
- Appraise the role of Business intelligence and its applications across industries
- Assess core data mining techniques for data analytics
- Identify various Text Mining techniques

Module – 1	**Teaching Hours**
Hadoop Distributed File System Basics, Running Example Programs and Benchmarks, Hadoop MapReduce Framework, MapReduce Programming	**10 Hours**
Module – 2	
Essential Hadoop Tools, Hadoop YARN Applications, Managing Hadoop with Apache Ambari, Basic Hadoop Administration Procedures	**10 Hours**
Module – 3	
Business Intelligence Concepts and Application, Data Warehousing, Data Mining, Data Visualization	**10 Hours**
Module – 4	
Decision Trees, Regression, Artificial Neural Networks, Cluster Analysis, Association Rule Mining	**10 Hours**
Module – 5	
Text Mining, Naive-Bayes Analysis, Support Vector Machines, Web Mining, Social Network Analysis	**10 Hours**

Course outcomes: The students should be able to:

- Master the concepts of HDFS and MapReduce framework
- Investigate Hadoop related tools for Big Data Analytics and perform basic Hadoop Administration
- Recognize the role of Business Intelligence, Data warehousing and Visualization in decision making
- Infer the importance of core data mining techniques for data analytics
- Compare and contrast different Text Mining Techniques

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Douglas Eadline, "**Hadoop 2 Quick-Start Guide: Learn the Essentials of Big Data Computing in the Apache Hadoop 2 Ecosystem**", 1st Edition, Pearson Education, 2016. ISBN-13: 978-9332570351

2. Anil Maheshwari, "**Data Analytics**", 1st Edition, McGraw Hill Education, 2017. ISBN-13: 978-9352604180
Reference Books:
1) Tom White, "**Hadoop: The Definitive Guide**", 4 Edition, O'Reilly Media, 2) Boris Lublinsky, Kevin T.Smith, Alexey Yakubovich, "**Professional Hadoop Solutions**", 1st Edition, Wrox Press, 2014ISBN-13: 978-8126551071 3) Eric Sammer, "**Hadoop Operations: A Guide for Developers and Administrators**", 1st Edition, O'Reilly Media, 2012.ISBN-13: 978-9350239261

INTERNSHIP / PROFESSIONAL PRACTISE [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – VIII			
Subject Code	15CS84	IA Marks	50
Duration	4 weeks	Exam Marks	50
		Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

Description (If any):

Course outcomes: The students should be able to:

Evaluation of Internship :

PYTHON APPLICATION PROGRAMMING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VI

Subject Code	15CS664	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Learn Syntax and Semantics and create Functions in Python.
- Handle Strings and Files in Python.
- Understand Lists, Dictionaries and Regular expressions in Python.
- Implement Object Oriented Programming concepts in Python.
- Build Web Services and introduction to Network and Database Programmingin Python.

Module – 1	**Teaching Hours**
Why should you learn to write programs, Variables, expressions and statements, Conditional execution, Functions	**8 Hours**
Module – 2	
Iteration, Strings, Files	**8 Hours**
Module – 3	
Lists, Dictionaries, Tuples, Regular Expressions	**8 Hours**
Module – 4	
Classes and objects, Classes and functions, Classes and methods	**8 Hours**
Module – 5	
Networked programs, Using Web Services, Using databases and SQL	**8 Hours**

Course outcomes: The students should be able to:

- Examine Python syntax and semantics and be fluent in the use of Python flow control and functions.
- Demonstrate proficiency in handling Strings and File Systems.
- Create, run and manipulate Python Programs using core data structures like Lists, Dictionaries and use Regular Expressions.
- Interpret the concepts of Object-Oriented Programming as used in Python.
- Implement exemplary applications related to Network Programming, Web Services and Databases in Python.

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Charles R. Severance, "Python for Everybody: Exploring Data Using Python 3", 1st Edition, CreateSpace Independent Publishing Platform, 2016. (http://do1.dr-chuck.com/pythonlearn/EN_us/pythonlearn.pdf) (Chapters 1 – 13, 15)
2. Allen B. Downey, "Think Python: How to Think Like a Computer Scientist", 2nd Edition, Green Tea Press, 2015.

(http://greenteapress.com/thinkpython2/thinkpython2.pdf) (Chapters 15, 16, 17) (Download pdf files from the above links)

Reference Books:

1. Charles Dierbach, "Introduction to Computer Science Using Python", 1st Edition, Wiley India Pvt Ltd. ISBN-13: 978-8126556014
2. Mark Lutz, "Programming Python", 4th Edition, O'Reilly Media, 2011.ISBN-13: 978-9350232873
3. Wesley J Chun, "Core Python Applications Programming", 3rd Edition,Pearson Education India, 2015. ISBN-13: 978-9332555365
4. Roberto Tamassia, Michael H Goldwasser, Michael T Goodrich, "Data Structures and Algorithms in Python",1stEdition, Wiley India Pvt Ltd, 2016. ISBN-13: 978-8126562176
5. Reema Thareja, "Python Programming using problem solving approach", Oxford university press, 2017

ARTIFICIAL INTELLIGENCE
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – V

Subject Code	15CS562	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Identify the problems where AI is required and the different methods available
- Compare and contrast different AI techniques available.
- Define and explain learning algorithms

Module – 1	**Teaching Hours**
What is artificial intelligence?, Problems, Problem Spaces and search, Heuristic search technique **TextBook1: Ch 1, 2 and 3**	**8 Hours**
Module – 2	
Knowledge Representation Issues, Using Predicate Logic, Representing knowledge using Rules, **TextBoook1: Ch 4, 5 and 6.**	**8 Hours**
Module – 3	
Symbolic Reasoning under Uncertainty, Statistical reasoning, Weak Slot and Filter Structures. **TextBoook1: Ch 7, 8 and 9.**	**8 Hours**
Module – 4	
Strong slot-and-filler structures, Game Playing. **TextBoook1: Ch 10 and 12**	**8 Hours**
Module – 5	
Natural Language Processing, Learning, Expert Systems. **TextBook1: Ch 15,17 and 20**	**8 Hours**

Course outcomes: The students should be able to:

- Identify the AI based problems
- Apply techniques to solve the AI problems
- Define learning and explain various learning techniques
- Discuss on expert systems

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. E. Rich , K. Knight & S. B. Nair - Artificial Intelligence, 3/e, McGraw Hill.

Reference Books:

1. Artificial Intelligence: A Modern Approach, Stuart Rusell, Peter Norving, Pearson Education 2nd Edition.

1. Dan W. Patterson, Introduction to Artificial Intelligence and Expert Systems – Prentice Hal of India.
2. G. Luger, "Artificial Intelligence: Structures and Strategies for complex problem Solving", Fourth Edition, Pearson Education, 2002.
3. Artificial Intelligence and Expert Systems Development by D W Rolston-Mc Graw hill.
4. N.P. Padhy "Artificial Intelligence and Intelligent Systems" , Oxford University Press-2015

ENGINEERING PHYSICS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - I/II

Subject Code	15PHY12/15PHY22	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS - 04

COURSE OBJECTIVES:

The Objective of this course is to make students learn and understand basic concepts and principles of physics to analyze practical engineering problems and apply its solutions effectively and meaningfully. To understand building up of models, design issues, practical oriented skills and problem solving challenges are the great task of the course. To know about shock waves and practical applications is the prime motto to introduce new technology at the initial stage of Engineering.

Module -1	Teaching Hours
Modern Physics and Quantum Mechanics Black body radiation spectrum, Assumptions of quantum theory of radiation, Plank's law, Weins law and Rayleigh Jeans law, for shorter and longer wavelength limits. Wave Particle duality, deBroglie hypothesis. Compton Effect. Matter waves and their Characteristic properties, Definition of Phase velocity and group velocity, Relation between phase velocity and group velocity, Relation between group velocity and particle velocity. Heisenberg's uncertainity principle and its application, (Non-existence of electron in the nucleus).Wave function, Properties and physical significance of wave function, Probability density and Normalization of wave function. Setting up of one dimensional time independent Schrodinger wave equation. Eigen values and Eigen functions. Application of Schrodinger wave equation for a particle in a potential well of infinite depth and for free particle.	10 Hours

Module -2	
Electrical Properties of Materials Free-electron concept (Drift velocity, Thermal velocity, Mean collision time, Mean free path, relaxation time). Failure of classical free electron theory. Quantum free electron theory, Assumptions, Fermi factor, density of states (qualitative only) Fermi-Dirac Statistics. Expression for electrical conductivity based on quantum free electron theory, Merits of quantum free electron theory. Conductivity of Semi conducting materials, Concentration of electrons and holes in intrinsic semiconductors, law of mass action. Temperature dependence of resistivity in metals and superconducting materials. Effect of magnetic field (Meissner effect). Type I and Type II superconductors-Temperature dependence of critical field. BCS theory (qualitative). High temperature superconductors. Applications of superconductors –. Maglav vehicles.	10 Hours
Module – 3	
Lasers and Optical Fibers Einstein's coefficients (expression for energy density). Requisites of a Laser system. Condition for laser action. Principle, Construction and working of CO_2 laser and semiconductor Laser. Applications of Laser – Laser welding, cutting and drilling. Measurement of atmospheric pollutants. Holography-Principle of Recording and reconstruction of images. Propagation mechanism in optical fibers. Angle of acceptance. Numerical aperture. Types of optical fibers and modes of propagation. Attenuation, Block diagram discussion of point to point communication, applications.	10 Hours
Module-4	

Crystal Structure Space lattice, Bravais lattice–Unit cell, primitive cell. Lattice parameters. Crystal systems. Direction and planes in a crystal. Miller indices. Expression for inter – planar spacing. Co-ordination number. Atomic packing factors (SC,FCC,BCC). Bragg's law, Determination of crystal structure using Bragg's X-ray difractometer. Polymorphism and Allotropy. Crystal Structure of Diamond, qualitative discussion of Perovskites.	10 Hours
Module-5	
Shock waves and Science of Nano Materials Definition of Mach number, distinctions between acoustic, ultrasonic, subsonic and supersonic waves. Description of a shock wave and its applications. Basics of conservation of mass, momentum and energy. Normal shock equations (Rankine-Hugonit equations). Method of creating shock waves in the laboratory using a shock tube, description of hand operated Reddy shock tube and its characteristics. Introduction to Nano Science, Density of states in 1D, 2D and 3D structures. Synthesis : Top-down and Bottom-up approach, Ball Milling and Sol-Gel methods. CNT – Properties, synthesis: Arc discharge, Pyrolysis methods, Applications. Scanning Electron microscope: Principle, working and applications.	10 Hours

Course outcomes:

On Completion of this course, students are able to –

- Learn and understand more about basic principles and to develop problem solving skills and implementation in technology.
- Gain Knowledge about Modern physics and quantum mechanics will update the basic concepts to implement the skills.
- Study of material properties and their applications is the prime role to understand and use in engineering applications and studies.
- Study Lasers and Optical fibers and its applications are to import knowledge and to develop skills and to use modern instruments in the engineering applications.
- Understand Crystal structure and applications are to boost the technical skills and its applications.
- Expose shock waves concept and its applications will bring latest technology to the students at the first year level to develop research orientation programs at higher semester level.
- Understand basic concepts of nano science and technology.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions(with a maximum of four sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Wiley precise Text, **Engineering Physics**, Wiley India Private Ltd., New Delhi.
   Book series – 2014,
2. Dr. M.N. Avadhanulu, Dr. P.G.Kshirsagar, **Text Book of Engineering Physics**, S Chand Publishing, New Delhi - 2012

CONSTITUTION OF INDIA, PROFESSIONAL ETHICS & HUMAN RIGHTS

Subject Code	15CPH18/15CPH28	IA Marks	20
Number of Lecture Hours/Week	02	Exam Marks	80
Total Number of Lecture Hours	25	Exam Hours	03
CREDITS - 01			

Course objectives:

1. To provide basic information about Indian constitution.
2. To identify individual role and ethical responsibility towards society.
3. To understand human rights and its implications.

Module 1

Introduction to the Constitution of India, The Making of the Constitution and Salient features of the Constitution. **2 Hours**

Preamble to the Indian Constitution Fundamental Rights & its limitations. **3 Hours**

Module 2

Directive Principles of State Policy & Relevance of Directive Principles State Policy Fundamental Duties. **2 Hours**

Union Executives – President, Prime Minister Parliament Supreme Court of India. **3 Hours**

Module 3

State Executives – Governor Chief Minister, State Legislature High Court of State. **2 Hours**

Electoral Process in India, Amendment Procedures, 42nd, 44th, 74th, 76th, 86th &91st Amendments. **3 Hours**

Module 4

Special Provision for SC & ST Special Provision for Women, Children & Backward Classes Emergency Provisions. Human Rights –Meaning and Definitions, Legislation Specific Themes in Human Rights- Working of National Human Rights Commission in India **3 Hours**

Powers and functions of Municipalities, Panchyats and Co - Operative Societies. **2 Hours**

Module 5

Scope & Aims of Engineering Ethics, Responsibility of Engineers Impediments to Responsibility. **2 Hours**

Risks, Safety and liability of Engineers, Honesty, Integrity & Reliability in Engineering. **3 Hours**

Course outcomes:

After study of the course, the students are able to

- Have general knowledge and legal literacy and thereby to take up competitive examinations
- Understand state and central policies, fundamental duties
- Understand Electoral Process, special provisions
- Understand powers and functions of Municipalities, Panchayats and Co-operative Societies, and
- Understand Engineering ethics and responsibilities of Engineers
- Have an awareness about basic human rights in India

Text Books:

1. Durga Das Basu: **"Introduction to the Constitution on India"**, (Students Edn.) Prentice –Hall EEE, 19th / 20th Edn., 2001
2. Charles E. Haries, Michael S Pritchard and Michael J. Robins **"Engineering Ethics"** Thompson Asia, 2003-08-05.

Reference Books:

1. M.V.Pylee, "An Introduction to Constitution of India", Vikas Publishing, 2002.
2. M.Govindarajan, S.Natarajan, V.S.Senthilkumar, **"Engineering Ethics"**, Prentice –Hall of India Pvt. Ltd. New Delhi, 2004
3. Brij Kishore Sharma,**"Introduction to the Constitution of India"**, PHI Learning Pvt. Ltd., New Delhi, 2011.
4. Latest Publications of Indian Institute of Human Rights, New Delhi.

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PROGRAMMING IN C AND DATA STRUCTURES
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - I/II

Subject Code	15PCD18/28	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS - 04

Course objectives:

The objectives of this course is to make students to learn basic principles of Problem solving, implementing through C programming language and to design & develop programming skills. To gain knowledge of data structures and their applications.

Module -1 : INTRODUCTION TO C LANGUAGE	**Teaching Hours**
Pseudo code solution to problem, Basic concepts in a C program, Declaration, Assignment & Print statements, Data Types, operators and expressions etc, Programming examples and exercise. **Text 1:** Chapter 2, **and** **Text 2:** 1.1, 1.2, 1.3	**10Hours**
Module -2: BRANCHING AND LOOPING	
Two way selection (if, if-else, nested if-else, cascaded if-else), switch statement, ternary operator? Go to, Loops (For, while-do, do-while) in C, break and continue, Programming examples and exercises. **Text 1:** Chapter 3. **&** **Text 2:** 4.4.	**10 Hours**
Module – 3: FUNCTIONS, ARRAYS AND STRINGS	
ARRAYS AND STRINGS: Using an array, Using arrays with Functions, Multi-Dimensional arrays. String: Declaring, Initializing, Printing and reading strings, string manipulation functions, String input and output functions, array of strings, Programming examples and Exercises. **Text 1:** 5.7, & **Text 2:** 7.8, 7.4, chapter 9 **FUNCTIONS:** Functions in C, Argument Passing – call by value, call by reference, Functions and program structure, location of functions, void and parameter less Functions, Recursion, Programming examples and exercises. **Text 1:** 1.7, 1.8, Chapter 4. **Text 2:** 5.1 to 5.4.	**10 Hours**

Module-4: STRUCTURES AND FILE MANAGEMENT	
Basic of structures, structures and Functions, Array of structures, structure Data types, type definition, Defining, opening and closing of files, Input and output operations, Programming examples and exercises. **Text 1:** 6.1 to 6.8. **Text 2**: 10.1 to 10.4, Chapter 11.	**10 Hours**
Module-5: POINTERS AND PREPROCESSORS & Data Structures	
Pointers and address, pointers and functions (call by reference) arguments, pointers and arrays, address arithmetic, character pointer and functions, pointers to pointer ,Initialization of pointer arrays, Dynamic memory allocations methods, Introduction to Preprocessors, compiler control Directives, Programming examples and exercises. **Text 1:** 5.1 to 5.6, 5.8. **Text 2: 12.2**, 12.3, **13.1 to 13.7**. **Introduction to Data Structures**: Primitive and non primitive data types, Abstract data types, Definition and applications of Stacks, Queues, Linked Lists and Trees. **Text 2 : 14.1, 14.2, 14.11, 14.12, 14.13, 14.15, 14.16, 14.17, 15.1.**	**10 Hours**
Course outcomes: On completion of this course, students are able to • Achieve Knowledge of design and development of C problem solving skills. • Understand the basic principles of Programming in C language • Design and develop modular programming skills. • Effective utilization of memory using pointer technology • Understands the basic concepts of pointers and data structures.	
Question paper pattern: • The question paper will have ten questions. • Each full Question consisting of 16 marks • There will be 2 full questions(with a **maximum** of **four** sub questions) from each module. • Each full question will have sub questions covering all the topics under a module. • The students will have to answer 5 full questions, selecting one full question from each module.	

BASIC ELECTRONICS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - I/II

Subject Code	15ELN15 / 15ELN25	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS - 04

Course objectives:
The course objective is to make students of all the branches of Engineering to understand the efficacy of Electronic principles which are pervasive in engineering applications

Module -1	**Teaching Hours**
Semiconductor Diodes and Applications (Text-1): p-n junction diode, Characteristics and Parameters, Diode approximations, DC load line analysis, Half-wave rectifier, Two-diode Full-wave rectifier, Bridge rectifier, Capacitor filter circuit (only qualitative approch), Zener diode voltage regulators: Regulator circuit with no load, Loaded Regulator. Numerical examples as applicable.	**06 Hours**
Bipolar Junction Transistors: BJT operation, BJT Voltages and Currents, BJT amplification, Common Base, Common Emitter and Common Collector Characteristics, Numerical examples as applicable.	**04 Hours**
Module -2	
BJT Biasing (Text-1): DC Load line and Bias Point, Base Bias, Voltage divider Bias, Numerical examples as applicable.	**04 Hours**
Introduction to Operational Amplifiers (Text-2): Ideal OPAMP, Inverting and Non Inverting OPAMP circuits, OPAMP applications: voltage follower, addition, subtraction, integration, differentiation; Numerical examples as applicable.	**06 Hours**

Module – 3	
Digital Electronics (Text-2): Introduction, Switching and Logic Levels, Digital Waveform (Sections 9.1to 9.3). Number Systems: Decimal Number System, Binary Number System, Converting Decimal to Binary, Hexadecimal Number System: Converting Binary to Hexadecimal, Hexadecimal to Binary, Converting Hexadecimal to Decimal, Converting Decimal to Hexadecimal, Octal Numbers: Binary to Octal Conversion. Complement of Binary Numbers. Boolean Algebra Theorems, De Morgan's theorem. Digital Circuits: Logic gates, NOT Gate, AND Gate, OR Gate, XOR Gate, NAND Gate, NOR Gate, X-NOR Gate. Algebraic Simplification, NAND and NOR Implementation (Sections 11.7 and 11.8): NAND Implementation, NOR Implementation. Half adder, Full adder.	**10 Hours**
Module-4	
Flip-Flops (Text-2): Introduction to Flip-Flops (Section 12.1), NAND Gate Latch/ NOR Gate Latch, RS Flip-Flop, Gated Flip-Flops: Clocked RS Flip-Flop (Sections 12.3 to 12.5). **Microcontrollers** (Ref.1): Introduction to Microcontrollers, 8051 Microcontroller Architecture and an example of Microcontroller based stepper motor control system (only Block Diagram approach).	**05 Hours** **05 Hours**
Module-5	
Communication Systems (Text-2): Introduction, Elements of Communication Systems, Modulation: Amplitude Modulation, Spectrum Power, AM Detection (Demodulation), Frequency and Phase Modulation. Amplitude and Frequency Modulation: A comparison. **Transducers** (Text-2): Introduction, Passive Electrical Transducers, Resistive Transducers, Resistance Thermometers, Thermistor. Linear Variable Differential Transformer (LVDT). Active Electrical Transducers, Piezoelectric Transducer, Photoelectric Transducer.	**06 Hours** **04 Hours**

Course outcomes:
After studying this course, students will be able to:

- Appreciate the significance of electronics in different applications,
- Understand the applications of diode in rectifiers, filter circuits and wave shaping,
- Apply the concept of diode in rectifiers, filters circuits
- Design simple circuits like amplifiers (inverting and non inverting), comparators, adders, integrator and differentiator using OPAMPS,
- Compile the different building blocks in digital electronics using logic gates and implement simple logic function using basic universal gates, and
- Understand the functioning of a communication system, and different modulation technologies, and
- Understand the basic principles of different types of Transuducers.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions(with a **maximum** of **four** sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. David A. Bell, **"Electronic Devices and Circuits"**, Oxford University Press, 5th Edition, 2008.
2. D.P. Kothari, I. J. Nagrath, **"Basic Electronics"**, McGraw Hill Education (India) Private Limited, 2014.

Reference Books: MuhammadAli Mazidi, **"The 8051 Microcontroller and Embedded. Systems. Using Assembly and C."** Second Edition, 2011, Pearson India.

COMPUTER PROGRAMMING LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - I/II

Laboratory Code	15CPL16 / 15CPL26	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS - 02

Course objectives: To provide basic principles C programming language. To provide design & develop of C programming skills. To provide practical exposures like designing flowcharts, algorithms, how to debug programs etc.

Descriptions (if any):

Demonstration of Personal Computer and its Accessories: Demonstration and Explanation on Disassembly and Assembly of a Personal Computer by the faculty-in-charge. Students have to prepare a write-up on the same and include it in the Lab record and evaluated.

Laboratory Session-1: Write-up on Functional block diagram of Computer, CPU, Buses, Mother Board, Chip sets, Operating System & types of OS, Basics of Networking & Topology and NIC.

Laboratory Session-2: Write-up on RAM, SDRAM, FLASH memory, Hard disks, Optical media, CD-ROM/R/RW, DVDs, Flash drives, Keyboard, Mouse, Printers and Plotters. Introduction to flowchart, algorithm and pseudo code.

***Note: These* TWO Laboratory sessions** are used to fill the gap between theory classes and practical sessions. Both sessions are to be evaluated as lab experiments.

1

Laboratory Experiments:

Implement the following programs with WINDOWS / LINUX platform using appropriate C compiler.

1. Design and develop a flowchart or an algorithm that takes three coefficients (*a*, *b*, and *c*) of a Quadratic equation ($ax^2+bx+c=0$) as input and compute all possible roots. Implement a C program for the developed flowchart/algorithm and execute the same to output the possible roots for a given set of coefficients with appropriate messages.

2. Design and develop an algorithm to find the ***reverse*** of an integer number **NUM** and check whether it is PALINDROME or NOT. Implement a C program for the developed algorithm that takes an integer number as input and output the reverse of the same with suitable messages. Ex: Num: **2014**, Reverse: **4102**, Not a Palindrome

3.

3a. Design and develop a flowchart to find the square root of a given number *N*. Implement a C program for the same and execute for all possible inputs with appropriate messages. Note: **Don't use library function *sqrt(n)*.**

3b. Design and develop a C program to read a ***year*** as an input and find whether it is ***leap year*** or not. Also consider end of the centuries.

4. Design and develop an algorithm to evaluate polynomial $f(x) = a_4x^4 + a_3x^3 + a_2x^2 + a_1x + a_0$ for a given value of *x* and its coefficients using Homer's method. Implement a C program for the same and execute the program with different set of values of coefficients and *x*.

5. Draw the flowchart and Write a C Program to compute **Sin(x)** using Taylor series approximation given by **Sin(x) = x - (x^3/3!) + (x^5/5!) - (x^7/7!) + ...**
Compare your result with the built- in Library function. Print both the results with appropriate messages.

6. Develop an algorithm, implement and execute a C program that reads *N* integer numbers and arrange them in ascending order using ***Bubble Sort***.

7. Develop, implement and execute a C program that reads two matrices ***A*** (m x n) and ***B*** (p x q) and Compute product of matrices ***A*** and ***B***. Read matrix ***A*** and matrix ***B*** in row major order and in column major order respectively. Print both the input matrices and resultant matrix with suitable headings and output should be in matrix format only. Program must check the compatibility of orders of the matrices for multiplication. Report appropriate message in case of incompatibility.

8. Develop, implement and execute a C program to search a Name in a list of names using **Binary searching** Technique.

9. Write and execute a C program that

i. Implements string copy operation ***STRCOPY***(str1,str2) that copies a string str2 to another string *str2* without using library function.

ii. Read a sentence and print frequency of vowels and total count of consonants.

10.

a. Design and develop a C function ***RightShift***(*x ,n*) that takes two integers *x* and *n* as input and returns value of the integer *x* rotated to the right by *n* positions. Assume the integers are unsigned. Write a C program that invokes this function with different values for *x* and *n* and tabulate the results with suitable headings.

b. Design and develop a C function ***isprime***(num) that accepts an integer argument and returns 1 if the argument is prime, a 0 otherwise. Write a C program that invokes this function to generate prime numbers between the given range.

11. Draw the flowchart and write a ***recursive*** C function to find the factorial of a number, *n*!, defined by *fact*(*n*)=1, if *n*=0. Otherwise *fact*(*n*)=*n***fact*(*n*-1). Using this function, write a C program to compute the binomial coefficient $_nC_r$. Tabulate the results for different values of *n* and *r* with suitable messages.

12. Given two university information files "**studentname.txt**" and "**usn.txt**" that contains students Name and USN respectively. Write a C program to create a new file called "**output.txt**" and copy the content of files "studentname.txt" and "usn.txt" into output file in the sequence shown below. Display the contents of output file "output.txt" on to the screen.

Student Name	USN	←	Heading
Name 1	USN1		
Name 2	USN2		
....	....		
....	....		

13. Write a C program to maintain a record of n student details using an array of structures with four fields (Roll number, Name, Marks, and Grade). Assume appropriate data type for each field. Print the marks of the student, given the student name as input.

14. Write a C program using pointers to compute the sum, mean and standard deviation of all elements stored in an array of n real numbers.

Course outcomes:

- **Gaining Knowledge on various parts of a computer.**
- **Able to draw flowcharts and write algorithms**
- **Able design and development of C problem solving skills.**
- **Able design and develop modular programming skills.**
- **Able to trace and debug a program**

ENGINEERING PHYSICS LAB

Laboratory Code	15PHYL17 / 15PHYL27	IA Marks	20
Labs / Instructions Hours/Week	3 (1 hr Tutorial +2 hrs lab)	Exam Marks	80
Total Number of Lecture Hours	48	Exam Hours	03

CREDITS - 02

Course Objectives:

- The Objective of this course is to make the students gain practical knowledge to correlate with the theoretical studies. To achieve perfectness in experimental skills and the study of practical applications will bring more confidence and ability to develop and fabricate engineering and technical equipments.
- Design of circuits using new technology and latest components and to develop practical applications of engineering materials and use of principle in the right way to implement the modern technology.

EXPERIMENTS:

1. Block box experiment; Identification of unknown passive electrical components and determine the value of Inductance and Capacitance
2. Series and parallel LCR Circuits (Determination of resonant frequency and quality factor)
3. I–V Characteristics of Zener Diode. (determination of knee voltage, zener voltage and forward resistance)
4. Characteristics of Transistor (Study of Input and Output characteristics and calculation of input resistance, output resistance and amplification factor)
5. Photo Diode Characteristics (Study of I–V characteristics in reverse bias and variation of photocurrent as a function of reverse voltage and intensity).
6. Dielectric constant (Measurement of dielectric constant).
7. Diffraction (Measurement of wavelength of laser source using diffraction grating).
8. Torsional pendulum (Determination of M.I. of wire and Rigidity modulus).
9. Determination of Fermi energy. (Measurement of Fermi energy in copper).
10. Uniform Bending Experiment (Determination of Youngs modulus of material bar).
11. Newtons Rings, (Determination of radius of curvature of plano convex lens).

12. Verification of Stefan's Law.

Course Outcomes:

On Completion of this course, students are able to –

- Develop skills to import practical knowledge in real time solution.
- Understand principle, concept, working and application of new technology and comparison of results with theoretical calculations.
- Design new instruments with practical knowledge.
- Gain knowledge of new concept in the solution of practical oriented problems and to understand more deep knowledge about the solution to theoretical problems.
- Understand measurement technology, usage of new instruments and real time applications in engineering studies.

Note: 1) All the above twelve experiments are to be conducted

2) Two experiments are to be performed by the students in the examination

ANALOG AND DIGITAL ELECTRONICS

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - III

Subject Code	15CS32	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable the students to

- Recall and Recognize construction and characteristics of JFETs and MOSFETs and differentiate with BJT
- Evolve and Analyze Operational Amplifier circuits and their applications
- Describe, Illustrate and Analyze Combinational Logic circuits, Simplification of Algebraic Equations using Karnaugh Maps and Quine McCluskey Techniques.
- Describe and Design Decoders, Encoders, Digital multiplexers, Adders and Subtractors, Binary comparators, Latches and Master-Slave Flip-Flops.
- Describe, Design and Analyze Synchronous and Asynchronous Sequential
- Explain and design registers and Counters, A/D and D/A converters.

Module -1	Teaching Hours
Field Effect Transistors: Junction Field Effect Transistors, MOSFETs, Differences between JFETs and MOSFETs, Biasing MOSFETs, FET Applications, CMOS Devices. Wave-Shaping Circuits: Integrated Circuit(IC) Multivibrators. **Introduction to Operational Amplifier**: Ideal v/s practical Opamp, Performance Parameters, **Operational Amplifier Application Circuits**:Peak Detector Circuit, Comparator, Active Filters, Non-Linear Amplifier, Relaxation Oscillator, Current-To-Voltage Converter, Voltage-To-Current Converter. **Text book 1:- Ch5**: 5.2, 5.3, 5.5, 5.8, 5.9, 5.1.**Ch13**: 13.10.**Ch 16**: 16.3, 16.4. **Ch 17**: 7.12, 17.14, 17.15, 17.18, 17.19, 17.20, 17.21.)	10 Hours
Module -2	
The Basic Gates: Review of Basic Logic gates, Positive and Negative Logic, Introduction to HDL. **Combinational Logic Circuits**: Sum-of-Products Method, Truth Table to Karnaugh Map, Pairs Quads, and Octets, Karnaugh Simplifications, Don't-care Conditions, Product-of-sums Method, Product-of-sums simplifications, Simplification by Quine-McCluskey Method, Hazards and Hazard covers, HDL Implementation Models. **Text book 2:- Ch2**: 2.4, 2.5. **Ch3**: 3.2 to 3.11.	10 Hours
Module – 3	

Data-Processing Circuits: Multiplexers, Demultiplexers, 1-of-16 Decoder, BCD to Decimal Decoders, Seven Segment Decoders, Encoders, Exclusive-OR Gates, Parity Generators and Checkers, Magnitude Comparator, Programmable Array Logic, Programmable Logic Arrays, HDL Implementation of Data Processing Circuits. Arithmetic Building Blocks, Arithmetic Logic Unit **Flip- Flops:** RS Flip-Flops, Gated Flip-Flops, Edge-triggered RS FLIP-FLOP, Edge-triggered D FLIP-FLOPs, Edge-triggered JK FLIP-FLOPs. **Text book 2:- Ch 4:- 4.1 to 4.9, 4.11, 4.12, 4.14.Ch6:-6.7, 6.10.Ch8:- 8.1 to 8.5.**	**10 Hours**
Module-4	
Flip- Flops: FLIP-FLOP Timing, JK Master-slave FLIP-FLOP, Switch Contact Bounce Circuits, Various Representation of FLIP-FLOPs, HDL Implementation of FLIP-FLOP. **Registers:** Types of Registers, Serial In - Serial Out, Serial In - Parallel out, Parallel In - Serial Out, Parallel In - Parallel Out, Universal Shift Register, Applications of Shift Registers, Register implementation in HDL. **Counters:** Asynchronous Counters, Decoding Gates, Synchronous Counters, Changing the Counter Modulus. **(Text book 2:- Ch 8: 8.6, 8.8, 8.9, 8.10, 8.13. Ch 9: 9.1 to 9.8. Ch 10: 10.1 to 10.4)**	**10 Hours**
Module-5	
Counters: Decade Counters, Presettable Counters, Counter Design as a Synthesis problem, A Digital Clock, Counter Design using HDL. **D/A Conversion and A/D Conversion:** Variable, Resistor Networks, Binary Ladders, D/A Converters, D/A Accuracy and Resolution, A/D Converter-Simultaneous Conversion, A/D Converter-Counter Method, Continuous A/D Conversion, A/D Techniques, Dual-slope A/D Conversion, A/D Accuracy and Resolution. **Text book 2:- Ch 10: 10.5 to 10.9. Ch 12: 12.1 to 12.10**	**10 Hours**
Course outcomes: After Studying this course, students will be able to	
• Explain the operation of JFETs and MOSFETs , Operational Amplifier circuits and their application • Explain Combinational Logic, Simplification Techniques using Karnaugh Maps, Quine McCluskey technique. • Demonstrate Operation of Decoders, Encoders, Multiplexers, Adders and Subtractors, working of Latches, Flip-Flops, Designing Registers, Counters, A/D and D/A Converters • Design of Counters, Registers and A/D & D/A converters	
Graduate Attributes (as per NBA) 1. Engineering Knowledge 2. Design/Development of Solutions(partly) 3. Modern Tool Usage 4. Problem Analysis	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books:	

ANALOG AND DIGITAL ELECTRONICS LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER - III

Laboratory Code	15CSL37	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03
CREDITS – 02			

Course objectives: This laboratory course enable students to get practical experience in design, assembly and evaluation/testing of

- Analog components and circuits including Operational Amplifier, Timer, etc.
- Combinational logic circuits.
- Flip - Flops and their operations
- Counters and registers using flip-flops.
- Synchronous and Asynchronous sequential circuits.
- A/D and D/A converters

Descriptions (if any)

Any simulation package like MultiSim / P-spice /Equivalent software may be used.
Faculty-in-charge should demonstrate and explain the required hardware components and their functional Block diagrams, timing diagrams etc. Students have to prepare a write-up on the same and include it in the Lab record and to be evaluated.

Laboratory Session-1: Write-upon analog components; functional block diagram, Pin diagram (if any), waveforms and description. The same information is also taught in theory class; this helps the students to understand better.

Laboratory Session-2: Write-upon Logic design components, pin diagram (if any), Timing diagrams, etc. The same information is also taught in theory class; this helps the students to understand better.

Note: These **TWO Laboratory** sessions are used to fill the gap between theory classes and practical sessions. Both sessions are to be evaluated for 20 marks as lab experiments.

Laboratory Experiments:

1. a) Design and construct a Schmitt trigger using Op-Amp for given UTP and LTP values and demonstrate its working.
   b) Design and implement a Schmitt trigger using Op-Amp using a simulation package for two sets of UTP and LTP values and demonstrate its working.
2. a) Design and construct a rectangular waveform generator (Op-Amp relaxation oscillator) for given frequency and demonstrate its working.
   b) Design and implement a rectangular waveform generator (Op-Amp relaxation oscillator) using a simulation package and demonstrate the change in frequency when all resistor values are doubled.
3. Design and implement an Astable multivibrator circuit using 555 timer for a given frequency and duty cycle.

NOTE: hardware and software results need to be compared

Continued:

4. Design and implement Half adder, Full Adder, Half Subtractor, Full Subtractor using basic gates.
5. a) Given a 4-variable logic expression, simplify it using Entered Variable Map and realize the simplified logic expression using 8:1 multiplexer IC.
   b) Design and develop the Verilog /VHDL code for an 8:1 multiplexer. Simulate and verify its working.
6. a) Design and implement code converter I)Binary to Gray (II) Gray to Binary Code using basic gates.
7. Design and verify the Truth Table of 3-bit Parity Generator and 4-bit Parity Checker using basic Logic Gates with an even parity bit.
8. a) Realize a J-K Master / Slave Flip-Flop using NAND gates and verify its truth table.
   b) Design and develop the Verilog / VHDL code for D Flip-Flop with positive-edge triggering. Simulate and verify it's working.
9. a) Design and implement a mod-n (n<8) synchronous up counter using J-K Flip-Flop ICs and demonstrate its working.
   b) Design and develop the Verilog / VHDL code for mod-8 up counter. Simulate and verify it's working.
10. Design and implement an asynchronous counter using decade counter IC to count up from 0 to n (n<=9) and demonstrate on 7-segment display (using IC-7447).
11. Generate a Ramp output waveform using DAC0800 (Inputs are given to DAC through IC74393 dual 4-bit binary counter).

Study experiment

DISCRETE MATHEMATICAL STRUCTURES

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER – III

Subject Code	15CS36	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable the students to

- Provide theoretical foundations of computer science to perceive other courses in the programme.
- Illustrate applications of discrete structures: logic, relations, functions, set theory and counting.
- Describe different mathematical proof techniques,
- Illustrate the use of graph theory in computer science.

Module -1	Teaching Hours
Fundamentals of Logic: Basic Connectives and Truth Tables, Logic Equivalence – The Laws of Logic, Logical Implication – Rules of Inference. Fundamentals of Logic contd.: The Use of Quantifiers, Quantifiers, Definitions and the Proofs of Theorems,	10Hours
Module -2	
Properties of the Integers: Mathematical Induction, The Well Ordering Principle – Mathematical Induction, Recursive Definitions. Principles of Counting. **Fundamental Principles of Counting**: The Rules of Sum and Product, Permutations, Combinations – The Binomial Theorem, Combinations with Repetition.	10 Hours
Module – 3	
Relations and Functions: Cartesian Products and Relations, Functions – Plain and One-to-One, Onto Functions. The Pigeon-hole Principle, Function Composition and Inverse Functions. Properties of Relations, Computer Recognition – Zero-One Matrices and Directed Graphs, Partial Orders – Hasse Diagrams, Equivalence Relations and Partitions.	10 Hours
Module-4	
The Principle of Inclusion and Exclusion: The Principle of Inclusion and Exclusion, Generalizations of the Principle, Derangements – Nothing is in its Right Place, Rook Polynomials. **Recurrence Relations**: First Order Linear Recurrence Relation, The Second Order Linear Homogeneous Recurrence Relation with Constant Coefficients,	10 Hours
Module-5	
Introduction to Graph Theory: Definitions and Examples, Sub graphs, Complements, and Graph Isomorphism, Vertex Degree, Euler Trails and Circuits , **Trees**: Definitions, Properties, and Examples, Rooted Trees, Trees and Sorting, Weighted Trees and Prefix Codes	10 Hours

Course outcomes: After studying this course, students will be able to:

- Use propositional and predicate logic in knowledge representation and truth verification.
- Demonstrate the application of discrete structures in different fields of computer science.
- Solve problems using recurrence relations and generating functions.
- Application of different mathematical proofs techniques in proving theorems in the courses.
- Compare graphs, trees and their applications.

Graduate Attributes: (as per NBA)

1. Engineering Knowledge
2. Problem Analysis
3. Conduct Investigations of Complex Problems
4. Design/Development of Solutions.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Ralph P. Grimaldi: Discrete and Combinatorial Mathematics, , 5th Edition, Pearson Education. 2004. (Chapter 3.1, 3.2, 3.3, 3.4, Appendix 3, Chapter 2, Chapter 4.1, 4.2, Chapter 5.1 to 5.6, Chapter 7.1 to 7.4, Chapter 16.1, 16.2, 16.3, 16.5 to 16.9, and Chapter 14.1, 14.2, 14.3).

Reference Books:

1. Basavaraj S Anami and Venakanna S Madalli: Discrete Mathematics – A Concept based approach Universities Press, 2016
2. Kenneth H. Rosen: Discrete Mathematics and its Applications, 6th Edition, McGraw Hill, 2007.
3. Jayant Ganguly: A Treatise on Discrete Mathematical Structures, Sanguine-Pearson, 2010.
4. D.S. Malik and M.K. Sen: Discrete Mathematical Structures: Theory and Applications, Thomson 2004.
5. Thomas Koshy: Discrete Mathematics with Applications, Elsevier, 2005, Reprint 2008.

OPERATING SYSTEMS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VI

Subject Code	15CS64	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Introduce concepts and terminology used in OS
- Explain threading and multithreaded systems
- Illustrate process synchronization and concept of Deadlock
- Introduce Memory and Virtual memory management, File system and storage techniques

Module – 1	**Teaching Hours**
Introduction to operating systems, System structures: What operating systems do; Computer System organization; Computer System architecture; Operating System structure; Operating System operations; Process management; Memory management; Storage management; Protection and Security; Distributed system; Special-purpose systems; Computing environments. Operating System Services; User - Operating System interface; System calls; Types of system calls; System programs; Operating system design and implementation; Operating System structure; Virtual machines; Operating System generation; System boot. **Process Management** Process concept; Process scheduling; Operations on processes; Inter process communication	**10 Hours**
Module – 2	
Multi-threaded Programming: Overview; Multithreading models; Thread Libraries; Threading issues. Process Scheduling: Basic concepts; Scheduling Criteria; Scheduling Algorithms; Multiple-processor scheduling; Thread scheduling. **Process Synchronization**: Synchronization: The critical section problem; Peterson's solution; Synchronization hardware; Semaphores; Classical problems of synchronization; Monitors.	**10 Hours**
Module – 3	
Deadlocks : Deadlocks; System model; Deadlock characterization; Methods for handling deadlocks; Deadlock prevention; Deadlock avoidance; Deadlock detection and recovery from deadlock. **Memory Management**: Memory management strategies: Background; Swapping; Contiguous memory allocation; Paging; Structure of page table; Segmentation.	**10 Hours**
Module – 4	
Virtual Memory Management: Background; Demand paging; Copy-on-write; Page replacement; Allocation of frames; Thrashing. **File System, Implementation of File System**: File system: File concept; Access methods; Directory structure; File system mounting; File sharing; Protection: Implementing File system: File system structure; File system implementation; Directory implementation; Allocation methods; Free space management.	**10 Hours**
Module – 5	
Secondary Storage Structures, Protection: Mass storage structures; Disk	**10 Hours**

structure; Disk attachment; Disk scheduling; Disk management; Swap space management. Protection: Goals of protection, Principles of protection, Domain of protection, Access matrix, Implementation of access matrix, Access control, Revocation of access rights, Capability- Based systems. **Case Study: The Linux Operating System:** Linux history; Design principles; Kernel modules; Process management; Scheduling; Memory Management; File systems, Input and output, Inter-process communication.	

Course outcomes: The students should be able to:

- Demonstrate need for OS and different types of OS
- Apply suitable techniques for management of different resources
- Use processor, memory, storage and file system commands
- Realize the different concepts of OS in platform of usage through case studies

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Abraham Silberschatz, Peter Baer Galvin, Greg Gagne, Operating System Principles 7th edition, Wiley-India, 2006.

Reference Books

1. Ann McHoes Ida M Flynn, Understanding Operating System, Cengage Learning, 6th Edition
2. D.M Dhamdhere, Operating Systems: A Concept Based Approach 3rd Ed, McGraw-Hill, 2013.
3. P.C.P. Bhatt, An Introduction to Operating Systems: Concepts and Practice 4th Edition, PHI(EEE), 2014.
4. William Stallings Operating Systems: Internals and Design Principles, 6th Edition, Pearson.

SYSTEM SOFTWARE AND OPERATING SYSTEM LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VI

Subject Code	15CSL67	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- To make students familiar with Lexical Analysis and Syntax Analysis phases of Compiler Design and implement programs on these phases using LEX & YACC tools and/or C/C++/Java
- To enable students to learn different types of CPU scheduling algorithms used in operating system.
- To make students able to implement memory management - page replacement and deadlock handling algorithms

Description (If any):

Exercises to be prepared with minimum three files (Where ever necessary):

i. Header file.
ii. Implementation file.
iii. Application file where main function will be present.

The idea behind using three files is to differentiate between the developer and user sides. In the developer side, all the three files could be made visible. For the user side only header file and application files could be made visible, which means that the object code of the implementation file could be given to the user along with the interface given in the header file, hiding the source file, if required. Avoid I/O operations (printf/scanf) and use *data input file* where ever it is possible

Lab Experiments:

1.
   a) Write a LEX program to recognize valid *arithmetic expression*. Identifiers in the expression could be only integers and operators could be + and *. Count the identifiers & operators present and print them separately.

   b) Write YACC program to evaluate *arithmetic expression* involving operators: +, -, *, and /

2. Develop, Implement and Execute a program using YACC tool to recognize all strings ending with *b* preceded by *n* *a's* using the grammar $a^n b$ (note: input *n* value)

3. Design, develop and implement YACC/C program to construct *Predictive / LL(1) Parsing Table* for the grammar rules: $A \rightarrow aBa$, $B \rightarrow bB \mid e$. Use this table to parse the sentence: *abba$*

4. Design, develop and implement YACC/C program to demonstrate *Shift Reduce Parsing* technique for the grammar rules: $E \rightarrow E+T \mid T$, $T \rightarrow T*F \mid F$, $F \rightarrow (E) \mid id$ and parse the sentence: *id + id * id*.

Triples for the statement *A = -B * (C +D)* whose intermediate code in three-address form:

T1 = -B
T2 = C + D
T3 = T1 + T2
A = T3

6. a) Write a LEX program to eliminate *comment lines* in a *C* program and copy the resulting program into a separate file.
   b) Write YACC program to recognize valid *identifier, operators and keywords* in the given text (*C program*) file.
7. Design, develop and implement a C/C++/Java program to simulate the working of Shortest remaining time and Round Robin (RR) scheduling algorithms. Experiment with different quantum sizes for RR algorithm.
8. Design, develop and implement a C/C++/Java program to implement Banker's algorithm. Assume suitable input required to demonstrate the results.
9. Design, develop and implement a C/C++/Java program to implement page replacement algorithms LRU and FIFO. Assume suitable input required to demonstrate the results.

Study Experiment / Project:

NIL

Course outcomes: The students should be able to:

- Implement and demonstrate Lexer's and Parser's
- Evaluate different algorithms required for management, scheduling, allocation and communication used in operating system.

Conduction of Practical Examination:

- All laboratory experiments are to be included for practical examination.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script
- Marks distribution: Procedure + Conduction + Viva:20 + 50 +10 (80)
- **Change of experiment is allowed only once and marks allotted to the procedure part to be made zero**

UNIX AND SHELL PROGRAMMING

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2015 -2016)
SEMESTER – III

Subject Code	15CS35	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable the students to

- Illustrate the UNIX system architecture and use of basic Commands.
- Use of editors and networking commands.
- Demonstrate writing shell scripts.
- Categorize, compare and make use of UNIX system calls.

Module -1	Teaching Hours
Introduction, Brief history. Unix Components/Architecture. Features of Unix. The UNIX Environment and UNIX Structure, Posix and Single Unix specification. The login prompt. General features of Unix commands/ command structure. Command arguments and options. Understanding of some basic commands such as echo, printf, ls, who, date, passwd, cal, Combining commands. Meaning of Internal and external commands. The type command: knowing the type of a command and locating it. The man command knowing more about Unix commands and using Unix online manual pages. The man with keyword option and whatis. The more command and using it with other commands. Knowing the user terminal, displaying its characteristics and setting characteristics. Managing the non-uniform behaviour of terminals and keyboards. The root login. Becoming the super user: su command. The /etc/passwd and /etc/shadow files. Commands to add, modify and delete users. **Topics from chapter 2 , 3 and 15 of text book 1,chapter 1 from text book 2**	10Hours
Module -2	
Unix files. Naming files. Basic file types/categories. Organization of files. Hidden files. Standard directories. Parent child relationship. The home directory and the HOME variable. Reaching required files- the PATH variable, manipulating the PATH, Relative and absolute pathnames. Directory commands – pwd, cd, mkdir, rmdir commands. The dot (.) and double dots (..) notations to represent present and parent directories and their usage in relative path names. File related commands – cat, mv, rm, cp, wc and od commands. File attributes and permissions and knowing them. The ls command with options. Changing file permissions: the relative and absolute permissions changing methods. Recursively changing file permissions. Directory permissions. **Topics from chapters 4, 5 and 6 of text book 1**	10Hours

Module – 3	
The vi editor. Basics. The .exrc file. Different ways of invoking and quitting vi. Different modes of vi. Input mode commands. Command mode commands. The ex mode commands. Illustrative examples Navigation commands. Repeat command. Pattern searching. The search and replace command. The set, map and abbr commands. Simple examples using these commands. The shells interpretive cycle. Wild cards and file name generation. Removing the special meanings of wild cards. Three standard files and redirection. Connecting commands: Pipe. Splitting the output: tee. Command substitution. Basic and Extended regular expressions. The grep, egrep. Typical examples involving different regular expressions. **Topics from chapters 7, 8 and 13 of text book 1. Topics from chapter 2 and 9 ,10 of text book 2**	**10Hours**
Module-4	
Shell programming. Ordinary and environment variables. The .profile. Read and readonly commands. Command line arguments. exit and exit status of a command. Logical operators for conditional execution. The test command and its shortcut. The if, while, for and case control statements. The set and shift commands and handling positional parameters. The here (<<) document and trap command. Simple shell program examples. File inodes and the inode structure. File links – hard and soft links. Filters. Head and tail commands. Cut and paste commands. The sort command and its usage with different options. The umask and default file permissions. Two special files /dev/null and /dev/tty. **Topics from chapter 11, 12, 14 of text book 1,chapter 17 from text book2**	**10Hours**
Module-5	
Meaning of a process. Mechanism of process creation. Parent and child process. The ps command with its options. Executing a command at a specified point of time: at command. Executing a command periodically: cron command and the crontab file.. Signals. The nice and nohup commands. Background processes. The bg and fg command. The kill command. The find command with illustrative example. Structure of a perl script. Running a perl script. Variables and operators. String handling functions. Default variables - $_ and $. – representing the current line and current line number. The range operator. Chop() and chomp() functions. Lists and arrays. The @-variable. The splice operator, push(), pop(), split() and join(). File handles and handling file – using open(), close() and die () functions.. Associative arrays – keys and value functions. Overview of decision making loop control structures – the foreach. Regular expressions – simple and multiple search patterns. The match and substitute operators. Defining and using subroutines. **Topics from chapter 9 and 19 of text book 1. Topics from chapter 11 of reference book 1**	**10Hours**
Course outcomes:	

After studying this course, students will be able to:

- Explain UNIX system and use different commands.
- Write Shell scripts for certain functions on different subsystems.
- Demonstrate use of editors and Perl script writing

Graduate Attributes (as per NBA)

1. Engineering Knowledge
2. Environment and Sustainability
3. Design/Development of Solutions

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Sumitabha Das., Unix Concepts and Applications., 4th Edition., Tata McGraw Hill
2. Behrouz A. Forouzan, Richard F. Gilberg : UNIX and Shell Programming- Cengage Learning – India Edition. 2009.

Reference Books:

1. M.G. Venkatesh Murthy: UNIX & Shell Programming, Pearson Education.
2. Richard Blum , Christine Bresnahan : Linux Command Line and Shell Scripting Bible, 2nd Edition , Wiley,2014.

COMPUTER GRAPHICS AND VISUALIZATION [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – VI			
Subject Code	15CS62	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to

- Explain hardware, software and OpenGL Graphics Primitives.
- Illustrate interactive computer graphic using the OpenGL.
- Design and implementation of algorithms for 2D graphics Primitives and attributes.
- Demonstrate Geometric transformations, viewing on both 2D and 3D objects.
- Infer the representation of curves, surfaces, Color and Illumination models

Module – 1	Teaching Hours
Overview: Computer Graphics and OpenGL: Computer Graphics:Basics of computer graphics, Application of Computer Graphics, Video Display Devices: Random Scan and Raster Scan displays, color CRT monitors, Flat panel displays. Raster-scan systems: video controller, raster scan Display processor, graphics workstations and viewing systems, Input devices, graphics networks, graphics on the internet, graphics software. OpenGL: Introduction to OpenGL ,coordinate reference frames, specifying two-dimensional world coordinate reference frames in OpenGL, OpenGL point functions, OpenGL line functions, point attributes, line attributes, curve attributes, OpenGL point attribute functions, OpenGL line attribute functions, Line drawing algorithms(DDA, Bresenham's), circle generation algorithms (Bresenham's). **Text-1:Chapter -1: 1-1 to 1-9,2-1 to 2-9 (Excluding 2-5),3-1 to 3-5,3-9,3-20**	**10 Hours**
Module – 2	
Fill area Primitives, 2D Geometric Transformations and 2D viewing: Fill area Primitives: Polygon fill-areas, OpenGL polygon fill area functions, fill area attributes, general scan line polygon fill algorithm, OpenGL fill-area attribute functions. 2DGeometric Transformations: Basic 2D Geometric Transformations, matrix representations and homogeneous coordinates. Inverse transformations, 2DComposite transformations, other 2D transformations, raster methods for geometric transformations, OpenGL raster transformations, OpenGL geometric transformations function, 2D viewing: 2D viewing pipeline, OpenGL 2D viewing functions. **Text-1:Chapter 3-14 to 3-16,4-9,4-10,4-14,5-1 to 5-7,5-17,6-1,6-4**	**10 Hours**
Module – 3	
Clipping,3D Geometric Transformations, Color and Illumination Models: Clipping: clipping window, normalization and viewport transformations, clipping algorithms,2D point clipping, 2D line clipping algorithms: cohen-sutherland line clipping only -polygon fill area clipping: Sutherland-Hodgeman polygon clipping algorithm only.3DGeometric Transformations: 3D translation, rotation, scaling, composite 3D transformations, other 3D transformations, affine transformations, OpenGL geometric transformations functions. Color Models: Properties of light, color models, RGB and CMY color models. Illumination Models: Light sources, basic illumination models-Ambient light, diffuse reflection, specular and phong	**10 Hours**

model, Corresponding openGL functions **Text-1: Chapter :6-2 to 6-86 (Excluding 6-6),5-9 to 5-17(Excluding 5-15),12-1,12-2,12-4,12-6,10-1,10-3**	
Module – 4	
3D Viewing and Visible Surface Detection: 3DViewing 3D viewing concepts, 3D viewing pipeline, 3D viewing coordinate parameters , Transformation from world to viewing coordinates, Projection transformation, orthogonal projections, perspective projections, The viewport transformation and 3D screen coordinates. OpenGL 3D viewing functions. Visible Surface Detection Methods Classification of visible surface Detection algorithms, back face detection, depth buffer method and OpenGL visibility detection functions. **Text-1: Chapter: 7-1 to 7-38(Excluding 7-7), 9-1 to 9-3, 9-14**	**10 Hours**
Module – 5	
Input& Interaction, Curves and Computer Animation: Input and Interaction: Input devices, clients and servers, Display Lists, Display Lists and Modelling, Programming Event Driven Input, Menus Picking, Building Interactive Models, Animating Interactive programs, Design of Interactive programs, Logic operations Curved surfaces, quadric surfaces, OpenGL Quadric-Surface and Cubic-Surface Functions, Bezier Spline Curves, Bezier surfaces, OpenGL curve functions Corresponding openGL functions. **Text-1: Chapter :8-2 to 8-6 (Excluding 8-5),8-9,8-10,8-11,2-8,8-18,13-11,2-2,13-3,13-4,13-10** **Text-2: Chapter 3: 3-1 to 3.11: Input& interaction**	**10 Hours**
Course outcomes: The students should be able to: • Design and implement algorithms for 2D graphics primitives and attributes. • Illustrate Geometric transformations on both 2D and 3D objects. • Apply concepts of clipping and visible surface detection in 2D and 3D viewing, and Illumination Models. • Decide suitable hardware and software for developing graphics packages using OpenGL.	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books: 1. Donald Hearn & Pauline Baker: Computer Graphics with OpenGL Version,3rd / 4th Edition, Pearson Education,2011 2. Edward Angel: Interactive Computer Graphics- A Top Down approach with OpenGL, 5th edition. Pearson Education, 2008	
Reference Books: 1. James D Foley, Andries Van Dam, Steven K Feiner, John F Hughes Computer graphics with OpenGL: pearson education 2. Xiang, Plastock : Computer Graphics , shaum's outline series, 2nd edition, TMH. 3. Kelvin Sung, Peter Shirley, steven Baer : Interactive Computer Graphics, concepts and applications, Cengage Learning 4. M M Raikar: Computer Graphics using OpenGL, Filplearning/Elsevier	

COMPUTER GRAPHICS LABORATORY WITH MINI PROJECT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VI

Subject Code	15CSL68	IA Marks	20
Number of Lecture Hours/Week	01I + 02P	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- Demonstrate simple algorithms using OpenGL Graphics Primitives and attributes.
- Implementation of line drawing and clipping algorithms using OpenGL functions
- Design and implementation of algorithms Geometric transformations on both 2D and 3D objects.

Description (If any):

–

Lab Experiments:

PART A

Design, develop, and implement the following programs using OpenGL API

1. Implement Brenham's line drawing algorithm for all types of slope.
   Refer:Text-1: Chapter 3.5
   Refer:Text-2: Chapter 8
2. Create and rotate a triangle about the origin and a fixed point.
   Refer:Text-1: Chapter 5-4
3. Draw a colour cube and spin it using OpenGL transformation matrices.
   Refer:Text-2: Modelling a Coloured Cube
4. Draw a color cube and allow the user to move the camera suitably to experiment with perspective viewing.
   Refer:Text-2: Topic: Positioning of Camera
5. Clip a lines using Cohen-Sutherland algorithm
   Refer:Text-1: Chapter 6.7
   Refer:Text-2: Chapter 8
6. To draw a simple shaded scene consisting of a tea pot on a table. Define suitably the position and properties of the light source along with the properties of the surfaces of the solid object used in the scene.
   Refer:Text-2: Topic: Lighting and Shading
7. Design, develop and implement recursively subdivide a tetrahedron to form 3D sierpinski gasket. The number of recursive steps is to be specified by the user.
   Refer: Text-2: Topic: sierpinski gasket.
8. Develop a menu driven program to animate a flag using Bezier Curve algorithm
   Refer: Text-1: Chapter 8-10
9. Develop a menu driven program to fill the polygon using scan line algorithm

Project:

PART –B (MINI-PROJECT) :

Student should develop mini project on the topics mentioned below or similar applications using Open GL API. Consider all types of attributes like color, thickness, styles, font, background, speed etc., while doing mini project.

(During the practical exam: the students should demonstrate and answer Viva-Voce)

Sample Topics:

Simulation of concepts of OS, Data structures, algorithms etc.

Course outcomes: The students should be able to:

- Apply the concepts of computer graphics
- Implement computer graphics applications using OpenGL
- Animate real world problems using OpenGL

Conduction of Practical Examination:

1. All laboratory experiments from part A are to be included for practical examination.
2. Mini project has to be evaluated for 30 Marks as per 6(b).
3. Report should be prepared in a standard format prescribed for project work.
4. Students are allowed to pick one experiment from the lot.
5. Strictly follow the instructions as printed on the cover page of answer script.
6. Marks distribution:
   a) Part A: Procedure + Conduction + Viva:10 + 35 +5 =50 Marks
   b) Part B: Demonstration + Report + Viva voce = 15+10+05 = 30 Marks
7. Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.

Reference books:

1. Donald Hearn & Pauline Baker: Computer Graphics-OpenGL Version, 3rd Edition, Pearson Education, 2011
2. Edward Angel: Interactive computer graphics- A Top Down approach with OpenGL, 5th edition. Pearson Education, 2011
3. M M Raikar, Computer Graphics using OpenGL, Fillip Learning / Elsevier, Bangalore / New Delhi (2013)

SYSTEM MODELLING AND SIMULATION
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VIII

Subject Code	15CS834	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to

- Explain the basic system concept and definitions of system;
- Discuss techniques to model and to simulate various systems;
- Analyze a system and to make use of the information to improve the performance.

Module – 1	**Teaching Hours**
Introduction: When simulation is the appropriate tool and when it is not appropriate, Advantages and disadvantages of Simulation; Areas of application. Systems and system environment; Components of a system; Discrete and continuous systems, Model of a system; Types of Models, Discrete-Event System Simulation. Simulation examples: Simulation of queuing systems. **General Principles, Simulation Software:** Concepts in Discrete-Event Simulation. The Event-Scheduling / Time-Advance Algorithm. Manual simulation Using Event Scheduling	**10 Hours**
Module – 2	
Statistical Models in Simulation :Review of terminology and concepts, Useful statistical models,Discrete distributions. Continuous distributions,Poisson process, Empirical distributions. **Queuing Models:** Characteristics of queuing systems,Queuing notation,Long-run measures of performance of queuing systems,Long-run measures of performance of queuing systems cont…,Steady-state behavior of M /G/1 queue, Networks of queues,	**10 Hours**
Module – 3	
Random-NumberGeneration: Properties of random numbers; Generation of pseudo-random numbers, Techniques for generating random numbers,Tests for Random Numbers, **Random-Variate Generation:** ,Inverse transform technique Acceptance-Rejection technique.	**10 Hours**
Module – 4	
Input Modeling: Data Collection; Identifying the distribution with data, Parameter estimation, Goodness of Fit Tests, Fitting a non-stationary Poisson process, Selecting input models without data, Multivariate and Time-Series input models. **Estimation of Absolute Performance:** Types of simulations with respect to output analysis ,Stochastic nature of output data, Measures of performance and their estimation, **Contd..**	**10 Hours**
Module – 5	
Measures of performance and their estimation,Output analysis for terminating simulations Continued. .,Output analysis for steady-state simulations. **Verification, Calibration And Validation:** Optimization: Model building, verification and validation, Verification of simulation models, Verification of	**10 Hours**

simulation models,Calibration and validation of models, Optimization via Simulation.	
Course outcomes: The students should be able to:	
• Explain the system concept and apply functional modeling method to model the activities of a static system • Describe the behavior of a dynamic system and create an analogous model for a dynamic system; • Simulate the operation of a dynamic system and make improvement according to the simulation results.	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books:	
1. Jerry Banks, John S. Carson II, Barry L. Nelson, David M. Nicol: Discrete-Event System Simulation, 5 th Edition, Pearson Education, 2010.	
Reference Books:	
1. Lawrence M. Leemis, Stephen K. Park: Discrete – Eve nt Simulation: A First Course, Pearson Education, 2006 2. Averill M. Law: Simulation Modeling and Analysis, 4 th Edition, Tata McGraw-Hill, 2007	

PROJECT WORK PHASE II [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – VIII			
Subject Code	15CSP85	IA Marks	100
Number of Lecture Hours/Week	06	Exam Marks	100
Total Number of Lecture Hours	–	Exam Hours	03
CREDITS – 06			
Course objectives: This course will enable students to			
Description (If any):			
Course outcomes: The students should be able to:			
Conduction of Practical Examination:			

INTERNSHIP / PROFESSIONAL PRACTISE [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2016 -2017) SEMESTER – VIII			
Subject Code	15CS84	IA Marks	50
Duration	4 weeks	Exam Marks	50
		Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

Description (If any):

Course outcomes: The students should be able to:

Evaluation of Internship :

SYSTEM SOFTWARE AND COMPILER DESIGN
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2016 -2017)
SEMESTER – VI

Subject Code	15CS63	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Define System Software such as Assemblers, Loaders, Linkers and Macroprocessors
- Familiarize with source file, object file and executable file structures and libraries
- Describe the front-end and back-end phases of compiler and their importance to students

Module – 1	**Teaching Hours**
Introduction to System Software, Machine Architecture of SIC and SIC/XE. **Assemblers**: Basic assembler functions, machine dependent assembler features, machine independent assembler features, assembler design options. **Macroprocessors**: Basic macro processor functions, **Text book 1: Chapter 1: 1.1,1.2,1.3.1,1.3.2, Chapter2 : 2.1-2.4,Chapter4: 4.1.1,4.1.2**	**10 Hours**
Module – 2	
Loaders and Linkers: Basic Loader Functions, Machine Dependent Loader Features, Machine Independent Loader Features, Loader Design Options, Implementation Examples. **Text book 1 : Chapter 3 ,3.1 -3.5**	**10 Hours**
Module – 3	
Introduction: Language Processors, The structure of a compiler, The evaluation of programming languages, The science of building compiler, Applications of compiler technology, Programming language basics **Lexical Analysis**: The role of lexical analyzer, Input buffering, Specifications of token, recognition of tokens, lexical analyzer generator, Finite automate. **Text book 2:Chapter 1 1.1-1.6 Chapter 3 3.1 – 3.6**	**10 Hours**
Module – 4	
Syntax Analysis: Introduction, Role Of Parsers, Context Free Grammars, Writing a grammar, Top Down Parsers, Bottom-Up Parsers, Operator-Precedence Parsing **Text book 2: Chapter 4 4.1 4.2 4.3 4.4 4.5 4.6 Text book 1 : 5.1.3**	**10 Hours**
Module – 5	
Syntax Directed Translation, Intermediate code generation, Code generation **Text book 2: Chapter 5.1, 5.2, 5.3, 6.1, 6.2, 8.1, 8.2**	**10 Hours**

Course outcomes: The students should be able to:

- Explain system software such as assemblers, loaders, linkers and macroprocessors
- Design and develop lexical analyzers, parsers and code generators
- Utilize lex and yacc tools for implementing different concepts of system software

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. System Software by Leland. L. Beck, D Manjula, 3rd edition, 2012
2. Compilers-Principles, Techniques and Tools by Alfred V Aho, Monica S. Lam, Ravi Sethi, Jeffrey D. Ullman. Pearson, 2nd edition, 2007

Reference Books:

1. Systems programming – Srimanta Pal , Oxford university press, 2016
2. System programming and Compiler Design, K C Louden, Cengage Learning
3. System software and operating system by D. M. Dhamdhere TMG
4. Compiler Design, K Muneeswaran, Oxford University Press 2013.

ADVANCES IN COMPUTER NETWORKS [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2018 -2019) SEMESTER – I			
Subject Code	18LNI321 / 18SCN12 / 18SCS151	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to

- Discuss with the basics of Computer Networks.
- Compare various Network architectures.
- Discuss fundamental protocols.
- Define and analyze network traffic, congestion, controlling and resource allocation.

Module 1	Contact Hours
Foundation: Building a Network, Requirements, Perspectives, Scalable Connectivity, Cost-Effective Resource sharing, Support for Common Services, Manageability, Protocol layering, Performance, Bandwidth and Latency, Delay X Bandwidth Product, Perspectives on Connecting, Classes of Links, Reliable Transmission, Stop-and-Wait , Sliding Window, Concurrent Logical Channels. **T1: Chapter 1.1, 1.2, 1.5.1, 1.5.2., 2.1, 2.5T2: Chapter 4** **RBT: L1, L2, L3**	10 Hours
Module 2	
Internetworking I: Switching and Bridging, Datagram's, Virtual Circuit Switching, Source Routing, Bridges and LAN Switches, Basic Internetworking (IP), What is an Internetwork?, Service Model, Global Addresses, Datagram Forwarding in IP, sub netting and classless addressing, Address Translation (ARP), Host Configuration (DHCP), Error Reporting (ICMP), Virtual Networks and Tunnels. **T1:** Chapter 3.1, 3.2, **RBT: L1, L2, L3**	10 Hours
Module 3	
Internetworking- II: Network as a Graph, Distance Vector (RIP), Link State (OSPF), Metrics, The Global Internet, Routing Areas, Routing among Autonomous systems (BGP), IP Version 6 (IPv6), Mobility and Mobile IP **T1:** Chapter 3.3, 4.1.1,4.1.3**T2:**Chapter 13.1 to 13.18 , Ch 18. **RBT: L1, L2, L3**	10 Hours
Module 4	
End-to-End Protocols: Simple Demultiplexer (UDP), Reliable Byte Stream(TCP), End-to-End Issues, Segment Format, Connecting Establishment and Termination, Sliding Window Revisited, Triggering Transmission, Adaptive Retransmission, Record Boundaries, TCP Extensions, Queuing Disciplines, FIFO, Fair Queuing, TCP Congestion Control, Additive Increase/ Multiplicative Decrease, Slow Start, Fast Retransmit and Fast Recovery **T1:** Chapter 5.1, 5.2.1 to 5.2.8, 6.2, 6.3 **RBT: L1, L2, L3**	10 Hours
Module 5	
Congestion Control and Resource Allocation Congestion-Avoidance Mechanisms, DEC bit, Random Early Detection (RED), Source-Based Congestion Avoidance. The Domain Name System (DNS), Electronic Mail (SMTP,POP,IMAP,MIME), World Wide Web	10 Hours

(HTTP), Network Management (SNMP) **T1**: Chapter 6.4 **T2**: Chapter 23.1 to 23.16, Chapter 24, Chapter 25, Chapter 27.1 to 27.8 **RBT: L1, L2, L3**	

Course Outcomes

The students should be able to:

- List and classify network services, protocols and architectures, explain why they are layered.
- Choose key Internet applications and their protocols, and apply to develop their own applications (e.g. Client Server applications, Web Services) using the sockets API.
- Explain develop effective communication mechanisms using techniques like connection establishment, queuing theory, recovery Etc.
- Explain various congestion control techniques.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Larry Peterson and Bruce S Davis "Computer Networks :A System Approach" 5th Edition , Elsevier -2014.
2. Douglas E Comer, "Internetworking with TCP/IP, Principles, Protocols and Architecture" 6th Edition, PHI – 2014.

Reference Books:

1. Uyless Black, "Computer Networks, Protocols , Standards and Interfaces" 2 nd Edition -PHI.
2. Behrouz A Forouzan, "TCP /IP Protocol Suite" 4th Edition – Tata McGraw-Hill.

INFORMATION AND NETWORK SECURITY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2018 -2019)
SEMESTER – I

Subject Code	18LNI13 / 18SCN13 / 18SCS322	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Explain standard algorithms used to provide confidentiality, integrity and authenticity.
- Distinguish key distribution and management schemes.
- Deploy encryption techniques to secure data in transit across data networks
- Implement security applications in the field of Information technology

Module 1	**Contact Hours**
Classical Encryption Techniques Symmetric Cipher Model, Cryptography, Cryptanalysis and Brute-Force Attack, Substitution Techniques, Caesar Cipher, Mono-alphabetic Cipher, Playfair Cipher, Hill Cipher, Poly alphabetic Cipher, One Time Pad. **Block Ciphers and the data encryption standard:** Traditional block Cipher structure, stream Ciphers and block Ciphers, Motivation for the feistel Cipher structure, the feistel Cipher, The data encryption standard, DES encryption, DES decryption, A DES example, results, the avalanche effect, the strength of DES, the use of 56-Bit Keys, the nature of the DES algorithm, timing attacks, Block cipher design principles, number of rounds, design of function F, key schedule algorithm **RBT: L1, L2, L3**	**10 Hours**
Module 2	
Public-Key Cryptography and RSA: Principles of public-key cryptosystems. Public-key cryptosystems. Applications for public-key cryptosystems, requirements for public-key cryptosystems. Public-key cryptanalysis. The RSA algorithm, description of the algorithm, computational aspects, the security of RSA. **Other Public-Key Cryptosystems:** Diffie-hellman key exchange, The algorithm, key exchange protocols, man in the middle attack, Elgamal Cryptographic systems, Elliptic curve arithmetic, abelian groups, elliptic curves over real numbers, elliptic curves over Zp, elliptic curves overGF(2m), Elliptic curve cryptography, Analog of Diffie-hellman key exchange, Elliptic curve encryption/ decryption, security of Elliptic curve cryptography, Pseudorandom number generation based on an asymmetric cipher, PRNG based on RSA. **RBT: L1, L2, L3**	**10 Hours**
Module 3	
Key Management and Distribution: Symmetric key distribution using Symmetric encryption, A key distribution scenario, Hierarchical key control, session key lifetime, a transparent key control scheme, Decentralized key control, controlling key usage, Symmetric key distribution using asymmetric encryption, simple secret key distribution, secret key distribution with confidentiality and authentication, A hybrid scheme, distribution of public keys, public announcement of public keys, publicly available directory, public key authority, public keys certificates, X-509 certificates. Certificates, X-509 version 3, public key infrastructure. **User Authentication:** Remote user Authentication principles, Mutual Authentication, one way Authentication, remote user Authentication using Symmetric encryption, Mutual Authentication, one way Authentication, Kerberos, Motivation , Kerberos version 4, Kerberos version 5, Remote user Authentication using Asymmetric encryption,	**10 Hours**

Mutual Authentication, one way Authentication, federated identity management, identity management, identity federation, personal identity verification. **RBT: L1, L2, L3**	
Module 4	
Wireless network security: Wireless security, Wireless network threats, Wireless network measures, mobile device security, security threats, mobile device security strategy, IEEE 802.11 Wireless LAN overview, the Wi-Fi alliance, IEEE 802 protocol architecture, Security, IEEE 802.11i services, IEEE 802.11i phases of operation, discovery phase, Authentication phase, key management phase, protected data transfer phase, the IEEE 802.11i pseudorandom function. **Web Security Considerations:** Web Security Threats, Web Traffic Security Approaches. **Secure Sockets Layer:** SSL Architecture, SSL Record Protocol, Change Cipher Spec Protocol, Alert Protocol, and shake Protocol, Cryptographic Computations. **Transport Layer Security:** Version Number, Message Authentication Code, Pseudorandom Functions, Alert Codes, Cipher Suites, Client Certificate Types, Certificate Verify and Finished Messages, Cryptographic Computations, and Padding. **HTTPS** Connection Initiation, Connection Closure. **Secure Shell(SSH)** Transport Layer Protocol, User Authentication Protocol, Connection Protocol **RBT: L1, L2, L3**	**10 Hours**
Module 5	
Electronic Mail Security: Pretty good privacy, notation, operational; description, S/MIME, RFC5322, Multipurpose internet mail extensions, S/MIME functionality, S/MIME messages, S/MIME certificate processing, enhanced security services, Domain keys identified mail, internet mail architecture, E-Mail threats, DKIM strategy, DKIM functional flow. **IP Security:** IP Security overview, applications of IPsec, benefits of IPsec, Routing applications, IPsec documents, IPsec services, transport and tunnel modes, IP Security policy, Security associations, Security associations database, Security policy database, IP traffic processing, Encapsulating Security payload, ESP format, encryption and authentication algorithms, Padding, Anti replay service, transport and tunnel modes, combining security associations, authentication plus confidentiality, basic combinations of security associations, internet key exchange, key determinations protocol, header and payload formats, cryptographic suits. **RBT: L1, L2, L3**	**10 Hours**
Course Outcomes	
The students should be able to: • Analyze the vulnerabilities in any computing system and hence be able to design a security solution. • Identify the security issues in the network and resolve it. • Evaluate security mechanisms using rigorous approaches, including theoretical.	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books: 1. William Stallings, Cryptography and Network Security, Pearson 6th edition.	
Reference Books: 1. V K Pachghare: Cryptography and Information Security.	

INTERNET OF THINGS [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2018 -2019) SEMESTER – I			
Subject Code	18LNI22 / 18SCE23 / 18SCN14 / 18SCS14 / 18SSE321	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to

- Define and explain basic issues, policy and challenges in the IoT
- Illustrate Mechanism and Key Technologies in IoT
- Explain the Standard of the IoT
- Explain resources in the IoT and deploy of resources into business
- Demonstrate data analytics for IoT

Module -1	Contact Hours
What is The Internet of Things? Overview and Motivations, Examples of Apllications, IPV6 Role, Areas of Development and Standardization, Scope of the Present Investigation.Internet of Things Definitions and frameworks-IoT Definitions, IoT Frameworks, Basic Nodal Capabilities. Internet of Things Apjplication Examples-Overview, Smart Metering/Advanced Metering Infrastructure-Health/Body Area Networks, City Automation, Automotive Applications, Home Automation, Smart Cards, Tracking, Over-The-Air-Passive Surveillance/Ring of Steel, Control Application Examples, Myriad Other Applications. **RBT: L1, L2, L3**	10 Hours
Module -2	
Fundamental IoT Mechanism and Key Technologies-Identification of IoT Object and Services, Structural Aspects of the IoT, Key IoT Technologies. Evolving IoT Standards-Overview and Approaches, IETF IPV6 Routing Protocol for RPL Roll, Constrained Application Protocol, Representational State Transfer, ETSI M2M,Third Generation Partnership Project Service Requirements for Machine-Type Communications, CENELEC, IETF IPv6 Over Low power WPAN, Zigbee IP(ZIP),IPSO **RBT: L1, L2, L3**	10 Hours
Module – 3	
Layer ½ Connectivity: Wireless Technologies for the IoT-WPAN Technologies for IoT/M2M, Cellular and Mobile Network Technologies for IoT/M2M,Layer 3 Connectivity :IPv6 Technologies for the IoT: Overview and Motivations. Address Capabilities,IPv6 Protocol Overview, IPv6 Tunneling, IPsec in IPv6,Header Compression Schemes, Quality of Service in IPv6, Migration Strategies to IPv6. **RBT: L1, L2, L3**	10 Hours
Module-4	
Case Studies illustrating IoT Design-Introduction, Home Automation, Cities, Environment, Agriculture, Productivity Applications. **RBT: L1, L2, L3**	10 Hours
Module-5	
Data Analytics for IoT – Introduction, Apache Hadoop, Using Hadoop MapReduce for Batch Data Analysis, Apache Oozie, Apache Spark, Apache Storm, Using Apache Storm for Real-time Data Analysis, Structural Health Monitoring Case Study. **RBT: L1, L2, L3**	10 Hours

Course outcomes:
At the end of this course the students will be able to: • Develop schemes for the applications of IOT in real time scenarios • Manage the Internet resources • Model the Internet of things to business • Understand the practical knowledge through different case studies • Understand data sets received through IoT devices and tools used for analysis
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.
Text Books: 1. Daniel Minoli, "Building the Internet of Things with IPv6 and MIPv6:The Evolving World of M2M Communications", Wiley, 2013. 2. ArshdeepBahga, Vijay Madisetti, "Internet of Things: A Hands on Approach" Universities Press., 2015
Reference Books: 1. Michael Miller," The Internet of Things", First Edition, Pearson, 2015. 2. Claire Rowland,Elizabeth Goodman et al.," Designing Connected Products", First Edition,O'Reilly, 2015.

WIRELESS NETWORKS AND MOBILE COMPUTING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2018 -2019)
SEMESTER – I

Subject Code	18LNI331 / 18SCE241 / 18SCN151 / 18SCS323	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Define concepts of wireless communication.
- Compare and contrast propagation methods, Channel models, capacity calculations multiple antennas and multiple user techniques used in the mobile communication.
- Explain CDMA, GSM, Mobile IP, Wimax and Different Mobile OS
- Illustrate various Markup Languages CDC, CLDC, MIDP; Programming for CLDC, MIDlet model and security concerns

Module -1	**Contact Hours**
Mobile Computing Architecture: Architecture for Mobile Computing, 3-tier Architecture, Design Considerations for Mobile Computing. Wireless Networks : Global Systems for Mobile Communication (GSM and Short Service Messages (SMS): GSM Architecture, Entities, Call routing in GSM, PLMN Interface, GSM Addresses and Identities, Network Aspects in GSM, Mobility Management, GSM Frequency allocation. Introduction to SMS, SMS Architecture, SM MT, SM MO, SMS as Information bearer, applications, GPRS and Packet Data Network, GPRS Network Architecture, GPRS Network Operations, Data Services in GPRS, Applications for GPRS, Billing and Charging in GPRS, Spread Spectrum technology, IS-95, CDMA versus GSM, Wireless Data, Third Generation Networks, Applications on 3G, Introduction to WiMAX. **RBT: L1, L2, L3**	10Hours
Module -2	
Mobile Client: Moving beyond desktop, Mobile handset overview, Mobile phones and their features, PDA, Design Constraints in applications for handheld devices. Mobile IP: Introduction, discovery, Registration, Tunneling, Cellular IP, Mobile IP with IPv6 **RBT: L1, L2, L3**	10 Hours
Module – 3	
Mobile OS and Computing Environment : Smart Client Architecture, The Client: User Interface, Data Storage, Performance, Data Synchronization, Messaging. The Server: Data Synchronization, Enterprise Data Source, Messaging. Mobile Operating Systems: WinCE, Palm OS, Symbian OS, Linux. Proprietary OS Client Development: The development process, Need analysis phase, Design phase, Implementation and Testing phase, Deployment phase, Development Tools, Device Emulators **RBT: L1, L2, L3**	10 Hours
Module-4	
Building Mobile Internet Applications: Thin client: Architecture, the client, Middleware, messaging Servers, Processing a Wireless request, Wireless Applications Protocol (WAP) Overview, Wireless Languages: Markup Languages, HDML, WML, HTML, cHTML, XHTML, VoiceXML. **RBT: L1, L2, L3**	10 Hours
Module-5	

J2ME: Introduction, CDC, CLDC, MIDP; Programming for CLDC, MIDlet model, Provisioning, MIDlet life-cycle, Creating new application, MIDlet event handling, GUI in MIDP, Low level GUI Components, Multimedia APIs; Communication in MIDP, Security Considerations in MIDP. **RBT: L1, L2, L3**	**10 Hours**
Course outcomes:	
The students shall able to: • Explain state of art techniques in wireless communication. • Discover CDMA, GSM. Mobile IP, Wimax • Demonstrate program for CLDC, MIDP let model and security concerns	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books: 1. Ashok Talukder, RoopaYavagal, Hasan Ahmed: Mobile Computing, Technology, Applications and Service Creation, 2nd Edition, Tata McGraw Hill, 2010. 2. Martyn Mallik: Mobile and Wireless Design Essentials, Wiley India, 2003	
Reference Books: 1. Raj kamal: Mobile Computing, Oxford University Press, 2007. 2. ItiSahaMisra: Wireless Communications and Networks, 3G and Beyond, Tata McGraw Hill, 2009.	

COMPUTER NETWORKS AND IOT LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2018 -2019)
SEMESTER – I

Subject Code	18SCNL16	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- Demonstrate Concepts of fundamental protocols.
- Illustrate internetworking concepts.
- Implement concepts in congestion control and error detections.
- Evaluate fundamentals of Cryptography through practical implementation.
- Implement standard algorithms used to provide confidentiality, integrity and authenticity.
- Design security applications in the field of Information technology.

PART – A Computer Network LABORATORY WORK

Note:
Implement the following using C/C++ or equivalent with LINUX/Windows environment:

1. Write a program to archive Traffic management at Flow level by implementing Closed Loop Control technique. (Leaky Bucket Algorithm)
2. Write a program to implement dynamic routing strategy in finding optimal path for data transmission. (Bellman ford algorithm).
3. Write a program to implement Link State Routing (Dijkstra Algorithm).
4. Write a program for providing security for transfer of data in the network. (RSA Algorithm)
5. Write a program for encrypting 64 bit playing text using DES algorithm.
6. Apply the RSA algorithm on a text file to produce cipher text file.
7. Develop a mechanism to setup a security channel using Diffie-Hellman Key Exchange between client and server
8. Implement secure hash algorithm for Data Integrity. Implement MD5 and SHA-1 algorithm, which accepts a string input, and produce a fixed size number - 128 bits for MD5; 160 bits for SHA-1, this number is a hash of the input. Show that a small change in the input results in a substantial change in the output.

Simulation Programs using OPNET /NS2/NS3 or any other equivalent software

9. Simulate a 3 node point to point network with duplex links between them. Set the Queue size and vary the bandwidth and find the number of packets dropped.
10. Simulate a four-node point-to-point network, and connect the links as follows: n0->n2, n1->n2 and n2->n3. Apply TCP agent changing the parameters and determine the number of packets sent/received by TCP/UDP

PART – B IOT LABORATORY WORK

1. Transmit a string using UART
2. Point-to-Point communication of two Motes over the radio frequency.
3. Multi-point to single point communication of Motes over the radio frequency.LAN (Sub-netting).
4. I2C protocol study
5. Reading Temperature and Relative Humidity value from the sensor

Course Outcomes:
The students should be able to: • Apply key Internet applications and their protocols, and ability to develop their own applications (e.g. Client Server applications, Web Services) using the sockets API. • Design and evaluate application layer protocol. • Analyze the vulnerabilities in any computing system and hence be able to design a security solution. • Identify the security issues in the network and resolve it. • Evaluate security mechanisms using rigorous approaches, including theoretical.
Conduction of Practical Examination: 1. All laboratory experiments (nos) are to be included for practical examination. 2. Students are allowed to pick one experiment from **each part and execute both** 3. Strictly follow the instructions as printed on the cover page of answer script for breakup of marks 4. **Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.**

NETWORK PROGRAMMING

[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2018 -2019)
SEMESTER – II

Subject Code	18LNI14 / 18SCE333 / 18SCN22	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Define Network Programming.
- Demonstrate programming with TCP and SCTP.
- Explain key management and routing sockets.
- Evaluate advanced Socket Programming APIs.

Module 1	**Contact Hours**
Introduction to network application, client/server communication, OSI Model, BSD Networking history, Test Networks and Hosts, Unix Standards, 64-bit architectures, Transport Layer: TCP, UDP and SCTP. **RBT: L1, L2, L3**	**10 Hours**
Module 2	
Sockets Introduction – socket address structures, value-result arguments, byte ordering and manipulation functions, address conversion functions, Elementary TCP Sockets – socket, connect, bind, listen, accept , fork and concurrent server design, getsockname and getpeername functions and TCP Client/Server Example- client/server programming through TCP sockets, Normal startup, termination, POSIX signal handling, Signal handling in server, Crashing, rebooting of server host, shutdown **RBT: L1, L2, L3**	**10 Hours**
Module 3	
I/O Multiplexing and Socket Options, Elementary SCTP Sockets- Interface Models, sctp_xx functions, shutdown function, Notifications, SCTP Client/Server Examples – One-to-Many, Head–of-Line Blocking, Controlling number of streams and Termination, IPv4 and IPv6 Interoperability–different interoperability scenarios. **RBT: L1, L2, L3**	**10 Hours**
Module 4	
Daemon Processes, syslogd, daemonizing functions and the inetd super server, Advanced I/O functions- readv, writev, sendmsg and recvmsg, Ancillary data, Advanced polling, Unix domain protocols- socket address structure, functions and communication scenarios, Nonblocking I/O – connect and accept examples. **RBT: L1, L2, L3**	**10 Hours**
Module 5	
ioctl operations- socket, file, interface configuration information, ARP cache and routing table operations, Routing sockets- data link socket address structure, reading and writing, sysctl operations, interface name and index functions, Key Management functions – reading, writing, SADB, SA, Dynamically Maintaining SA's, Out-of-Band data, Threads-basic thread functions, TCP echo server using threads, Mutexes and Conditional variables. **RBT: L1, L2, L3**	**10 Hours**

Course Outcomes:

The students should be able to:

- Develop applications that communicate with each other using TCP and SCTP.

• Identify the IPv4 and IPv6 compatibility. • Evaluate socket programming APIs.
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.
Text Books: 1. W. Richard Stevens, Bill Fenner, Andrew M. Rudoff: "UNIX Network Programming". Volume 1, Third Edition, Pearson 2004.
Reference Books: 1. Barry Nance: "Network Programming in C", PHI 2002 3.Bob Quinn, Dave Shute: "Windows Socket Network Programming", Pearson 2003. 2. Richard Stevens: "UNIX Network Programming". Volume 2, Second Edition.

ADVANCES IN STORAGE AREA NETWORKS [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2018 -2019) SEMESTER – II			
Subject Code	18LNI243 / 18SCE323 / 18SCN241 / 18SCS241 / 18SIT253 / 18SSE153	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to

- Define and contrast storage centric and server centric systems
- Define metrics used for Designing storage area networks
- Illustrate RAID concepts
- Demonstrate, how data centers maintain the data with the concepts of backup mainly remote mirroring concepts for both simple and complex systems.

Module 1	Contact Hours
Introduction: Server Centric IT Architecture and its Limitations; Storage – Centric IT Architecture and its advantages. Case study: Replacing a server with Storage Networks The Data Storage and Data Access problem; The Battle for size and access. Intelligent Disk Subsystems: Architecture of Intelligent Disk Subsystems; Hard disks and Internal I/O Channels; JBOD, Storage virtualization using RAID and different RAID levels; Caching: Acceleration of Hard Disk Access; Intelligent disk subsystems, Availability of disk subsystems. **RBT: L1, L2, L3**	10 Hours
Module 2	
I/O Techniques: The Physical I/O path from the CPU to the Storage System; SCSI; Fibre Channel Protocol Stack; Fibre Channel SAN; IP Storage. Network Attached Storage. The NAS Architecture, The NAS hardware Architecture, The NAS Software Architecture, Network connectivity, NAS as a storage system. File System and NAS: Local File Systems; Network file Systems and file servers; Shared Disk file systems; Comparison of fibre Channel and NAS. **RBT: L1, L2, L3**	10 Hours
Module 3	
Storage Virtualization: Definition of Storage virtualization; Implementation Considerations; Storage virtualization on Block or file level; Storage virtualization on various levels of the storage Network; Symmetric and Asymmetric storage virtualization in the Network. **RBT: L1, L2, L3**	10 Hours
Module 4	
SAN Architecture and Hardware devices: Overview, Creating a Network for storage; SAN Hardware devices; The fibre channel switch; Host Bus Adaptors; Putting the storage in SAN; Fabric operation from a Hardware perspective. Software Components of SAN: The switch's Operating system; Device Drivers; Supporting the switch's components; Configuration options for SANs. **RBT: L1, L2, L3**	10 Hours
Module 5	
Management of Storage Network: System Management, Requirement of management System, Support by Management System, Management Interface, Standardized	10 Hours

Mechanisms, Property Mechanisms, In-band Management, Use of SNMP, CIM and WBEM, Storage Management Initiative Specification (SMI-S), CMIP and DMI, Optional Aspects of the Management of Storage Networks, Summary **RBT: L1, L2, L3**	

Course Outcomes:

The students should be able to:

- Identify the need for performance evaluation and the metrics used for it
- Apply the techniques used for data maintenance.
- Realize strong virtualization concepts
- Develop techniques for evaluating policies for LUN masking, file systems

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Ulf Troppens, Rainer Erkens and Wolfgang Muller: Storage Networks Explained, Wiley India, 2013.

Reference Books:

1. Robert Spalding: "Storage Networks The Complete Reference", Tata McGraw-Hill, 2011.
2. Marc Farley: Storage Networking Fundamentals – An Introduction to Storage Devices, Subsystems, Applications, Management, and File Systems, Cisco Press, 2005.
3. Richard Barker and Paul Massiglia: "Storage Area Network Essentials A Complete Guide to understanding and Implementing SANs", Wiley India, 2006.

WIRELESS SENSOR NETWORKS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2018 -2019)
SEMESTER – II

Subject Code	18LNI324 /18SCE251 / 18SCN251	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Explain sensor networks for various application setups.
- Demonstrate the design space and conduct trade-off analysis between performance and resources.
- Assess coverage and conduct node deployment planning.
- Devise appropriate data dissemination protocols and model links cost.
- Determine suitable medium access protocols and radio hardware.
- Illustrate sensor networks using commercial components.
- Discuss quality of service, fault-tolerance, security and other dependability requirements while coping with resource constraints.

Module -1	**Contact Hours**
Introduction, Overview and Applications of Wireless Sensor Networks Introduction, Basic overview of the Technology, Applications of Wireless Sensor Networks: Introduction, Background, Range of Applications, Examples of Category 2 WSN Applications, Examples of Category 1 WSN Applications, Another Taxonomy of WSN Technology. (Chapter 1: 1.1, 1.2, Chapter2: 2.1-2.6) **RBT: L1, L2, L3**	**10 Hours**
Module -2	
Basic Wireless Sensor Technology and Systems: Introduction, Sensor Node Technology, Sensor Taxonomy, WN Operating Environment, WN Trends, Wireless Transmission Technology and Systems: Introduction, Radio Technology Primer, Available Wireless Technologies (Chapter3: 3.1-3.5, Chapter 4: 4.1-4.3) **RBT: L1, L2, L3**	**10 Hours**
Module – 3	
MAC and Routing Protocols for Wireless Sensor Networks: Introduction, Background, Fundamentals of MAC Protocols, MAC Protocols for WSNs, Sensor-MAC case Study, IEEE 802.15.4 LR-WPANs Standard Case Study. Routing Protocols for Wireless Sensor Networks: Introduction, Background, Data Dissemination and Gathering, Routing Challenges and Design Issues in WSNs, Routing Strategies in WSNs. (Chapter 5: 5.1-5.6, Chapter 6: 6.1-6.5) **RBT: L1, L2, L3**	**10 Hours**
Module-4	
Transport Control and Middleware for Wireless Sensor Networks: Traditional Transport Control Protocols, Transport Protocol Design Issues, Examples of Existing Transport Control Protocols, Performance of Transport Control Protocols. Middleware for Wireless Sensor Networks: Introduction, WSN Middleware Principles, Middleware Architecture, Existing Middleware. (Chapter 7: 7.1-7.4, Chap. 8: 8.1-8.4) **RBT: L1, L2, L3**	**10 Hours**
Module-5	

Network Management and Operating System for Wireless Sensor Networks: Introduction, Network Management Requirements, Traditional Network Management Models, Network Management Design Issues. Operating Systems for Wireless Sensor Networks: Introduction, Operating System Design Issues, Examples of Operating Systems. (Chapter 9: 9.1-9.5, Chapter 10: 10.1-10.3) **RBT: L1, L2, L3**	**10 Hours**
Course outcomes:	
The students shall able to: • Explain existing applications of wireless sensor actuator networks • Apply in the context of wireless sensor networks and explain elements of distributed computing and network protocol design • Contrast Various hardware, software platforms that exist for sensor networks • Summarize various network level protocols for MAC, routing, time synchronization, aggregation, consensus and distributed tracking	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books: 1. KAZEM SOHRABY, DANIEL MINOLI, TAIEB ZNATI, "Wireless Sensor Networks: Technology, Protocols and Applications:, WILEY , Second Edition (Indian) , 2014	
Reference Books: 1. Ian F. Akyildiz, Mehmet Can Vuran "Wireless Sensor Networks", Wiley 2010 2. Feng Zhao & Leonidas J. Guibas, "Wireless Sensor Networks- An Information Processing Approach", Elsevier, 2007.	

MACHINE LEARNING TECHNIQUES
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2018 -2019)
SEMESTER - III

Subject Code	18LNI322 / 18SCE321 / 18SCN324 / 18SCS31 / 18SFC254 / 18SIT322 / 18SSE334	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Explain basic concepts of learning and decision trees.
- Compare and contrast neural networks and genetic algorithms
- Apply the Bayesian techniques and instant based learning
- Examine analytical learning and reinforced learning

Module -1	**Contact Hours**
INTRODUCTION, CONCEPT LEARNING AND DECISION TREES Learning Problems – Designing Learning systems, Perspectives and Issues – Concept Learning – Version Spaces and Candidate Elimination Algorithm – Inductive bias – Decision Tree learning – Representation – Algorithm – Heuristic Space Search **RBT: L1, L2, L3**	**10Hours**
Module -2	
NEURAL NETWORKS AND GENETIC ALGORITHMS: Neural Network Representation – Problems – Perceptrons – Multilayer Networks and Back Propagation Algorithms – Advanced Topics – Genetic Algorithms – Hypothesis Space Search – Genetic Programming – Models of Evolution and Learning **RBT: L1, L2, L3**	**10 Hours**
Module – 3	
BAYESIAN AND COMPUTATIONAL LEARNINGL Bayes Theorem – Concept Learning – Maximum Likelihood – Minimum Description Length Principle – Bayes Optimal Classifier – Gibbs Algorithm – Naïve Bayes Classifier– Bayesian Belief Network – EM Algorithm – Probably Learning – Sample Complexity for Finite and Infinite Hypothesis Spaces – Mistake Bound Model **RBT: L1, L2, L3**	**10 Hours**
Module-4	
INSTANT BASED LEARNING AND LEARNING SET OF RULES: K- Nearest Neighbor Learning – Locally Weighted Regression – Radial Basis Functions –Case-Based Reasoning – Sequential Covering Algorithms – Learning Rule Sets – Learning First Order Rules – Learning Sets of First Order Rules – Induction as Inverted Deduction – Inverting Resolution **RBT: L1, L2, L3**	**10 Hours**
Module-5	
ANALYTICAL LEARNING AND REINFORCED LEARNING: Perfect Domain Theories – Explanation Based Learning – Inductive-Analytical Approaches - FOCL Algorithm – Reinforcement Learning – Task – Q-Learning – Temporal Difference Learning **RBT: L1, L2, L3**	**10 Hours**
Course outcomes:	
On Completion of the course, the students will be able to	

- Choose the learning techniques with this basic knowledge.
- Apply effectively neural networks and genetic algorithms for appropriate applications.
- Apply bayesian techniques and derive effectively learning rules.
- Choose and differentiate reinforcement and analytical learning techniques

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Tom M. Mitchell, "Machine Learning", McGraw-Hill Education (INDIAN EDITION), 2013.

Reference Books:

1. EthemAlpaydin, "Introduction to Machine Learning", 2nd Ed., PHI Learning Pvt. Ltd., 2013.
2. T. Hastie, R. Tibshirani, J. H. Friedman, "The Elements of Statistical Learning", Springer; 1st edition, 2001.

ANALYSIS OF COMPUTER NETWORKS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2018 -2019)
SEMESTER - III

Subject Code	18SCN331	IA Marks	40
Number of Contact Hours/Week	04	Exam Marks	60
Total Number of Contact Hours	50	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to

- Explain with the concepts of computer networks
- What is a computer network and what are the fundamental protocols.
- Analyze network architectures in stochastic and deterministic way.
- Illustrate RSVP, Principles of TCP
- Discover more on different network protocols.
- Explain multiplexing, streaming sessions in computer network.

Module -1	**Contact Hours**
Introduction: Two examples of analysis: Efficient transport of packet voice calls, Achievable throughput in an input-queuing packet switch: the importance of quantitative modeling in the Engineering of Telecommunication Networks. **RBT: L1, L2, L3**	**10 Hours**
Module -2	
Multiplexing: Network performance and source characterization; Stream sessions in a packet network: Delay guarantees; Elastic transfers in a packet network; Packet multiplexing over Wireless networks. **RBT: L1, L2, L3**	**10 Hours**
Module – 3	
Stream Sessions: Deterministic Network Analysis: Events and processes in packet multiplexer models: Universal concepts; Deterministic traffic models and Network Calculus; Scheduling; Application to a packet voice example; Connection setup: The RSVP approach; Scheduling (continued). **RBT: L1, L2, L3**	**10 Hours**
Module-4	
Stream Sessions: Stochastic Analysis: Deterministic analysis can yield loose bounds; Stochastic traffic models; Additional notation; Performance measures; Little's theorem, Brumelle's theorem, and applications; Multiplexer analysis with stationary and ergodic traffic; The effective bandwidth approach for admission control; Application to the packet voice example; Stochastic analysis with shaped traffic; Multihop networks; Long-Range-Dependent traffic **RBT: L1, L2, L3**	**10 Hours**
Module-5	
Adaptive Bandwidth Sharing for Elastic Traffic: Elastic transfers in a Network; Network parameters and performance objectives; sharing a single link; Rate-Based Control; Window-Based Control: General Principles; TCP: The Internet's Adaptive Window Protocol; Bandwidth sharing in a Network.	**10 Hours**

RBT: L1, L2, L3
Course outcomes:
On completion, student will be able to: • List and classify network services, protocols and architectures, explain why they are layered. • Implement key Internet applications and their protocols, and will apply to develop their own applications (e.g. Client Server applications, Web Services) using the sockets API.
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.
Text Books: 1. Anurag Kumar, D. Manjunath, Joy Kuri: Communication Networking An Analytical Approach, Elsevier, 2004.
Reference Books: 1. M. Schwartz: Broadband Integrated Networks, Prentice Hall PTR, 1996. 2. J. Walrand, P. Varaiya: High Performance Communication Networks, 2nd Edition, Morgan Kaufmann, 1999

ENGINEERING MATHEMATICS-III
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – III

Subject Code	**17MAT31**	**IA Marks**	**40**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**60**
Total Number of Lecture Hours	**50**	**Exam Hours**	**03**

CREDITS – 04

Module -1	**Teaching Hours**
Fourier Series: Periodic functions, Dirichlet's condition, Fourier Series of periodic functions with period 2π and with arbitrary period 2c. Fourier series of even and odd functions. Half range Fourier Series, practical harmonic analysis-Illustrative examples from engineering field.	**10Hours**
Module -2	
Fourier Transforms: Infinite Fourier transforms, Fourier sine and cosine transforms. Inverse Fourier transform. **Z-transform:** Difference equations, basic definition, z-transform-definition, Standard z-transforms, Damping rule, Shifting rule, Initial value and final value theorems (without proof) and problems, Inverse z-transform. Applications of z-transforms to solve difference equations.	**10 Hours**
Module – 3	
Statistical Methods: Review of measures of central tendency and dispersion. Correlation-Karl Pearson's coefficient of correlation-problems. Regression analysis- lines of regression (without proof) –problems **Curve Fitting:** Curve fitting by the method of least squares- fitting of the curves of the form, $y = ax + b$, $y = ax^2 + bx + c$ and $y = ae^{bx}$. **Numerical Methods:** Numerical solution of algebraic and transcendental equations by Regula- Falsi Method and Newton-Raphson method.	**10 Hours**
Module-4	
Finite differences: Forward and backward differences, Newton's forward and backward interpolation formulae. Divided differences- Newton's divided difference formula. Lagrange's interpolation formula and inverse interpolation formula (all formulae without proof)-Problems. **Numerical integration:** Simpson's $(1/3)^{th}$ and $(3/8)^{th}$ rules, Weddle's rule (without proof) – Problems.	**10 Hours**
Module-5	
Vector integration: Line integrals-definition and problems, surface and volume integrals-definition, Green's theorem in a plane, Stokes and Gauss-divergence theorem(without proof) and problems. **Calculus of Variations:** Variation of function and Functional, variational problems. Euler's equation, Geodesics, hanging chain, problems.	**10 Hours**
Course outcomes:	

After Studying this course, students will be able to

- Know the use of periodic signals and Fourier series to analyze circuits and system communications.
- Explain the general linear system theory for continuous-time signals and digital signal processing using the Fourier Transform and z-transform.
- Employ appropriate numerical methods to solve algebraic and transcendental equations.
- Apply Green's Theorem, Divergence Theorem and Stokes' theorem in various applications in the field of electro-magnetic and gravitational fields and fluid flow problems.
- Determine the extremals of functionals and solve the simple problems of the calculus of variations.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. B. S. Grewal," Higher Engineering Mathematics", Khanna publishers, 42nd edition, 2013.
2. B.V. Ramana "Higher Engineering Mathematics" Tata McGraw-Hill, 2006.

Reference Books:

1. N. P. Bali and Manish Goyal, "A text book of Engineering mathematics" , Laxmi publications, latest edition.
2. Kreyszig, "Advanced Engineering Mathematics " - 9th edition, Wiley.
3. H. K Dass and Er. Rajnish Verma ,"Higher Engineering Mathematics", S. Chand, 1st ed.

ANALOG AND DIGITAL ELECTRONICS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER - III

Subject Code	**17CS32**	**IA Marks**	**40**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**60**
Total Number of Lecture Hours	**50**	**Exam Hours**	**03**

CREDITS – 04

Module -1	**Teaching Hours**
Field Effect Transistors: Junction Field Effect Transistors, MOSFETs, Differences between JFETs and MOSFETs, Biasing MOSFETs, FET Applications, CMOS Devices. Wave-Shaping Circuits: Integrated Circuit(IC) Multivibrators. **Introduction to Operational Amplifier**: Ideal v/s practical Opamp, Performance Parameters, **Operational Amplifier Application Circuits**:Peak Detector Circuit, Comparator, Active Filters, Non-Linear Amplifier, Relaxation Oscillator, Current-To-Voltage Converter, Voltage-To-Current Converter. **Text book 1:- Ch5: 5.2, 5.3, 5.5, 5.8, 5.9, 5.1.Ch13: 13.10.Ch 16: 16.3, 16.4. Ch 17: 7.12, 17.14, 17.15, 17.18, 17.19, 17.20, 17.21.)**	**10 Hours**
Module -2	
The Basic Gates: Review of Basic Logic gates, Positive and Negative Logic, Introduction to HDL. **Combinational Logic Circuits:** Sum-of-Products Method, Truth Table to Karnaugh Map, Pairs Quads, and Octets, Karnaugh Simplifications, Don't-care Conditions, Product-of-sums Method, Product-of-sums simplifications, Simplification by Quine-McClusky Method, Hazards and Hazard covers, HDL Implementation Models. **Text book 2:- Ch2: 2.4, 2.5. Ch3: 3.2 to 3.11.**	**10 Hours**
Module – 3	
Data-Processing Circuits: Multiplexers, Demultiplexers, 1-of-16 Decoder, BCD to Decimal Decoders, Seven Segment Decoders, Encoders, Exclusive-OR Gates, Parity Generators and Checkers, Magnitude Comparator, Programmable Array Logic, Programmable Logic Arrays, HDL Implementation of Data Processing Circuits. Arithmetic Building Blocks, Arithmetic Logic Unit **Flip- Flops:** RS Flip-Flops, Gated Flip-Flops, Edge-triggered RS FLIP-FLOP, Edge-triggered D FLIP-FLOPs, Edge-triggered JK FLIP-FLOPs. **Text book 2:- Ch 4:- 4.1 to 4.9, 4.11, 4.12, 4.14.Ch6:-6.7, 6.10.Ch8:- 8.1 to 8.5.**	**10 Hours**
Module-4	
Flip- Flops: FLIP-FLOP Timing, JK Master-slave FLIP-FLOP, Switch Contact Bounce Circuits, Various Representation of FLIP-FLOPs, HDL Implementation of FLIP-FLOP. **Registers:** Types of Registers, Serial In - Serial Out, Serial In - Parallel out, Parallel In - Serial Out, Parallel In - Parallel Out, Universal Shift Register, Applications of Shift Registers, Register implementation in HDL. **Counters:** Asynchronous Counters, Decoding Gates, Synchronous Counters, Changing the Counter Modulus. (**Text book 2:- Ch 8: 8.6, 8.8, 8.9, 8.10, 8.13. Ch 9: 9.1 to 9.8. Ch 10: 10.1 to 10.4)**	**10 Hours**

Module-5

Counters: Decade Counters, Presettable Counters, Counter Design as a Synthesis problem, A Digital Clock, Counter Design using HDL. **D/A Conversion and A/D Conversion:** Variable, Resistor Networks, Binary Ladders, D/A Converters, D/A Accuracy and Resolution, A/D Converter-Simultaneous Conversion, A/D Converter-Counter Method, Continuous A/D Conversion, A/D Techniques, Dual-slope A/D Conversion, A/D Accuracy and Resolution. **Text book 2:- Ch 10: 10.5 to 10.9. Ch 12: 12.1 to 12.10**	**10 Hours**

Course outcomes: After Studying this course, students will be able to

- Explain the operation of JFETs and MOSFETs , Operational Amplifier circuits and their application
- Explain Combinational Logic, Simplification Techniques using Karnaugh Maps, Quine McClusky technique.
- Demonstrate Operation of Decoders, Encoders, Multiplexers, Adders and Subtractors, working of Latches, Flip-Flops, Designing Registers, Counters, A/D and D/A Converters
- Design of Counters, Registers and A/D & D/A converters

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Anil K Maini, Varsha Agarwal: Electronic Devices and Circuits, Wiley, 2012.
2. Donald P Leach, Albert Paul Malvino & Goutam Saha: Digital Principles and Applications, 8th Edition, Tata McGraw Hill, 2015

Reference Books:

1. Stephen Brown, Zvonko Vranesic: Fundamentals of Digital Logic Design with VHDL, 2nd Edition, Tata McGraw Hill, 2005.
2. R D Sudhaker Samuel: Illustrative Approach to Logic Design, Sanguine-Pearson, 2010.
3. M Morris Mano: Digital Logic and Computer Design, 10th Edition, Pearson, 2008.

DATA STRUCTURES AND APPLICATIONS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER - III

Subject Code	**17CS33**	**IA Marks**	**40**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**60**
Total Number of Lecture Hours	**50**	**Exam Hours**	**03**

CREDITS - 04

Module -1	**Teaching Hours**
Introduction: Data Structures, Classifications (Primitive & Non Primitive), Data structure Operations, Review of Arrays, Structures, Self-Referential Structures, and Unions. Pointers and Dynamic Memory Allocation Functions. Representation of Linear Arrays in Memory, Dynamically allocated arrays, **Array Operations**: Traversing, inserting, deleting, searching, and sorting. Multidimensional Arrays, Polynomials and Sparse Matrices. **Strings:** Basic Terminology, Storing, Operations and Pattern Matching algorithms. Programming Examples. **Text 1: Ch 1: 1.2, Ch2: 2.2 -2.7** **Text 2: Ch 1: 1.1 -1.4, Ch 3: 3.1-3.3,3.5,3.7, Ch 4: 4.1-4.9,4.14** **Ref 3: Ch 1: 1.4**	**10 Hours**
Module -2	
Stacks and Queues **Stacks:** Definition, Stack Operations, Array Representation of Stacks, Stacks using Dynamic Arrays, Stack Applications: Polish notation, Infix to postfix conversion, evaluation of postfix expression, **Recursion** - Factorial, GCD, Fibonacci Sequence, Tower of Hanoi, Ackerman's function. **Queues:** Definition, Array Representation, Queue Operations, Circular Queues, Circular queues using Dynamic arrays, Dequeues, Priority Queues, A Mazing Problem. Multiple Stacks and Queues. Programming Examples. **Text 1: Ch3: 3.1 -3.7** **Text 2: Ch6: 6.1 -6.3, 6.5, 6.7-6.10, 6.12, 6.13**	**10 Hours**
Module – 3	
Linked Lists: Definition, Representation of linked lists in Memory, Memory allocation; Garbage Collection. Linked list operations: Traversing, Searching, Insertion, and Deletion. Doubly Linked lists, Circular linked lists, and header linked lists. Linked Stacks and Queues. Applications of Linked lists – Polynomials, Sparse matrix representation. Programming Examples **Text 1: Ch4: 4.1 -4.8 except 4.6** **Text 2: Ch5: 5.1 – 5.10**	**10 Hours**

Module-4	
Trees: Terminology, Binary Trees, Properties of Binary trees, Array and linked Representation of Binary Trees, Binary Tree Traversals - Inorder, postorder, preorder; Additional Binary tree operations. Threaded binary trees, Binary Search Trees – Definition, Insertion, Deletion, Traversal, Searching, Application of Trees-Evaluation of Expression, Programming Examples **Text 1: Ch5: 5.1 –5.5, 5.7** **Text 2: Ch7: 7.1 – 7.9**	**10 Hours**
Module-5	
Graphs: Definitions, Terminologies, Matrix and Adjacency List Representation Of Graphs, Elementary Graph operations, Traversal methods: Breadth First Search and Depth First Search. **Sorting and Searching**: Insertion Sort, Radix sort, Address Calculation Sort. **Hashing:** Hash Table organizations, Hashing Functions, Static and Dynamic Hashing. **Files and Their Organization*:*** Data Hierarchy, File Attributes, Text Files and Binary Files, Basic File Operations, File Organizations and Indexing **Text 1: Ch6: 6.1 –6.2, Ch 7:7.2, Ch 8:8.1-8.3** **Text 2: Ch8: 8.1 – 8.7, Ch 9:9.1-9.3,9.7,9.9** **Reference 2: Ch 16: 16.1 - 16.7**	**10 Hours**

Course outcomes: After studying this course, students will be able to:

- Explain different types of data structures, operations and algorithms
- Apply searching and sorting operations on files
- Make use of stack, Queue, Lists, Trees and Graphs in problem solving.
- Develop all data structures in a high-level language for problem solving.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Fundamentals of Data Structures in C - Ellis Horowitz and Sartaj Sahni, 2nd edition, Universities Press,2014
2. Data Structures - Seymour Lipschutz, Schaum's Outlines, Revised 1st edition, McGraw Hill, 2014

Reference Books:

1. Data Structures: A Pseudo-code approach with C –Gilberg & Forouzan, 2nd edition, Cengage Learning,2014
2. Data Structures using C, , Reema Thareja, 3rd edition Oxford press, 2012
3. An Introduction to Data Structures with Applications- Jean-Paul Tremblay & Paul G. Sorenson, 2nd Edition, McGraw Hill, 2013
4. Data Structures using C - A M Tenenbaum, PHI, 1989
5. Data Structures and Program Design in C - Robert Kruse, 2nd edition, PHI, 1996

COMPUTER ORGANIZATION
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER - III

Subject Code	**17CS34**	**IA Marks**	**40**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**60**
Total Number of Lecture Hours	**50**	**Exam Hours**	**03**

CREDITS – 04

Module -1	**Teaching Hours**
Basic Structure of Computers: Basic Operational Concepts, Bus Structures, Performance – Processor Clock, Basic Performance Equation, Clock Rate, Performance Measurement. Machine Instructions and Programs: Memory Location and Addresses, Memory Operations, Instructions and Instruction Sequencing, Addressing Modes, Assembly Language, Basic Input and Output Operations, Stacks and Queues, Subroutines, Additional Instructions, Encoding of Machine Instructions	**10Hours**
Module -2	
Input/Output Organization: Accessing I/O Devices, Interrupts – Interrupt Hardware, Enabling and Disabling Interrupts, Handling Multiple Devices, Controlling Device Requests, Exceptions, Direct Memory Access, Buses Interface Circuits, Standard I/O Interfaces – PCI Bus, SCSI Bus, USB.	**10 Hours**
Module – 3	
Memory System: Basic Concepts, Semiconductor RAM Memories, Read Only Memories, Speed, Size, and Cost, Cache Memories – Mapping Functions, Replacement Algorithms, Performance Considerations, Virtual Memories, Secondary Storage.	**10 Hours**
Module-4	
Arithmetic: Numbers, Arithmetic Operations and Characters, Addition and Subtraction of Signed Numbers, Design of Fast Adders, Multiplication of Positive Numbers, Signed Operand Multiplication, Fast Multiplication, Integer Division, Floating-point Numbers and Operations.	**10 Hours**
Module-5	
Basic Processing Unit: Some Fundamental Concepts, Execution of a Complete Instruction, Multiple Bus Organization, Hard-wired Control, Micro programmed Control. Pipelining, Embedded Systems and Large Computer Systems: Basic Concepts of pipelining, Examples of Embedded Systems, Processor chips for embedded applications, Simple Microcontroller, The structure of General-Purpose Multiprocessors.	**10 Hours**

Course outcomes: After studying this course, students will be able to:

- Explain the basic organization of a computer system.
- Demonstrate functioning of different sub systems, such as processor, Input/output, and memory.
- Illustrate hardwired control and micro programmed control. pipelining, embedded and other computing systems.
- Build simple arithmetic and logical units.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Carl Hamacher, Zvonko Vranesic, Safwat Zaky: Computer Organization, 5th Edition, Tata McGraw Hill, 2002. (Listed topics only from Chapters 1, 2, 4, 5, 6, 7, 8, 9 and12)

Reference Books:

1. William Stallings: Computer Organization & Architecture, 9th Edition, Pearson, 2015.

UNIX AND SHELL PROGRAMMING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – III

Subject Code	**17CS35**	**IA Marks**	**40**
Number of Lecture Hours/Week	**03**	**Exam Marks**	**60**
Total Number of Lecture Hours	**40**	**Exam Hours**	**03**

CREDITS – 03

Module -1	**Teaching Hours**
Introduction, Brief history. Unix Components/Architecture. Features of Unix. The UNIX Environment and UNIX Structure, Posix and Single Unix specification. The login prompt. General features of Unix commands/ command structure. Command arguments and options. Understanding of some basic commands such as echo, printf, ls, who, date, passwd, cal, Combining commands. Meaning of Internal and external commands. The type command: knowing the type of a command and locating it. The man command knowing more about Unix commands and using Unix online manual pages. The man with keyword option and whatis. The more command and using it with other commands. Knowing the user terminal, displaying its characteristics and setting characteristics. Managing the non-uniform behaviour of terminals and keyboards. The root login. Becoming the super user: su command. The /etc/passwd and /etc/shadow files. Commands to add, modify and delete users. **Topics from chapter 2 , 3 and 15 of text book 1,chapter 1 from text book 2**	**08 Hours**
Module -2	
Unix files. Naming files. Basic file types/categories. Organization of files. Hidden files. Standard directories. Parent child relationship. The home directory and the HOME variable. Reaching required files- the PATH variable, manipulating the PATH, Relative and absolute pathnames. Directory commands – pwd, cd, mkdir, rmdir commands. The dot (.) and double dots (..) notations to represent present and parent directories and their usage in relative path names. File related commands – cat, mv, rm, cp, wc and od commands. File attributes and permissions and knowing them. The ls command with options. Changing file permissions: the relative and absolute permissions changing methods. Recursively changing file permissions. Directory permissions. **Topics from chapters 4, 5 and 6 of text book 1**	**08 Hours**
Module – 3	
The vi editor. Basics. The .exrc file. Different ways of invoking and quitting vi. Different modes of vi. Input mode commands. Command mode commands. The ex mode commands. Illustrative examples Navigation commands. Repeat command. Pattern searching. The search and replace command. The set, map and abbr commands. Simple examples using these commands. The shells interpretive cycle. Wild cards and file name generation. Removing the special meanings of wild cards. Three standard files and redirection. Connecting commands: Pipe. Splitting the output: tee. Command substitution. Basic and Extended regular expressions. The grep, egrep. Typical examples involving different regular expressions. **Topics from chapters 7, 8 and 13 of text book 1. Topics from chapter 2 and 9 ,10 of text book 2**	**08 Hours**

Module-4	
Shell programming. Ordinary and environment variables. The .profile. Read and readonly commands. Command line arguments. exit and exit status of a command. Logical operators for conditional execution. The test command and its shortcut. The if, while, for and case control statements. The set and shift commands and handling positional parameters. The here (<<) document and trap command. Simple shell program examples. File inodes and the inode structure. File links – hard and soft links. Filters. Head and tail commands. Cut and paste commands. The sort command and its usage with different options. The umask and default file permissions. Two special files /dev/null and /dev/tty. **Topics from chapter 11, 12, 14 of text book 1,chapter 17 from text book2**	**08 Hours**
Module-5	
Meaning of a process. Mechanism of process creation. Parent and child process. The ps command with its options. Executing a command at a specified point of time: at command. Executing a command periodically: cron command and the crontab file.. Signals. The nice and nohup commands. Background processes. The bg and fg command. The kill command. The find command with illustrative example. Structure of a perl script. Running a perl script. Variables and operators. String handling functions. Default variables - $_ and $. – representing the current line and current line number. The range operator. Chop() and chomp() functions. Lists and arrays. The @- variable. The splice operator, push(), pop(), split() and join(). File handles and handling file – using open(), close() and die () functions.. Associative arrays – keys and value functions. Overview of decision making loop control structures – the foreach. Regular expressions – simple and multiple search patterns. The match and substitute operators. Defining and using subroutines. **Topics from chapter 9 and 19 of text book 1. Topics from chapter 11 of reference book 1**	**08 Hours**
Course outcomes:	
After studying this course, students will be able to: • Explain UNIX system and use different commands. • Compile Shell scripts for certain functions on different subsystems. • Demonstrate use of editors and Perl script writing	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books: 1. Sumitabha Das., Unix Concepts and Applications., 4th Edition., Tata McGraw Hill **2.** Behrouz A. Forouzan, Richard F. Gilberg : UNIX and Shell Programming- Cengage Learning – India Edition. 2009.	
Reference Books: **1.** M.G. Venkatesh Murthy: UNIX & Shell Programming**,** Pearson Education. **2.** Richard Blum , Christine Bresnahan : Linux Command Line and Shell Scripting Bible, 2nd Edition , Wiley,2014.	

DISCRETE MATHEMATICAL STRUCTURES
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – III

Subject Code	**17CS36**	**IA Marks**	**40**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**60**
Total Number of Lecture Hours	**50**	**Exam Hours**	**03**

CREDITS – 04

Module -1	**Teaching Hours**
Fundamentals of Logic: Basic Connectives and Truth Tables, Logic Equivalence – The Laws of Logic, Logical Implication – Rules of Inference. Fundamentals of Logic contd.: The Use of Quantifiers, Quantifiers, Definitions and the Proofs of Theorems,	**10Hours**
Module -2	
Properties of the Integers: Mathematical Induction, The Well Ordering Principle – Mathematical Induction, Recursive Definitions. Principles of Counting. **Fundamental Principles of Counting:** The Rules of Sum and Product, Permutations, Combinations – The Binomial Theorem, Combinations with Repetition,.	**10 Hours**
Module – 3	
Relations and Functions: Cartesian Products and Relations, Functions – Plain and One-to-One, Onto Functions. The Pigeon-hole Principle, Function Composition and Inverse Functions. Properties of Relations, Computer Recognition – Zero-One Matrices and Directed Graphs, Partial Orders – Hasse Diagrams, Equivalence Relations and Partitions.	**10 Hours**
Module-4	
The Principle of Inclusion and Exclusion: The Principle of Inclusion and Exclusion, Generalizations of the Principle, Derangements – Nothing is in its Right Place, Rook Polynomials. **Recurrence Relations:** First Order Linear Recurrence Relation, The Second Order Linear Homogeneous Recurrence Relation with Constant Coefficients,	**10 Hours**
Module-5	
Introduction to Graph Theory: Definitions and Examples, Sub graphs, Complements, and Graph Isomorphism, Vertex Degree, Euler Trails and Circuits , **Trees**: Definitions, Properties, and Examples, Routed Trees, Trees and Sorting, Weighted Trees and Prefix Codes	**10 Hours**

Course outcomes: After studying this course, students will be able to:

- Make use of propositional and predicate logic in knowledge representation and truth verification.
- Demonstrate the application of discrete structures in different fields of computer science.
- Solve problems using recurrence relations and generating functions.
- Apply different mathematical proofs, techniques in proving theorems.
- Compare graphs, trees and their applications.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Ralph P. Grimaldi: Discrete and Combinatorial Mathematics, , 5th Edition, Pearson Education. 2004. (Chapter 3.1, 3.2, 3.3, 3.4, Appendix 3, Chapter 2, Chapter 4.1, 4.2, Chapter 5.1 to 5.6, Chapter 7.1 to 7.4, Chapter 16.1, 16.2, 16.3, 16.5 to 16.9, and Chapter 14.1, 14.2, 14.3).

Reference Books:

1. Basavaraj S Anami and Venakanna S Madalli: Discrete Mathematics – A Concept based approach, Universities Press, 2016
2. Kenneth H. Rosen: Discrete Mathematics and its Applications, 6th Edition, McGraw Hill, 2007.
3. Jayant Ganguly: A Treatise on Discrete Mathematical Structures, Sanguine-Pearson, 2010.
4. D.S. Malik and M.K. Sen: Discrete Mathematical Structures: Theory and Applications, Thomson, 2004.
5. Thomas Koshy: Discrete Mathematics with Applications, Elsevier, 2005, Reprint 2008.

ANALOG AND DIGITAL ELECTRONICS LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER - III

Laboratory Code	**17CSL37**	**IA Marks**	**40**
Number of Lecture Hours/Week	**01I + 02P**	**Exam Marks**	**60**
Total Number of Lecture Hours	**40**	**Exam Hours**	**03**

CREDITS – 02

Descriptions (if any)

Any simulation package like MultiSim / P-spice /Equivalent software may be used.
Faculty-in-charge should demonstrate and explain the required hardware components and their functional Block diagrams, timing diagrams etc. Students have to prepare a write-up on the same and include it in the Lab record and to be evaluated.

Laboratory Session-1: Write-upon analog components; functional block diagram, Pin diagram (if any), waveforms and description. The same information is also taught in theory class; this helps the students to understand better.

Laboratory Session-2: Write-upon Logic design components, pin diagram (if any), Timing diagrams, etc. The same information is also taught in theory class; this helps the students to understand better.

Note: These **TWO Laboratory sessions** are used to fill the gap between theory classes and practical sessions. Both sessions are to be evaluated for 40 marks as lab experiments.

Laboratory Experiments:

1. a) Design and construct a Schmitt trigger using Op-Amp for given UTP and LTP values and demonstrate its working.
   b) Design and implement a Schmitt trigger using Op-Amp using a simulation package for two sets of UTP and LTP values and demonstrate its working.

2. a) Design and construct a rectangular waveform generator (Op-Amp relaxation oscillator) for given frequency and demonstrate its working.
   b) Design and implement a rectangular waveform generator (Op-Amp relaxation oscillator) using a simulation package and demonstrate the change in frequency when all resistor values are doubled.

3. Design and implement an Astable multivibrator circuit using 555 timer for a given frequency and duty cycle.

NOTE: hardware and software results need to be compared

4. Design and implement Half adder, Full Adder, Half Subtractor, Full Subtractor using basic gates.

5. a) Given a 4-variable logic expression, simplify it using Entered Variable Map and realize the simplified logic expression using 8:1 multiplexer IC.
   b) Design and develop the Verilog /VHDL code for an 8:1 multiplexer. Simulate and verify its working.

6. a) Design and implement code converter I)Binary to Gray (II) Gray to Binary Code using basic gates.

7. Design and verify the Truth Table of 3-bit Parity Generator and 4-bit Parity Checker using basic Logic Gates with an even parity bit.

8. a) Realize a J-K Master / Slave Flip-Flop using NAND gates and verify its truth table.

b) Design and develop the Verilog / VHDL code for D Flip-Flop with positive-edge triggering. Simulate and verify it's working.

9. a) Design and implement a mod-n (n<8) synchronous up counter using J-K Flip-Flop ICs and demonstrate its working.

b) Design and develop the Verilog / VHDL code for mod-8 up counter. Simulate and verify it's working.

10. Design and implement an asynchronous counter using decade counter IC to count up from 0 to n (n<=9) and demonstrate on 7-segment display (using IC- 7447).

11. Generate a Ramp output waveform using DAC0800 (Inputs are given to DAC through IC74393 dual 4-bit binary counter).

Study experiment

12. To study 4-bitALU using IC-74181.

Course outcomes:

On the completion of this laboratory course, the students will be able to:

- Demonstrate various Electronic Devices like Cathode ray Oscilloscope, Signal generators, Digital Trainer Kit, Multimeters and components like Resistors, Capacitors, Op amp and Integrated Circuit.
- Design and demonstrate various combinational logic circuits.
- Design and demonstrate various types of counters and Registers using Flip-flops
- Make use of simulation package to design circuits.
- Infer the working and implementation of ALU.

Conduction of Practical Examination:

1. All laboratory experiments (1 to 11 nos) are to be included for practical examination.
2. Students are allowed to pick one experiment from the lot.
3. Strictly follow the instructions as printed on the cover page of answer script.
4. Marks distribution:
   a) For questions having part a only- Procedure + Conduction + Viva:**15 + 70 +15 =100 Marks**
   b) For questions having part a and b
      Part a- Procedure + Conduction + Viva:**09 + 42 +09= 60 Marks**
      Part b- Procedure + Conduction + Viva:**06 + 28 +06= 40 Marks**
5. **Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.**

DATA STRUCTURES LABORATORY
[As per Choice Based Credit System (CBCS) scheme]

(Effective from the academic year 2017 -2018) SEMESTER - III			
Laboratory Code	**17CSL38**	**IA Marks**	**40**
Number of Lecture Hours/Week	**01I + 02P**	**Exam Marks**	**60**
Total Number of Lecture Hours	**40**	**Exam Hours**	**03**
CREDITS - 02			

Descriptions (if any)

Implement all the experiments in C Language under Linux / Windows environment.

Laboratory Experiments:

1. Design, Develop and Implement a menu driven Program in C for the following **Array** operations
    a. Creating an Array of **N** Integer Elements
    b. Display of Array Elements with Suitable Headings
    c. Inserting an Element (**ELEM**) at a given valid Position (**POS**)
    d. Deleting an Element at a given valid Position(**POS**)
    e. Exit.

    Support the program with functions for each of the above operations.

2. Design, Develop and Implement a Program in C for the following operationson **Strings**
    a. Read a main String (**STR),** a Pattern String (**PAT**) and a Replace String (**REP**)
    b. Perform Pattern Matching Operation: Find and Replace all occurrences of **PAT** in **STR** with **REP** if **PAT** exists in **STR.** Report suitable messages in case **PAT** does not exist in **STR**

    Support the program with functions for each of the above operations. Don't use Built-in functions.

3. Design, Develop and Implement a menu driven Program in C for the following operations on **STACK** of Integers (Array Implementation of Stack with maximum size **MAX**)
    a. ***Push*** an Element on to Stack
    b. ***Pop*** an Element from Stack
    c. Demonstrate how Stack can be used to check ***Palindrome***
    d. Demonstrate ***Overflow*** and ***Underflow*** situations on Stack
    e. Display the status of Stack
    f. Exit

    Support the program with appropriate functions for each of the above operations

4. Design, Develop and Implement a Program in C for converting an Infix Expression to Postfix Expression. Program should support for both parenthesized and free parenthesized expressions with the operators: **+, -, *, /, %(Remainder), ^(Power)** and **alphanumeric** operands**.**

5. Design, Develop and Implement a Program in C for the following Stack Applications
    a. Evaluation of **Suffix expression** with single digit operands and operators: **+, -, *, /, %, ^**
    b. Solving **Tower of Hanoi** problem with **n** disks

6. Design, Develop and Implement a menu driven Program in C for the following operations on **Circular QUEUE** of Characters (Array Implementation of Queue with maximum size **MAX**)
    a. Insert an Element on to Circular QUEUE
    b. Delete an Element from Circular QUEUE
    c. Demonstrate ***Overflow*** and ***Underflow*** situations on Circular QUEUE
    d. Display the status of Circular QUEUE
    e. Exit

    Support the program with appropriate functions for each of the above operations

7. Design, Develop and Implement a menu driven Program in C for the following operations on **Singly Linked List (SLL)** of Student Data with the fields: ***USN, Name, Branch, Sem, PhNo***
    a. Create a **SLL** of **N** Students Data by using ***front insertion***.
    b. Display the status of **SLL** and count the number of nodes in it
    c. Perform Insertion / Deletion at End of **SLL**
    d. Perform Insertion / Deletion at Front of **SLL(Demonstration of stack)**
    e. Exit

8. Design, Develop and Implement a menu driven Program in C for the following operations on **Doubly Linked List (DLL)** of Employee Data with the fields: ***SSN, Name, Dept, Designation, Sal, PhNo***
    a. Create a **DLL** of **N** Employees Data by using ***end insertion***.
    b. Display the status of **DLL** and count the number of nodes in it
    c. Perform Insertion and Deletion at End of **DLL**
    d. Perform Insertion and Deletion at Front of **DLL**
    e. Demonstrate how this **DLL** can be used as **Double Ended Queue**
    f. Exit

9. Design, Develop and Implement a Program in C for the following operationson **Singly Circular Linked List (SCLL)** with header nodes
    a. Represent and Evaluate a Polynomial **P(x,y,z) = $6x^2y^2z-4yz^5+3x^3yz+2xy^5z-2xyz^3$**
    b. Find the sum of two polynomials **POLY1(x,y,z)** and **POLY2(x,y,z)** and store the result in **POLYSUM(x,y,z)**

    Support the program with appropriate functions for each of the above operations
10. Design, Develop and Implement a menu driven Program in C for the following operations on **Binary Search Tree (BST)** of Integers
    a. Create a BST of **N** Integers: 6, 9, 5, 2, 8, 15, 24, 14, 7, 8, 5, 2
    b. Traverse the BST in Inorder, Preorder and Post Order
    c. Search the BST for a given element (**KEY**) and report the appropriate message
    e. Exit

11. Design, Develop and Implement a Program in C for the following operations on **Graph(G)** of Cities
    a. Create a Graph of **N** cities using Adjacency Matrix.
    b. Print all the nodes **reachable** from a given starting node in a digraph using DFS/**BFS** method

12. Given a File of **N** employee records with a set **K** of Keys(4-digit) which uniquely determine the records in file **F**. Assume that file **F** is maintained in memory by a Hash Table(HT) of **m** memory locations with **L** as the set of memory addresses (2-digit) of locations in HT. Let the keys in **K** and addresses in **L** are Integers. Design and develop a Program in C that uses Hash function **H: K →L** as H(**K**)=**K** mod **m (remainder** method), and implement hashing technique to map a given key **K** to the address space **L.** Resolve the collision (if any) using **linear probing**.

Course outcomes:

On the completion of this laboratory course, the students will be able to:

- Analyze and Compare various linear and non-linear data structures
- Demonstrate the working nature of different types of data structures and their applications
- Develop, analyze and evaluate the searching and sorting algorithms
- Choose the appropriate data structure for solving real world problems

Conduction of Practical Examination:

1. All laboratory experiments (**TWELVE** nos) are to be included for practical examination.
2. Students are allowed to pick one experiment from the lot.
3. Strictly follow the instructions as printed on the cover page of answer script
4. Marks distribution: Procedure + Conduction + Viva:**15 + 70 +15 (100)**
5. **Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.**

ENGINEERING MATHEMATICS-IV
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – IV

Subject Code	17MAT41	IA Marks	40
Number of Lecture Hours/Week	04	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module 1	**Teaching Hours**
Numerical Methods: Numerical solution of ordinary differential equations of first order and first degree, Taylor's series method, modified Euler's method. Runge - Kutta method of fourth order, Milne's and Adams-Bashforth predictor and corrector methods (No derivations of formulae-single step computation only).	**10 Hours**
Module 2	
Numerical Methods: Numerical solution of second order ordinary differential equations, Runge-Kutta method and Milne's method. (No derivations of formulae-single step computation only). **Special Functions:** Series solution of Bessel's differential equation leading to $J_n(x)$-Bessel's function of first kind. Basic properties and orthogonality. Series solution of Legendre's differential equation leading to $P_n(x)$-Legendre polynomials. Rodrigue's formula, problems	**10 Hours**
Module 3	
Complex Variables: Review of a function of a complex variable, limits, continuity, differentiability. Analytic functions-Cauchy-Riemann equations in cartesian and polar forms. Properties and construction of analytic functions. Complex line integrals-Cauchy's theorem and Cauchy's integral formula, Residue, poles, Cauchy's Residue theorem (without proof) and problems. **Transformations:** Conformal transformations-Discussion of transformations: $w = z^2$, $w = e^z$, $w = z + (1/z)$ $(z \neq 0)$, Bilinear transformations-problems.	**10 Hours**
Module 4	
Probability Distributions: Random variables (discrete and continuous), probability functions. Poisson distributions**,** geometric distribution, uniform distribution, exponential and normal distributions, Problems. **Joint probability distribution:** Joint Probability distribution for two variables, expectation, covariance, correlation coefficient.	**10 Hours**
Module 5	
Sampling Theory: Sampling, Sampling distributions, standard error, test of hypothesis for means and proportions, confidence limits for means, student's t-distribution, Chi-square distribution as a test of goodness of fit. **Stochastic process:** Stochastic process, probability vector, stochastic matrices, fixed points, regular stochastic matrices, Markov chains, higher transition probability.	**10 Hours**

Course Outcomes: After studying this course, students will be able to:

- Solve first and second order ordinary differential equation arising in flow problems using single step and multistep numerical methods.
- Illustrate problems of potential theory, quantum mechanics and heat conduction by employing notions and properties of Bessel's functions and Legendre's polynomials.
- Explain the concepts of analytic functions, residues, poles of complex potentials and describe

conformal and Bilinear transformation arising in field theory and signal processing.

- Develop probability distribution of discrete, continuous random variables and joint probability distribution occurring in digital signal processing, information theory and design engineering.
- Demonstrate testing of hypothesis of sampling distributions and illustrate examples of Markov chains related to discrete parameter stochastic process.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. B.V.Ramana "Higher Engineering Mathematics" Tata McGraw-Hill, 2006.
2. B. S. Grewal," Higher Engineering Mathematics", Khanna publishers, 42nd edition, 2013.

Reference Books:

1. N P Bali and Manish Goyal, "A text book of Engineering mathematics" , Laxmi publications, latest edition.
2. Kreyszig, "Advanced Engineering Mathematics " - 9th edition, Wiley, 2013.
3. H. K Dass and Er. RajnishVerma, "Higher Engineering Mathematics", S. Chand, 1st ed, 2011.

OBJECT ORIENTED CONCEPTS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – IV

Subject Code	17CS42	IA Marks	40
Number of Lecture Hours/Week	03	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module 1	**Teaching Hours**
Introduction to Object Oriented Concepts: A Review of structures, Procedure–Oriented Programming system, Object Oriented Programming System, Comparison of Object Oriented Language with C, Console I/O, variables and reference variables, Function Prototyping, Function Overloading. **Class and Objects:** Introduction, member functions and data, objects and functions, objects and arrays, Namespaces, Nested classes, Constructors, Destructors. **Text book 1: Ch 1: 1.1 to 1.9 Ch 2: 2.1 to 2.6 Ch 4: 4.1 to 4.2**	**08 Hours**
Module 2	
Introduction to Java: Java's magic: the Byte code; Java Development Kit (JDK); the Java Buzzwords, Object-oriented programming; Simple Java programs. Data types, variables and arrays, Operators, Control Statements. **Text book 2: Ch:1 Ch: 2 Ch:3 Ch:4 Ch:5**	**08 Hours**
Module 3	
Classes, Inheritance, Exceptions, Packages and Interfaces: Classes: Classes fundamentals; Declaring objects; Constructors, this keyword, garbage collection. **Inheritance:** inheritance basics, using super, creating multi level hierarchy, method overriding. **Exception handling:** Exception handling in Java. Packages, Access Protection, Importing Packages, Interfaces. **Text book 2: Ch:6 Ch: 8 Ch:9 Ch:10**	**08 Hours**
Module 4	
Multi Threaded Programming, Event Handling: Multi Threaded Programming: What are threads? How to make the classes threadable ; Extending threads; Implementing runnable; Synchronization; Changing state of the thread; Bounded buffer problems, read-write problem, producer consumer problems. **Event Handling:** Two event handling mechanisms; The delegation event model; Event classes; Sources of events; Event listener interfaces; Using the delegation event model; Adapter classes; Inner classes. **Text book 2: Ch 11: Ch: 22**	**08 Hours**
Module 5	
The Applet Class: Introduction, Two types of Applets; Applet basics; Applet Architecture; An Applet skeleton; Simple Applet display methods; Requesting repainting; Using the Status Window; The HTML APPLET tag; Passing parameters to Applets; getDocumentbase() and getCodebase(); ApletContext and showDocument(); The AudioClip Interface; The AppletStub Interface;Output to the Console. **Swings:** Swings: The origins of Swing; Two key Swing features; Components and Containers; The Swing Packages; A simple Swing Application; Create a Swing Applet; Jlabel and ImageIcon; JTextField;The Swing Buttons; JTabbedpane; JScrollPane; JList; JComboBox; JTable. **Text book 2: Ch 21: Ch: 29 Ch: 30**	**08 Hours**

Course Outcomes: After studying this course, students will be able to

- Explain the object-oriented concepts and JAVA.
- Develop computer programs to solve real world problems in Java.
- Develop simple GUI int erfaces for a computer program to interact with users, and to **comprehend** the event-based GUI handling principles using Applets and swings.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Sourav Sahay, Object Oriented Programming with C++ , 2nd Ed, Oxford University Press,2006
   (Chapters 1, 2, 4)
2. Herbert Schildt, Java The Complete Reference, 7th Edition, Tata McGraw Hill, 2007.
   (Chapters 1, 2, 3, 4, 5, 6, 8, 9,10, 11, 21, 22, 29, 30)

Reference Book:

1. Mahesh Bhave and Sunil Patekar, "Programming with Java", First Edition, Pearson Education,2008, ISBN:9788131720806
2. Herbert Schildt, The Complete Reference C++, 4th Edition, Tata McGraw Hill, 2003.
3. Stanley B.Lippmann, Josee Lajore, C++ Primer, 4th Edition, Pearson Education, 2005.
4. Rajkumar Buyya,S Thamarasi selvi, xingchen chu, Object oriented Programming with java, Tata McGraw Hill education private limited.
5. Richard A Johnson, Introduction to Java Programming and OOAD, CENGAGE Learning.
6. E Balagurusamy, Programming with Java A primer, Tata McGraw Hill companies.

Note: Every institute shall organize a bridge organize on C++ either in the vacation or in the beginning of even semester.

DESIGN AND ANALYSIS OF ALGORITHMS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – IV

Subject Code	17CS43	IA Marks	40
Number of Lecture Hours/Week	04	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module 1	**Teaching Hours**
Introduction: What is an Algorithm? **(T2:1.1),** Algorithm Specification **(T2:1.2),** Analysis Framework **(T1:2.1), Performance Analysis**: Space complexity, Time complexity **(T2:1.3)**. **Asymptotic Notations:** Big-Oh notation (O), Omega notation (Ω), Theta notation (Θ), and Little-oh notation (o), Mathematical analysis of Non-Recursive and recursive Algorithms with Examples **(T1:2.2, 2.3, 2.4). Important Problem Types:** Sorting, Searching, String processing, Graph Problems, Combinatorial Problems. **Fundamental Data Structures:** Stacks, Queues, Graphs, Trees, Sets and Dictionaries. **(T1:1.3,1.4)**	**10 Hours**
Module 2	
Divide and Conquer: General method, Binary search, Recurrence equation for divide and conquer, Finding the maximum and minimum **(T2:3.1, 3.3, 3.4)**, Merge sort, Quick sort **(T1:4.1, 4.2)**, Strassen's matrix multiplication **(T2:3.8)**, Advantages and Disadvantages of divide and conquer. **Decrease and Conquer Approach:** Topological Sort. **(T1:5.3)**	**10 Hours**
Module 3	
Greedy Method: General method, Coin Change Problem, Knapsack Problem, Job sequencing with deadlines **(T2:4.1, 4.3, 4.5). Minimum cost spanning trees:** Prim's Algorithm, Kruskal's Algorithm **(T1:9.1, 9.2)**. **Single source shortest paths:** Dijkstra's Algorithm **(T1:9.3)**. **Optimal Tree problem:** Huffman Trees and Codes **(T1:9.4)**. **Transform and Conquer Approach:** Heaps and Heap Sort **(T1:6.4)**.	**10 Hours**
Module 4	
Dynamic Programming: General method with Examples, Multistage Graphs **(T2:5.1, 5.2)**. **Transitive Closure:** Warshall's Algorithm, **All Pairs Shortest Paths:** Floyd's Algorithm, Optimal Binary Search Trees, Knapsack problem **((T1:8.2, 8.3, 8.4)**, Bellman-Ford Algorithm **(T2:5.4)**, Travelling Sales Person problem **(T2:5.9)**, Reliability design **(T2:5.8)**.	**10 Hours**
Module 5	
Backtracking: General method **(T2:7.1)**, N-Queens problem **(T1:12.1)**, Sum of subsets problem **(T1:12.1)**, Graph coloring **(T2:7.4)**, Hamiltonian cycles **(T2:7.5)**. **Branch and Bound:** Assignment Problem, Travelling Sales Person problem **(T1:12.2)**, **0/1 Knapsack problem (T2:8.2, T1:12.2):** LC Branch and Bound solution **(T2:8.2)**, FIFO Branch and Bound solution **(T2:8.2)**. **NP-Complete and NP-Hard problems:** Basic concepts, non-deterministic algorithms, P, NP, NP-Complete, and NP-Hard classes **(T2:11.1)**.	**10 Hours**

Course Outcomes: After studying this course, students will be able to

- Describe computational solution to well known problems like searching, sorting etc.
- Estimate the computational complexity of different algorithms.

- Develop an algorithm using appropriate design strategies for problem solving.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

T1. Introduction to the Design and Analysis of Algorithms, Anany Levitin:, 2rd Edition, 2009. Pearson.
T2. Computer Algorithms/C++, Ellis Horowitz, Satraj Sahni and Rajasekaran, 2nd Edition, 2014, Universities Press

Reference Books:

1. Introduction to Algorithms, Thomas H. Cormen, Charles E. Leiserson, Ronal L. Rivest, Clifford Stein, 3rd Edition, PHI
2. Design and Analysis of Algorithms , S. Sridhar, Oxford (Higher Education)

MICROPROCESSORS AND MICROCONTROLLERS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – IV

Subject Code	17CS44	IA Marks	40
Number of Lecture Hours/Week	04	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module 1	**Teaching Hours**
The x86 microprocessor: Brief history of the x86 family, Inside the 8088/86, Introduction to assembly programming, Introduction to Program Segments, The Stack, Flag register, x86 Addressing Modes. **Assembly language programming:** Directives & a Sample Program, Assemble, Link & Run a program, More Sample programs, Control Transfer Instructions, Data Types and Data Definition, Full Segment Definition, Flowcharts and Pseudo code. **Text book 1: Ch 1: 1.1 to 1.7, Ch 2: 2.1 to 2.7**	**10 Hours**
Module 2	
x86: Instructions sets description**, Arithmetic and logic instructions and programs:** Unsigned Addition and Subtraction, Unsigned Multiplication and Division, Logic Instructions, BCD and ASCII conversion, Rotate Instructions. **INT 21H and INT 10H Programming :** Bios INT 10H Programming , DOS Interrupt 21H. 8088/86 Interrupts, x86 PC and Interrupt Assignment. **Text book 1: Ch 3: 3.1 to 3.5, Ch 4: 4.1 , 4.2 Chapter 14: 14.1 and 14.2**	**10 Hours**
Module 3	
Signed Numbers and Strings: Signed number Arithmetic Operations, String operations. **Memory and Memory interfacing: M**emory address decoding, data integrity in RAM and ROM, 16-bit memory interfacing. **8255 I/O programming:** I/O addresses MAP of x86 PC's, programming and interfacing the 8255. **Text book 1: Ch 6: 6.1, 6.2. Ch 10: 10.2, 10.4, 10.5. Ch 11: 11.1 to 11.4**	**10 Hours**
Module 4	
Microprocessors versus Microcontrollers, **ARM Embedded Systems :**The RISC design philosophy, The ARM Design Philosophy, Embedded System Hardware, Embedded System Software, **ARM Processor Fundamentals :** Registers , Current Program Status Register , Pipeline, Exceptions, Interrupts, and the Vector Table , Core Extensions **Text book 2:Ch 1:1.1 to 1.4, Ch 2:2.1 to 2.5**	**10 Hours**
Module 5	
Introduction to the ARM Instruction Set : Data Processing Instructions , Branch Instructions, Software Interrupt Instructions, Program Status Register Instructions, Coprocessor Instructions, Loading Constants, Simple programming exercises. **Text book 2: Ch 3:3.1 to 3.6 (Excluding 3.5.2)**	**10 Hours**

Course Outcomes: After studying this course, students will be able to

- Differentiate between microprocessors and microcontrollers
- Develop assembly language code to solve problems
- Explain interfacing of various devices to x86 family and ARM processor
- Demonstrate interrupt routines for interfacing devices

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Muhammad Ali Mazidi, Janice Gillispie Mazidi, Danny Causey, The x86 PC Assembly Language Design and Interfacing, 5th Edition, Pearson, 2013.
2. **ARM system developers guide**, Andrew N Sloss, Dominic Symes and Chris Wright, Elsevier,Morgan Kaufman publishers, 2008.

Reference Books:

1. Douglas V. Hall: Microprocessors and Interfacing, Revised 2nd Edition, TMH, 2006.
2. K. Udaya Kumar & B.S. Umashankar : Advanced Microprocessors & IBM-PC Assembly Language Programming, TMH 2003.
3. Ayala : The 8086 Microprocessor: programming and interfacing - 1st edition, Cengage Learning
4. The Definitive Guide to the ARM Cortex-M3, by Joseph Yiu, 2nd Edition , Newnes, 2009
5. The Insider's Guide to the ARM7 based microcontrollers, Hitex Ltd.,1st edition, 2005
6. ARM System-on-Chip Architecture, Steve Furber, Second Edition, Pearson, 2015
7. Architecture, Programming and Interfacing of Low power Processors- ARM7, Cortex-M and MSP430, Lyla B Das Cengage Learning, 1st Edition

SOFTWARE ENGINEERING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – IV

Subject Code	17CS45	IA Marks	40
Number of Lecture Hours/Week	04	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module 1	**Teaching Hours**
Introduction: Software Crisis, Need for Software Engineering. Professional Software Development, Software Engineering Ethics. Case Studies. **Software Processes**: Models: Waterfall Model **(Sec 2.1.1)**, Incremental Model **(Sec 2.1.2)** and Spiral Model **(Sec 2.1.3)**. Process activities. **Requirements Engineering**: Requirements Engineering Processes **(Chap 4)**. Requirements Elicitation and Analysis **(Sec 4.5).** Functional and non-functional requirements **(Sec 4.1)**. The software Requirements Document **(Sec 4.2)**. Requirements Specification **(Sec 4.3)**. Requirements validation **(Sec 4.6)**. Requirements Management **(Sec 4.7)**.	**12 Hours**
Module 2	
System Models: Context models **(Sec 5.1)**. Interaction models **(Sec 5.2)**. Structural models **(Sec 5.3)**. Behavioral models **(Sec 5.4)**. Model-driven engineering **(Sec 5.5)**. **Design and Implementation**: Introduction to RUP **(Sec 2.4)**, Design Principles **(Chap 17)**. Object-oriented design using the UML **(Sec 7.1)**. Design patterns **(Sec 7.2)**. Implementation issues **(Sec 7.3)**. Open source development **(Sec 7.4)**.	**11 Hours**
Module 3	
Software Testing: Development testing **(Sec 8.1)**, Test-driven development **(Sec 8.2)**, Release testing **(Sec 8.3)**, User testing **(Sec 8.4)**. Test Automation **(Page no 42, 70,212, 231,444,695)**. **Software Evolution**: Evolution processes **(Sec 9.1)**. Program evolution dynamics **(Sec 9.2)**. Software maintenance **(Sec 9.3)**. Legacy system management **(Sec 9.4)**.	**9 Hours**
Module 4	
Project Planning: Software pricing **(Sec 23.1)**. Plan-driven development **(Sec 23.2)**. Project scheduling **(Sec 23.3)**: Estimation techniques **(Sec 23.5)**. **Quality management**: Software quality **(Sec 24.1)**. Reviews and inspections **(Sec 24.3)**. Software measurement and metrics **(Sec 24.4)**. Software standards **(Sec 24.2)**	**10 Hours**
Module 5	
Agile Software Development: Coping with Change **(Sec 2.3)**, The Agile Manifesto: Values and Principles. Agile methods: SCRUM (Ref "**The SCRUM Primer, Ver 2.0**") and Extreme Programming **(Sec 3.3)**. Plan-driven and agile development **(Sec 3.2)**. Agile project management **(Sec 3.4),** Scaling agile methods **(Sec 3.5)**:	**8 Hours**

Course Outcomes: After studying this course, students will be able to:

- Design a software system, component, or process to meet desired needs within realistic constraints.
- Assess professional and ethical responsibility
- Function on multi-disciplinary teams
- Make use of techniques, skills, and modern engineering tools necessary for engineering

practice

- Comprehend software systems or parts of software systems.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Ian Sommerville: Software Engineering, 9th Edition, Pearson Education, 2012.

(Listed topics only from Chapters 1,2,3,4, 5, 7, 8, 9, 23, and 24)

2. The SCRUM Primer, Ver 2.0, http://www.goodagile.com/scrumprimer/scrumprimer20.pdf

Reference Books:

1. Roger S. Pressman: Software Engineering-A Practitioners approach, 7th Edition, Tata McGraw Hill**.**
2. Pankaj Jalote: An Integrated Approach to Software Engineering, Wiley India

Web Reference for eBooks on Agile:

1. http://agilemanifesto.org/
2. http://www.jamesshore.com/Agile-Book/

DATA COMMUNICATION

[As per Choice Based Credit System (CBCS) scheme]

(Effective from the academic year 2017 -2018)

SEMESTER – IV

Subject Code	17CS46	IA Marks	40
Number of Lecture Hours/Week	04	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Contents	**Teaching Hours**
Module 1	
Introduction: Data Communications, Networks, Network Types, Internet History, Standards and Administration, **Networks Models**: Protocol Layering, TCP/IP Protocol suite, The OSI model, **Introduction to Physical Layer-1:** Data and Signals**,** Digital Signals, Transmission Impairment, Data Rate limits, Performance, **Digital Transmission**: Digital to digital conversion (Only Line coding: Polar, Bipolar and Manchester coding).	**10 Hours**
Module 2	
Physical Layer-2: Analog to digital conversion (only PCM), Transmission Modes, **Analog Transmission**: Digital to analog conversion, **Bandwidth Utilization**: Multiplexing and Spread Spectrum, **Switching**: Introduction, Circuit Switched Networks and Packet switching.	**10 Hours**
Module 3	
Error Detection and Correction: Introduction, Block coding, Cyclic codes, Checksum, Forward error correction**, Data link control**: DLC services, Data link layer protocols, HDLC, and Point to Point protocol (Framing, Transition phases only).	**10 Hours**
Module 4	
Media Access control: Random Access, Controlled Access and Channelization, **Wired LANs Ethernet**: Ethernet Protocol, Standard Ethernet, Fast Ethernet, Gigabit Ethernet and 10 Gigabit Ethernet, **Wireless LANs:** Introduction, IEEE 802.11 Project and Bluetooth.	**10 Hours**
Module 5	
Other wireless Networks: WIMAX, Cellular Telephony, Satellite networks, **Network layer Protocols** : Internet Protocol, ICMPv4,Mobile IP, **Next generation IP**: IPv6 addressing, The IPv6 Protocol, The ICMPv6 Protocol and Transition from IPv4 to IPv6.	**10 Hours**

Course Outcomes: After studying this course, students will be able to

- Illustrate basic computer network technology.
- Identify the different types of network topologies and protocols.
- List and explain the layers of the OSI model and TCP/IP model.
- Comprehend the different types of network devices and their functions within a network
- Demonstrate subnetting and routing mechanisms.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Behrouz A. Forouzan, Data Communications and Networking 5E, 5th Edition, Tata McGraw-Hill, 2013. (Chapters 1.1 to 1.5, 2.1 to 2.3, 3.1, 3.3 to 3.6, 4.1 to 4.3, 5.1, 6.1, 6.2, 8.1 to 8.3, 10.1 to 10.5, 11.1 to 11.4, 12.1 to 12.3, 13.1 to 13.5, 15.1 to 15.3, 16.1 to 16.3, 19.1 to 19.3, 22.1 to 22.4)

Reference Books:

1. Alberto Leon-Garcia and Indra Widjaja: Communication Networks - Fundamental Concepts and Key architectures, 2nd Edition Tata McGraw-Hill, 2004.
2. William Stallings: Data and Computer Communication, 8th Edition, Pearson Education, 2007.
3. Larry L. Peterson and Bruce S. Davie: Computer Networks – A Systems Approach, 4th Edition, Elsevier, 2007.
4. Nader F. Mir: Computer and Communication Networks, Pearson Education, 2007

DESIGN AND ANALYSIS OF ALGORITHM LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – IV

Subject Code	17CSL47	IA Marks	40
Number of Lecture Hours/Week	01 I + 02 P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description

Design, develop, and implement the specified algorithms for the following problems using Java language under LINUX /Windows environment.Netbeans/Eclipse IDE tool can be used for development and demonstration.

Experiments

1	A	Create a Java class called ***Student***with the following details as variables within it. (i) USN (ii) Name (iii) Branch (iv) Phone Write a Java program to create *nStudent* objects and print the USN, Name, Branch, and Phoneof these objects with suitable headings.
	B	Write a Java program to implement the Stack using arrays. Write Push(), Pop(), and Display() methods to demonstrate its working.
2	A	Design a superclass called ***Staff*** with details as StaffId, Name, Phone, Salary. Extend this class by writing three subclasses namely ***Teaching*** (domain, publications), ***Technical*** (skills), and ***Contract*** (period). Write a Java program to read and display at least 3 *staff* objects of all three categories.
	B	Write a Java class called ***Customer*** to store their name and date_of_birth. The date_of_birth format should be dd/mm/yyyy. Write methods to read customer data as <name, dd/mm/yyyy> and display as <name, dd, mm, yyyy> using StringTokenizer class considering the delimiter character as "/".
3	A	Write a Java program to read two integers *a* and*b*. Compute *a*/*b* and print, when *b* is not zero. Raise an exception when *b* is equal to zero.
	B	Write a Java program that implements a multi-thread application that has three threads. First thread generates a random integer for every 1 second; second thread computes the square of the number andprints; third thread will print the value of cube of the number.
4		Sort a given set of ***n*** integer elements using **Quick Sort** method and compute its time complexity. Run the program for varied values of *n*> 5000 and record the time taken to sort. Plot a graph of the time taken versus ***n***on graph sheet. The elements can be read from a file or can be generated using the random number generator. Demonstrate using Java how the divide-and-conquer method works along with its time complexity analysis: worst case, average case and best case.
5		Sort a given set of ***n*** integer elements using **Merge Sort** method and compute its time complexity. Run the program for varied values of *n*> 5000, and record the time taken to sort. Plot a graph of the time taken versus ***n***on graph sheet. The elements can be read from a file or can be generated using the random number generator. Demonstrate using Java how the divide-

	and-conquer method works along with its time complexity analysis: worst case, average case and best case.
6	Implement in Java, the **0/1 Knapsack** problem using (a) Dynamic Programming method (b) Greedy method.
7	From a given vertex in a weighted connected graph, find shortest paths to other vertices using **Dijkstra's algorithm**. Write the program in Java.
8	Find Minimum Cost Spanning Tree of a given connected undirected graph using **Kruskal'salgorithm.** Use Union-Find algorithms in your program.
9	Find Minimum Cost Spanning Tree of a given connected undirected graph using **Prim's algorithm**.
10	Write Java programs to (a) Implement All-Pairs Shortest Paths problem using **Floyd's algorithm**. (b) Implement **Travelling Sales Person problem** using Dynamic programming.
11	Design and implement in Java to find a **subset** of a given set **S** = {S_1, S_2,.....,S_n} of ***n*** positive integers whose SUM is equal to a given positive integer ***d***. For example, if S ={1, 2, 5, 6, 8} and ***d***= 9, there are two solutions {1,2,6}and {1,8}. Display a suitable message, if the given problem instance doesn't have a solution.
12	Design and implement in Java to find all **Hamiltonian Cycles** in a connected undirected Graph G of *n* vertices using backtracking principle.

Course Outcomes: The students should be able to:

- Design algorithms using appropriate design techniques (brute-force, greedy, dynamic programming, etc.)
- Develop variety of algorithms such as sorting, graph related, combinatorial, etc., in a high level language.
- Analyze and compare the performance of algorithms using language features.
- Apply and implement learned algorithm design techniques and data structuresto solve real-world problems.

Conduction of Practical Examination:

All laboratory experiments (Twelve problems) are to be included for practical examination. Students are allowed to pick one experiment from the lot.
To generate the data set use random number generator function.
Strictly follow the instructions as printed on the cover page of answer script for breakup of marks
Marks distribution: Procedure + Conduction + Viva: 15 + 70 + 15 (100). Change of experiment is allowed only once and marks allotted to the procedure

MICROPROCESSOR AND MICROCONTROLLER LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – IV

Subject Code	17CSL48	IA Marks	40
Number of Lecture Hours/Week	01 I + 02 P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description

Demonstration and Explanation hardware components and Faculty in-charge should explain 8086 architecture, pin diagram in one slot. The second slot, the Faculty in-charge should explain instruction set types/category etc. Students have to prepare a write-up on the same and include it in the Lab record and to be evaluated.

Laboratory Session-1: Write-up on Microprocessors, 8086 Functional block diagram, Pin diagram and description. The same information is also taught in theory class; this helps the students to understand better.

Laboratory Session-2: Write-up on Instruction group, Timing diagrams, etc. The same information is also taught in theory class; this helps the students to understand better.

Note: These TWO Laboratory sessions are used to fill the gap between theory classes and practical sessions. Both sessions are evaluated as lab experiments for 20 marks.

Experiments

- Develop and execute the following programs using 8086 Assembly Language. Any suitable assembler like MASM/TASM/8086 kit or any equivalent software may be used.
- Program should have suitable comments.
- The board layout and the circuit diagram of the interface are to be provided to the student during the examination.
- Software Required: Open source ARM Development platform, KEIL IDE and Proteus for simulation

SOFTWARE PROGRAMS: PART A

1. Design and develop an assembly language program to search a key element "X" in a list of 'n' 16-bit numbers. Adopt Binary search algorithm in your program for searching.
2. Design and develop an assembly program to sort a given set of 'n' 16-bit numbers in ascending order. Adopt Bubble Sort algorithm to sort given elements.
3. Develop an assembly language program to reverse a given string and verify whether it is a palindrome or not. Display the appropriate message.
4. Develop an assembly language program to compute nCr using recursive procedure. Assume that 'n' and 'r' are non-negative integers.
5. Design and develop an assembly language program to read the current time and Date from the system and display it in the standard format on the screen.
6. To write and simulate ARM assembly language programs for data transfer, arithmetic and logical operations (Demonstrate with the help of a suitable program).
7. To write and simulate C Programs for ARM microprocessor using KEIL (Demonstrate with the help of a suitable program)

   Note : To use KEIL one may refer the book: Insider's Guide to the ARM7 based microcontrollers, Hitex Ltd.,1st edition, 2005

HARDWARE PROGRAMS: PART B

8. a. Design and develop an assembly program to demonstrate BCD Up-Down Counter (00-99) on the Logic Controller Interface.
   b. Design and develop an assembly program to read the status of two 8-bit inputs (X & Y) from the Logic Controller Interface and display X*Y.
9. Design and develop an assembly program to display messages "FIRE" and "HELP" alternately with flickering effects on a 7-segment display interface for a suitable period of time. Ensure a flashing rate that makes it easy to read both the messages (Examiner does not specify these delay values nor is it necessary for the student to compute these values).
10. Design and develop an assembly program to drive a Stepper Motor interface and rotate the motor in specified direction (clockwise or counter-clockwise) by N steps (Direction and N are specified by the examiner). Introduce suitable delay between successive steps. (Any arbitrary value for the delay may be assumed by the student).
11. Design and develop an assembly language program to
    a. Generate the Sine Wave using DAC interface (The output of the DAC is to be displayed on the CRO).
    b. Generate a Half Rectified Sine waveform using the DAC interface. (The output of the DAC is to be displayed on the CRO).
12. To interface LCD with ARM processor-- ARM7TDMI/LPC2148. Write and execute programs in C language for displaying text messages and numbers on LCD
13. To interface Stepper motor with ARM processor-- ARM7TDMI/LPC2148. Write a program to rotate stepper motor

Study Experiments:

1. Interfacing of temperature sensor with ARM freedom board (or any other ARM microprocessor board) and display temperature on LCD
2. To design ARM cortex based automatic number plate recognition system
3. To design ARM based power saving system

Course Outcomes: After studying this course, students will be able to

- Summarize 80x86 instruction sets and comprehend the knowledge of how assembly language works.
- Design and develop assembly programs using 80x86 assembly language instructions
- Infer functioning of hardware devices and interfacing them to x86 family
- Choose processors for various kinds of applications.

Conduction of Practical Examination:

- All laboratory experiments (all 7 + 6 nos) are to be included for practical examination.
- Students are allowed to pick one experiment from each of the lot.
- Strictly follow the instructions as printed on the cover page of answer script for breakup of marks
- PART –A: Procedure + Conduction + Viva: **08 + 35 +07 (50)**
- PART –B: Procedure + Conduction + Viva: **08 + 35 +07 (50)**
- Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.

MANAGEMENT AND ENTREPRENEURSHIP FOR IT INDUSTRY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS51	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Introduction - Meaning, nature and characteristics of management, scope and Functional areas of management, goals of management, levels of management, brief overview of evolution of management theories,. Planning- Nature, importance, types of plans, steps in planning, Organizing- nature and purpose, types of Organization, Staffing- meaning, process of recruitment and selection	**10 Hours**
Module – 2	
Directing and controlling- meaning and nature of directing, leadership styles, motivation Theories, Communication- Meaning and importance, Coordination- meaning and importance, Controlling- meaning, steps in controlling, methods of establishing control.	**10 Hours**
Module – 3	
Entrepreneur – meaning of entrepreneur, characteristics of entrepreneurs, classification and types of entrepreneurs, various stages in entrepreneurial process, role of entrepreneurs in economic development, entrepreneurship in India and barriers to entrepreneurship. Identification of business opportunities, market feasibility study, technical feasibility study, financial feasibility study and social feasibility study.	**10 Hours**
Module – 4	
Preparation of project and ERP - meaning of project, project identification, project selection, project report, need and significance of project report, contents, formulation, guidelines by planning commission for project report, **Enterprise Resource Planning: Meaning and Importance- ERP** and Functional areas of Management – Marketing / Sales- Supply Chain Management – Finance and Accounting – Human Resources – Types of reports and methods of report generation	**10 Hours**
Module – 5	
Micro and Small Enterprises: Definition of micro and small enterprises, characteristics and advantages of micro and small enterprises, steps in establishing micro and small enterprises, Government of India indusial policy 2007 on micro and small enterprises, case study (Microsoft), Case study(Captain G R Gopinath),case study (N R Narayana Murthy & Infosys), **Institutional support:** MSME-DI, NSIC, SIDBI, KIADB, KSSIDC, TECSOK, KSFC, DIC and District level single window agency, **Introduction to IPR.**	**10 Hours**

Course outcomes: The students should be able to:

- Define management, organization, entrepreneur, planning, staffing, ERP and outline their importance in entrepreneurship
- Utilize the resources available effectively through ERP
- Make use of IPRs and institutional support in entrepreneurship

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Principles of Management -P. C. Tripathi, P. N. Reddy; Tata McGraw Hill, 4th / 6th Edition, 2010.
2. Dynamics of Entrepreneurial Development & Management -Vasant Desai Himalaya Publishing House.
3. Entrepreneurship Development -Small Business Enterprises -Poornima M Charantimath Pearson Education – 2006.
4. Management and Entrepreneurship - Kanishka Bedi- Oxford University Press-2017

Reference Books:

1. Management Fundamentals -Concepts, Application, Skill Development Robert Lusier – Thomson.
2. Entrepreneurship Development -S S Khanka -S Chand & Co.
3. Management -Stephen Robbins -Pearson Education /PHI -17th Edition, 2003

COMPUTER NETWORKS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS52	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Application Layer: Principles of Network Applications: Network Application Architectures, Processes Communicating, Transport Services Available to Applications, Transport Services Provided by the Internet, Application-Layer Protocols. The Web and HTTP: Overview of HTTP, Non-persistent and Persistent Connections, HTTP Message Format, User-Server Interaction: Cookies, Web Caching, The Conditional GET, File Transfer: FTP Commands & Replies, Electronic Mail in the Internet: SMTP, Comparison with HTTP, Mail Message Format, Mail Access Protocols, DNS; The Internet's Directory Service: Services Provided by DNS, Overview of How DNS Works, DNS Records and Messages, Peer-to-Peer Applications: P2P File Distribution, Distributed Hash Tables. **T1: Chap 2**	**10 Hours**
Module – 2	
Transport Layer : Introduction and Transport-Layer Services: Relationship Between Transport and Network Layers, Overview of the Transport Layer in the Internet, Multiplexing and Demultiplexing: Connectionless Transport: UDP,UDP Segment Structure, UDP Checksum, Principles of Reliable Data Transfer: Building a Reliable Data Transfer Protocol, Pipelined Reliable Data Transfer Protocols, Go-Back-N, Selective repeat, Connection-Oriented Transport TCP: The TCP Connection, TCP Segment Structure, Round-Trip Time Estimation and Timeout, Reliable Data Transfer, Flow Control, TCP Connection Management, Principles of Congestion Control: The Causes and the Costs of Congestion, Approaches to Congestion Control. **T1: Chap 3**	**10 Hours**
Module – 3	
The Network layer: What's Inside a Router?: Input Processing, Switching, Output Processing, Where Does Queuing Occur? Routing control plane, IPv6,A Brief foray into IP Security, Routing Algorithms: The Link-State (LS) Routing Algorithm, The Distance-Vector (DV) Routing Algorithm, Hierarchical Routing, Routing in the Internet, Intra-AS Routing in the Internet: RIP, Intra-AS Routing in the Internet: OSPF, Inter/AS Routing: BGP, Broadcast Routing Algorithms and Multicast. **T1: Chap 4: 4.3-4.7**	**10 Hours**
Module – 4	
Wireless and Mobile Networks: Cellular Internet Access: An Overview of Cellular Network Architecture, 3G Cellular Data Networks: Extending the Internet to Cellular subscribers, On to 4G:LTE,Mobility management: Principles,	**10 Hours**

Addressing, Routing to a mobile node, Mobile IP, Managing mobility in cellular Networks, Routing calls to a Mobile user, Handoffs in GSM, Wireless and Mobility: Impact on Higher-layer protocols. **T1: Chap: 6 : 6.4-6.8**	
Module – 5	
Multimedia Networking: Properties of video, properties of Audio, Types of multimedia Network Applications, Streaming stored video: UDP Streaming, HTTP Streaming, Adaptive streaming and DASH, content distribution Networks, case study: You Tube. **Network Support for Multimedia:** Quality-of-Service (QoS) Guarantees: Resource Reservation and Call Admission **T1: Chap: 7**	**10 Hours**
Course outcomes: The students should be able to:	
• Explain principles of application layer protocols • Outline transport layer services and infer UDP and TCP protocols • Classify routers, IP and Routing Algorithms in network layer • Explain the Wireless and Mobile Networks covering IEEE 802.11 Standard • Define Multimedia Networking and Network Management	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. James F Kurose and Keith W Ross, Computer Networking, A Top-Down Approach, Sixth edition, Pearson,2017 .	
Reference Books:	
1. Behrouz A Forouzan, Data and Communications and Networking, Fifth Edition, McGraw Hill, Indian Edition 2. Larry L Peterson and Brusce S Davie, Computer Networks, fifth edition, ELSEVIER 3. Andrew S Tanenbaum, Computer Networks, fifth edition, Pearson 4. Mayank Dave, Computer Networks, Second edition, Cengage Learning	

DATABASE MANAGEMENT SYSTEM
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS53	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Introduction to Databases: Introduction, Characteristics of database approach, Advantages of using the DBMS approach, History of database applications. **Overview of Database Languages and Architectures:** Data Models, Schemas, and Instances. Three schema architecture and data independence, database languages, and interfaces, The Database System environment. **Conceptual Data Modelling using Entities and Relationships:** Entity types, Entity sets, attributes, roles, and structural constraints, Weak entity types, ER diagrams, examples, Specialization and Generalization. **Textbook 1:Ch 1.1 to 1.8, 2.1 to 2.6, 3.1 to 3.10**	**10 Hours**
Module – 2	
Relational Model: Relational Model Concepts, Relational Model Constraints and relational database schemas, Update operations, transactions, and dealing with constraint violations. **Relational Algebra:** Unary and Binary relational operations, additional relational operations (aggregate, grouping, etc.) Examples of Queries in relational algebra. **Mapping Conceptual Design into a Logical Design:** Relational Database Design using ER-to-Relational mapping. **SQL:** SQL data definition and data types, specifying constraints in SQL, retrieval queries in SQL, INSERT, DELETE, and UPDATE statements in SQL, Additional features of SQL. **Textbook 1: Ch4.1 to 4.5, 5.1 to 5.3, 6.1 to 6.5, 8.1; Textbook 2: 3.5**	**10 Hours**
Module – 3	
SQL : Advances Queries: More complex SQL retrieval queries, Specifying constraints as assertions and action triggers, Views in SQL, Schema change statements in SQL. **Database Application Development:** Accessing databases from applications, An introduction to JDBC, JDBC classes and interfaces, SQLJ, Stored procedures, Case study: The internet Bookshop. **Internet Applications:** The three-Tier application architecture, The presentation layer, The Middle Tier **Textbook 1: Ch7.1 to 7.4; Textbook 2: 6.1 to 6.6, 7.5 to 7.7.**	**10 Hours**
Module – 4	
Normalization: Database Design Theory – Introduction to Normalization using Functional and Multivalued Dependencies: Informal design guidelines for relation schema, Functional Dependencies, Normal Forms based on Primary Keys, Second and Third Normal Forms, Boyce-Codd Normal Form, Multivalued Dependency and Fourth Normal Form, Join Dependencies and Fifth Normal Form. **Normalization Algorithms:** Inference Rules, Equivalence, and Minimal Cover, Properties of Relational Decompositions, Algorithms for Relational Database Schema Design, Nulls, Dangling tuples, and alternate Relational Designs, Further discussion of Multivalued dependencies and 4NF, Other dependencies and Normal Forms	**10 Hours**

Textbook 1: Ch14.1 to 14.7, 15.1 to 15.6	
Module – 5	
Transaction Processing: Introduction to Transaction Processing, Transaction and System concepts, Desirable properties of Transactions, Characterizing schedules based on recoverability, Characterizing schedules based on Serializability, Transaction support in SQL. **Concurrency Control in Databases:** Two-phase locking techniques for Concurrency control, Concurrency control based on Timestamp ordering, Multiversion Concurrency control techniques, Validation Concurrency control techniques, Granularity of Data items and Multiple Granularity Locking. **Introduction to Database Recovery Protocols:** Recovery Concepts, NO-UNDO/REDO recovery based on Deferred update, Recovery techniques based on immediate update, Shadow paging, Database backup and recovery from catastrophic failures **Textbook 1: 20.1 to 20.6, 21.1 to 21.7, 22.1 to 22.4, 22.7.**	**10 Hours**
Course outcomes: The students should be able to:	
• Summarize the concepts of database objects; enforce integrity constraints on a database using RDBMS. • Use Structured Query Language (SQL) for database manipulation. • Design simple database systems • Design code for some application to interact with databases.	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. Fundamentals of Database Systems, Ramez Elmasri and Shamkant B. Navathe, 7th Edition, 2017, Pearson. 2. Database management systems, Ramakrishnan, and Gehrke, 3rd Edition, 2014, McGraw Hill	
Reference Books:	
1. Silberschatz Korth and Sudharshan, Database System Concepts, 6th Edition, Mc-GrawHill, 2013. 2. Coronel, Morris, and Rob, Database Principles Fundamentals of Design, Implementation and Management, Cengage Learning 2012.	

AUTOMATA THEORY AND COMPUTABILITY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS54	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Why study the Theory of Computation, Languages and Strings: Strings, Languages. A Language Hierarchy, Computation, **Finite State Machines (FSM)**: Deterministic FSM, Regular languages, Designing FSM, Nondeterministic FSMs, From FSMs to Operational Systems, Simulators for FSMs, Minimizing FSMs, Canonical form of Regular languages, Finite State Transducers, Bidirectional Transducers. **Textbook 1: Ch 1,2, 3,4, 5.1 to 5.10**	**10 Hours**
Module – 2	
Regular Expressions (RE): what is a RE?, Kleene's theorem, Applications of REs, Manipulating and Simplifying REs. Regular Grammars: Definition, Regular Grammars and Regular languages. Regular Languages (RL) and Non-regular Languages: How many RLs, To show that a language is regular, Closure properties of RLs, to show some languages are not RLs. **Textbook 1: Ch 6, 7, 8: 6.1 to 6.4, 7.1, 7.2, 8.1 to 8.4**	**10 Hours**
Module – 3	
Context-Free Grammars(CFG): Introduction to Rewrite Systems and Grammars, CFGs and languages, designing CFGs, simplifying CFGs, proving that a Grammar is correct, Derivation and Parse trees, Ambiguity, Normal Forms. Pushdown Automata (PDA): Definition of non-deterministic PDA, Deterministic and Non-deterministic PDAs, Non-determinism and Halting, alternative equivalent definitions of a PDA, alternatives that are not equivalent to PDA. **Textbook 1: Ch 11, 12: 11.1 to 11.8, 12.1, 12.2, 12,4, 12.5, 12.6**	**10 Hours**
Module – 4	
Context-Free and Non-Context-Free Languages: Where do the Context-Free Languages(CFL) fit, Showing a language is context-free, Pumping theorem for CFL, Important closure properties of CFLs, Deterministic CFLs. Algorithms and Decision Procedures for CFLs: Decidable questions, Un-decidable questions. Turing Machine: Turing machine model, Representation, Language acceptability by TM, design of TM, Techniques for TM construction. **Textbook 1: Ch 13: 13.1 to 13.5, Ch 14: 14.1, 14.2, Textbook 2: Ch 9.1 to 9.6**	**10 Hours**
Module – 5	
Variants of Turing Machines (TM), The model of Linear Bounded automata: Decidability: Definition of an algorithm, decidability, decidable languages, Undecidable languages, halting problem of TM, Post correspondence problem. Complexity: Growth rate of functions, the classes of P and NP, Quantum Computation: quantum computers, Church-Turing thesis. **Textbook 2: Ch 9.7 to 9.8, 10.1 to 10.7, 12.1, 12.2, 12.8, 12.8.1, 12.8.2**	**10 Hours**
Course outcomes: The students should be able to:	
• Tell the core concepts in automata theory and Theory of Computation	

- Explain how to translate between different models of Computation (e.g., Deterministic and Non-deterministic and Software models).
- Interpret Grammars and Automata (recognizers) for different language classes and become knowledgeable about restricted models of Computation (Regular, Context Free) and their relative powers.
- Develop skills in formal reasoning and reduction of a problem to a formal model, with an emphasis on semantic precision and conciseness.
- Classify a problem with respect to different models of Computation.

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Elaine Rich, Automata, Computability and Complexity, 1st Edition, Pearson Education,2012/2013
2. K L P Mishra, N Chandrasekaran , 3rd Edition, Theory of Computer Science, PhI, 2012.

Reference Books:

1. John E Hopcroft, Rajeev Motwani, Jeffery D Ullman, Introduction to AutomataTheory, Languages, and Computation, 3rd Edition, Pearson Education, 2013
2. Michael Sipser : Introduction to the Theory of Computation, 3rd edition, Cengage learning,2013
3. John C Martin, Introduction to Languages and The Theory of Computation, 3rd Edition, Tata McGraw –Hill Publishing Company Limited, 2013
4. Peter Linz, "An Introduction to Formal Languages and Automata", 3rd Edition, Narosa Publishers, 1998
5. Basavaraj S. Anami, Karibasappa K G, Formal Languages and Automata theory, Wiley India, 2012
6. C K Nagpal, Formal Languages and Automata Theory, Oxford University press, 2012.

OBJECT ORIENTED MODELING AND DESIGN
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS551	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction, Modelling Concepts and Class Modelling: What is Object orientation? What is OO development? OO Themes; Evidence for usefulness of OO development; OO modelling history. Modelling as Design technique: Modelling; abstraction; The Three models. Class Modelling: Object and Class Concept; Link and associations concepts; Generalization and Inheritance; A sample class model; Navigation of class models; Advanced Class Modelling, Advanced object and class concepts; Association ends; N-ary associations; Aggregation; Abstract classes; Multiple inheritance; Metadata; Reification; Constraints; Derived Data; Packages. **Text Book-1: Ch 1, 2, 3 and 4**	**8 Hours**
Module – 2	
UseCase Modelling and Detailed Requirements: Overview; Detailed object-oriented Requirements definitions; System Processes-A use case/Scenario view; Identifying Input and outputs-The System sequence diagram; Identifying Object Behaviour-The state chart Diagram; Integrated Object-oriented Models. **Text Book-2:Chapter- 6:Page 210 to 250**	**8 Hours**
Module – 3	
Process Overview, System Conception and Domain Analysis: Process Overview: Development stages; Development life Cycle; System Conception: Devising a system concept; elaborating a concept; preparing a problem statement. Domain Analysis: Overview of analysis; Domain Class model: Domain state model; Domain interaction model; Iterating the analysis. **Text Book-1:Chapter- 10,11,and 12**	**8 Hours**
Module – 4	
Use case Realization :The Design Discipline within up iterations: Object Oriented Design-The Bridge between Requirements and Implementation; Design Classes and Design within Class Diagrams; Interaction Diagrams-Realizing Use Case and defining methods; Designing with Communication Diagrams; Updating the Design Class Diagram; Package Diagrams-Structuring the Major Components; Implementation Issues for Three-Layer Design. **Text Book-2: Chapter 8: page 292 to 346**	**8 Hours**
Module – 5	
Design Patterns: Introduction; what is a design pattern?, Describing design patterns, the catalogue of design patterns, Organizing the catalogue, How design patterns solve design problems, how to select a design patterns, how to use a design pattern; Creational patterns: prototype and singleton (only); structural patterns adaptor and proxy (only). **Text Book-3: Ch-1: 1.1, 1.3, 1.4, 1.5, 1.6, 1.7, 1.8,Ch-3,Ch-4.**	**8 Hours**
Course outcomes: The students should be able to:	

- Describe the concepts of object-oriented and basic class modelling.
- Draw class diagrams, sequence diagrams and interaction diagrams to solve problems.
- Choose and apply a befitting design pattern for the given problem.

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Michael Blaha, James Rumbaugh: Object Oriented Modelling and Design with UML,2nd Edition, Pearson Education,2005
2. Satzinger, Jackson and Burd: Object-Oriented Analysis & Design with the Unified Process, Cengage Learning, 2005.
3. Erich Gamma, Richard Helm, Ralph Johnson and john Vlissides: Design Patterns – Elements of Reusable Object-Oriented Software, Pearson Education,2007.

Reference Books:

1. Grady Booch et. al.: Object-Oriented Analysis and Design with Applications,3rd Edition,Pearson Education,2007.
2. 2.Frank Buschmann, RegineMeunier, Hans Rohnert, Peter Sommerlad, Michel Stal: Pattern –Oriented Software Architecture. A system of patterns , Volume 1, John Wiley and Sons.2007.
3. 3. Booch, Jacobson, Rambaugh : Object-Oriented Analysis and Design with Applications, 3rd edition, pearson, Reprint 2013

INTRODUCTION TO SOFTWARE TESTING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS552	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Basics of Software Testing: Basic definitions, Software Quality , Requirements, Behaviour and Correctness, Correctness versus Reliability, Testing and Debugging, Test cases, Insights from a Venn diagram, Identifying test cases, Test-generation Strategies, Test Metrics, Error and fault taxonomies , Levels of testing, Testing and Verification, Static Testing. **Textbook 3: Ch 1:1.2 - 1.5, 3; Textbook 1: Ch 1**	**8 Hours**
Module – 2	
Problem Statements: Generalized pseudo code, the triangle problem, the NextDate function, the commission problem, the SATM (Simple Automatic Teller Machine) problem, the currency converter, Saturn windshield wiper **Functional Testing:** Boundary value analysis, Robustness testing, Worst-case testing, Robust Worst testing for triangle problem, NextDate problem and commission problem, Equivalence classes, Equivalence test cases for the triangle problem, NextDate function, and the commission problem, Guidelines and observations, Decision tables, Test cases for the triangle problem, NextDate function, and the commission problem, Guidelines and observations. **Textbook 1: Ch 2, 5, 6 & 7, Textbook 2: Ch 3**	**8 Hours**
Module – 3	
Fault Based Testing: Overview, Assumptions in fault based testing, Mutation analysis, Fault-based adequacy criteria, Variations on mutation analysis. **Structural Testing:** Overview, Statement testing, Branch testing, Condition testing, Path testing: DD paths, Test coverage metrics, Basis path testing, guidelines and observations, Data –Flow testing: Definition-Use testing, Slice-based testing, Guidelines and observations**.** **T2:Chapter 16, 12 T1:Chapter 9 & 10**	**8 Hours**
Module – 4	
Test Execution: Overview of test execution, from test case specification to test cases, Scaffolding, Generic versus specific scaffolding, Test oracles, Self-checks as oracles, Capture and replay **Process Framework :**Basic principles: Sensitivity, redundancy, restriction, partition, visibility, Feedback, the quality process, Planning and monitoring, Quality goals, Dependability properties ,Analysis Testing, Improving the process, Organizational factors. **Planning and Monitoring the Process:** Quality and process, Test and analysis strategies and plans, Risk planning, monitoring the process, Improving the process, the quality team. **T2: Chapter 17, 20.**	**8 Hours**
Module – 5	
Integration and Component-Based Software Testing: Overview, Integration testing strategies, Testing components and assemblies. System, Acceptance and	**8 Hours**

Regression Testing: Overview, System testing, Acceptance testing, Usability, Regression testing, Regression test selection techniques, Test case prioritization and selective execution. **Levels of Testing, Integration Testing:** Traditional view of testing levels, Alternative life-cycle models, The SATM system, Separating integration and system testing, A closer look at the SATM system, Decomposition-based, call graph-based, Path-based integrations**.** **T2: Chapter 21 & 22, T1 : Chapter 12 & 13**	

Course outcomes: The students should be able to:

- Identify test cases for any given problem.
- Compare the different testing techniques.
- Classify the problems according to a suitable testing model.
- Apply the appropriate technique for the design of flow graph.
- Create appropriate document for the software artefact.

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Paul C. Jorgensen: Software Testing, A Craftsman's Approach, 3rd Edition, Auerbach Publications, 2008.
2. Mauro Pezze, Michal Young: Software Testing and Analysis – Process, Principles and Techniques, Wiley India, 2009.
3. Aditya P Mathur: Foundations of Software Testing, Pearson Education, 2008.

Reference Books:

1. Software testing Principles and Practices – Gopalaswamy Ramesh, Srinivasan Desikan, 2 nd Edition, Pearson, 2007.
2. Software Testing – Ron Patton, 2nd edition, Pearson Education, 2004.
3. The Craft of Software Testing – Brian Marrick, Pearson Education, 1995.
4. Anirban Basu, Software Quality Assurance, Testing and Metrics, PHI, 2015
5. Naresh Chauhan, Software Testing, Oxford University press.

ADVANCED JAVA AND J2EE
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS553	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Enumerations, Autoboxing and Annotations(metadata): Enumerations, Enumeration fundamentals, the values() and valueOf() Methods, java enumerations are class types, enumerations Inherits Enum, example, type wrappers, Autoboxing, Autoboxing and Methods, Autoboxing/Unboxing occurs in Expressions, Autoboxing/Unboxing, Boolean and character values, Autoboxing/Unboxing helps prevent errors, A word of Warning. Annotations, Annotation basics, specifying retention policy, Obtaining Annotations at run time by use of reflection, Annotated element Interface, Using Default values, Marker Annotations, Single Member annotations, Built-In annotations.	**8 Hours**
Module – 2	
The collections and Framework: Collections Overview, Recent Changes to Collections, The Collection Interfaces, The Collection Classes, Accessing a collection Via an Iterator, Storing User Defined Classes in Collections, The Random Access Interface, Working With Maps, Comparators, The Collection Algorithms, Why Generic Collections?, The legacy Classes and Interfaces, Parting Thoughts on Collections.	**8 Hours**
Module – 3	
String Handling **:**The String Constructors, String Length, Special String Operations, String Literals, String Concatenation, String Concatenation with Other Data Types, String Conversion and toString( ) Character Extraction, charAt( ), getChars( ), getBytes( ) toCharArray(), String Comparison, equals( ) and equalsIgnoreCase( ), regionMatches( ) startsWith( ) and endsWith( ), equals( ) Versus == , compareTo( ) Searching Strings, Modifying a String, substring( ), concat( ), replace( ), trim( ), Data Conversion Using valueOf( ), Changing the Case of Characters Within a String, Additional String Methods, StringBuffer , StringBuffer Constructors, length( ) and capacity( ), ensureCapacity( ), setLength( ), charAt( ) and setCharAt( ), getChars( ),append( ), insert( ), reverse( ), delete( ) and deleteCharAt( ), replace( ), substring( ), Additional StringBuffer Methods, StringBuilder **Text Book 1: Ch 15**	**8 Hours**
Module – 4	
Background; The Life Cycle of a Servlet; Using Tomcat for Servlet Development; A simple Servlet; The Servlet API; The Javax.servlet Package; Reading Servlet Parameter; The Javax.servlet.http package; Handling HTTP Requests and Responses; Using Cookies; Session Tracking. Java Server Pages (JSP): JSP, JSP Tags, Tomcat, Request String, User Sessions, Cookies, Session Objects	**8 Hours**

Text Book 1: Ch 31 Text Book 2: Ch 11	
Module – 5	
The Concept of JDBC; JDBC Driver Types; JDBC Packages; A Brief Overview of the JDBC process; Database Connection; Associating the JDBC/ODBC Bridge with the Database; Statement Objects; ResultSet; Transaction Processing; Metadata, Data types; Exceptions. **Text Book 2: Ch 06**	**8 Hours**

Course outcomes: The students should be able to:

- Interpret the need for advanced Java concepts like enumerations and collections in developing modular and efficient programs
- Build client-server applications and TCP/IP socket programs
- Illustrate database access and details for managing information using the JDBC API
- Describe how servlets fit into Java-based web application architecture
- Develop reusable software components using Java Beans

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Herbert Schildt: JAVA the Complete Reference, 7th/9th Edition, Tata McGraw Hill, 2007.
2. Jim Keogh: J2EE-TheCompleteReference, McGraw Hill, 2007.

Reference Books:

1. Y. Daniel Liang: Introduction to JAVA Programming, 7thEdition, Pearson Education, 2007.
2. Stephanie Bodoff et al: The J2EE Tutorial, 2nd Edition, Pearson Education,2004.
3. Uttam K Roy, Advanced JAVA programming, Oxford University press, 2015.

ADVANCED ALGORITHMS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CS554	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Analysis Techniques: Growth functions, Recurrences and solution of recurrence equations; Amortized analysis: Aggregate, Accounting, and Potential methods, String Matching Algorithms: Naive Algorithm; Robin-Karp Algorithm, String matching with Finite Automata, Knuth-Morris-Pratt and Boyer-Moore Algorithms	**8 Hours**
Module – 2	
Number Theoretic Algorithms: Elementary notions, GCD, Modular arithmetic, Solving modular linear equations, The Chinese remainder theorem, Powers of an element RSA Cryptosystem, Primality testing, Integer factorization, - Huffman Codes, Polynomials. FFT-Huffman codes: Concepts, construction, Proof correctness of Huffman's algorithm; Representation of polynomials	**8 Hours**
Module – 3	
DFT and FFT efficient implementation of FFT, Graph Algorithms, Bellman-Ford Algorithm Shortest paths in a DAG, Johnson's Algorithm for sparse graphs, Flow networks and the Ford-Fulkerson Algorithm, Maximum bipartite matching.	**8 Hours**
Module – 4	
Computational Geometry-I: Geometric data structures using, C, Vectors, Points, Polygons, Edges Geometric objects in space; Finding the intersection of a line and a triangle, Finding star-shaped polygons using incremental insertion.	**8 Hours**
Module – 5	
Computational Geometry-II: Clipping: Cyrus-Beck and Sutherland-Hodman Algorithms; Triangulating, monotonic polygons; Convex hulls, Gift wrapping and Graham Scan; Removing hidden surfaces	**8 Hours**

Course outcomes: The students should be able to:

- Explain the principles of algorithms analysis approaches
- Apply different theoretic based strategies to solve problems
- Illustrate the complex signals and data flow in networks with usage of tools
- Describe the computational geometry criteria.

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Thomas H. Cormen et al: Introduction to Algorithms, Prentice Hall India, 1990
2. Michael J. Laszlo: Computational Geometry and Computer Graphics in C' Prentice Hall India, 1996

Reference Books:

1. E. Horowitz, S. Sahni and S. Rajasekaran, Fundamentals of Computer Algorithms, University Press, Second edition, 2007
2. Kenneth A Berman & Jerome L Paul, Algorithms, Cengage Learning, First Indian reprint, 2008

PROGRAMMING IN JAVA
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – V

Subject Code	17CS561	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
An Overview of Java: Object-Oriented Programming, A First Simple Program, A Second Short Program, Two Control Statements, Using Blocks of Code, Lexical Issues, The Java Class Libraries, Data Types, Variables, and Arrays: Java Is a Strongly Typed Language, The Primitive Types, Integers, Floating-Point Types, Characters, Booleans, A Closer Look at Literals, Variables, Type Conversion and Casting, Automatic Type Promotion in Expressions, Arrays, A Few Words About Strings **Text book 1: Ch 2, Ch 3**	**8 Hours**
Module – 2	
Operators: Arithmetic Operators, The Bitwise Operators, Relational Operators, Boolean Logical Operators, The Assignment Operator, The ? Operator, Operator Precedence, Using Parentheses, Control Statements: Java's Selection Statements, Iteration Statements, Jump Statements. **Text book 1: Ch 4, Ch 5**	**8 Hours**
Module – 3	
Introducing Classes: Class Fundamentals, Declaring Objects, Assigning Object Reference Variables, Introducing Methods, Constructors, The this Keyword, Garbage Collection, The finalize( ) Method, A Stack Class, A Closer Look at Methods and Classes: Overloading Methods, Using Objects as Parameters, A Closer Look at Argument Passing, Returning Objects, Recursion, Introducing Access Control, Understanding static, Introducing final, Arrays Revisited, Inheritance: Inheritance, Using super, Creating a Multilevel Hierarchy, When Constructors Are Called, Method Overriding, Dynamic Method Dispatch, Using Abstract Classes, Using final with Inheritance, The Object Class. **Text book 1: Ch 6, Ch 7.1-7.9, Ch 8.**	**8 Hours**
Module – 4	
Packages and Interfaces: Packages, Access Protection, Importing Packages, Interfaces, Exception Handling: Exception-Handling Fundamentals, Exception Types, Uncaught Exceptions, Using try and catch, Multiple catch Clauses, Nested try Statements, throw, throws, finally, Java's Built-in Exceptions, Creating Your Own Exception Subclasses, Chained Exceptions, Using Exceptions. **Text book 1: Ch 9, Ch 10**	**8 Hours**
Module – 5	
Enumerations, Type Wrappers, I/O, Applets, and Other Topics: I/O Basics, Reading Console Input, Writing Console Output, The PrintWriter Class, Reading and Writing Files, Applet Fundamentals, The transient and volatile Modifiers, Using instanceof, strictfp, Native Methods, Using assert, Static Import, Invoking Overloaded Constructors Through this( ), String Handling: The String	**8 Hours**

Constructors, String Length, Special String Operations, Character Extraction, String Comparison, Searching Strings, Modifying a String, Data Conversion Using valueOf( ), Changing the Case of Characters Within a String , Additional String Methods, StringBuffer, StringBuilder. **Text book 1: Ch 12.1,12.2, Ch 13, Ch 15**	

Course outcomes: The students should be able to:

- Explain the object-oriented concepts and JAVA.
- Develop computer programs to solve real world problems in Java.
- Develop simple GUI interfaces for a computer program to interact with users

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Herbert Schildt, Java The Complete Reference, 7th Edition, Tata McGraw Hill, 2007. (Chapters 2, 3, 4, 5, 6,7, 8, 9,10, 12,13,15)

Reference Books:

1. Mahesh Bhave and Sunil Patekar, "Programming with Java", First Edition, Pearson Education,2008, ISBN:9788131720806.
2. Rajkumar Buyya,S Thamarasi selvi, xingchen chu, Object oriented Programming with java, Tata McGraw Hill education private limited.
3. E Balagurusamy, Programming with Java A primer, Tata McGraw Hill companies.
4. Anita Seth and B L Juneja, JAVA One step Ahead, Oxford University Press, 2017.

ARTIFICIAL INTELLIGENCE
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – V

Subject Code	17CS562	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
What is artificial intelligence?, Problems, Problem Spaces and search, Heuristic search technique **TextBook1: Ch 1, 2 and 3**	**8 Hours**
Module – 2	
Knowledge Representation Issues, Using Predicate Logic, Representing knowledge using Rules, **TextBoook1: Ch 4, 5 and 6.**	**8 Hours**
Module – 3	
Symbolic Reasoning under Uncertainty, Statistical reasoning, Weak Slot and Filter Structures. **TextBoook1: Ch 7, 8 and 9.**	**8 Hours**
Module – 4	
Strong slot-and-filler structures, Game Playing. **TextBoook1: Ch 10 and 12**	**8 Hours**
Module – 5	
Natural Language Processing, Learning, Expert Systems. **TextBook1: Ch 15,17 and 20**	**8 Hours**

Course outcomes: The students should be able to:

- Identify the AI based problems
- Apply techniques to solve the AI problems
- Define learning and explain various learning techniques
- Discuss expert systems

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. E. Rich , K. Knight & S. B. Nair - Artificial Intelligence, 3/e, McGraw Hill.

Reference Books:

1. Artificial Intelligence: A Modern Approach, Stuart Rusell, Peter Norving, Pearson Education 2nd Edition.
1. Dan W. Patterson, Introduction to Artificial Intelligence and Expert Systems – Prentice Hal of India.
2. G. Luger, "Artificial Intelligence: Structures and Strategies for complex problem

Solving", Fourth Edition, Pearson Education, 2002.
3. Artificial Intelligence and Expert Systems Development by D W Rolston-Mc Graw hill.
4. N.P. Padhy "Artificial Intelligence and Intelligent Systems" , Oxford University Press-2015

EMBEDDED SYSTEMS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – V

Subject Code	17CS563	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction to embedded systems: Embedded systems, Processor embedded into a system, Embedded hardware units and device in a system, Embedded software in a system, Examples of embedded systems, Design process in embedded system, Formalization of system design, Design process and design examples, Classification of embedded systems, skills required for an embedded system designer.	**8 Hours**
Module – 2	
Devices and communication buses for devices network: IO types and example, Serial communication devices, Parallel device ports, Sophisticated interfacing features in device ports, Wireless devices, Timer and counting devices, Watchdog timer, Real time clock, Networked embedded systems, Serial bus communication protocols, Parallel bus device protocols-parallel communication internet using ISA, PCI, PCI-X and advanced buses, Internet enabled systems-network protocols, Wireless and mobile system protocols.	**8 Hours**
Module – 3	
Device drivers and interrupts and service mechanism: Programming-I/O busy-wait approach without interrupt service mechanism, ISR concept, Interrupt sources, Interrupt servicing (Handling) Mechanism, Multiple interrupts, Context and the periods for context switching, interrupt latency and deadline, Classification of processors interrupt service mechanism from Context-saving angle, Direct memory access, Device driver programming.	**8 Hours**
Module – 4	
Inter process communication and synchronization of processes, Threads and tasks: Multiple process in an application, Multiple threads in an application, Tasks, Task states, Task and Data, Clear-cut distinction between functions. ISRS and tasks by their characteristics, concept and semaphores, Shared data, Inter-process communication, Signal function, Semaphore functions, Message Queue functions, Mailbox functions, Pipe functions, Socket functions, RPC functions.	**8 Hours**
Module – 5	
Real-time operating systems: OS Services, Process management, Timer functions, Event functions, Memory management, Device, file and IO subsystems management, Interrupt routines in RTOS environment and handling of interrupt source calls, Real-time operating systems, Basic design using an RTOS, RTOS task scheduling models, interrupt latency and response of the tasks as performance metrics, OS security issues. Introduction to embedded software development process and tools, Host and target machines, Linking and location software.	**8 Hours**
Course outcomes: The students should be able to:	
• Distinguish the characteristics of embedded computer systems.	

- Identify the various vulnerabilities of embedded computer systems.
- Design and develop modules using RTOS.
- Explain RPC, threads and tasks

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Raj Kamal, "Embedded Systems: Architecture, Programming, and Design" 2nd / 3rd edition , Tata McGraw hill-2013.

Reference Books:

1. Marilyn Wolf, "Computer as Components, Principles of Embedded Computing System Design" 3rd edition, Elsevier-2014.

DOT NET FRAMEWORK FOR APPLICATION DEVELOPMENT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – V

Subject Code	17CS564	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introducing Microsoft Visual C# and Microsoft Visual Studio 2015: Welcome to C#, Working with variables, operators and expressions, Writing methods and applying scope, Using decision statements, Using compound assignment and iteration statements, Managing errors and exceptions **T1: Chapter 1 – Chapter 6**	**8 Hours**
Module – 2	
Understanding the C# object model: Creating and Managing classes and objects, Understanding values and references, Creating value types with enumerations and structures, Using arrays **Textbook 1: Ch 7 to 10**	**8 Hours**
Module – 3	
Understanding parameter arrays, Working with inheritance, Creating interfaces and defining abstract classes, Using garbage collection and resource management **Textbook 1: Ch 11 to 14**	**8 Hours**
Module – 4	
Defining Extensible Types with C#: Implementing properties to access fields, Using indexers, Introducing generics, Using collections **Textbook 1: Ch 15 to 18**	**8 Hours**
Module – 5	
Enumerating Collections, Decoupling application logic and handling events, Querying in-memory data by using query expressions, Operator overloading **Textbook 1: Ch 19 to 22**	**8 Hours**

Course outcomes: The students should be able to:

- Build applications on Visual Studio .NET platform by understanding the syntax and semantics of C#
- Demonstrate Object Oriented Programming concepts in C# programming language
- Design custom interfaces for applications and leverage the available built-in interfaces in building complex applications.
- Illustrate the use of generics and collections in C#
- Compose queries to query in-memory data and define own operator behaviour

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. John Sharp, Microsoft Visual C# Step by Step, 8th Edition, PHI Learning Pvt. Ltd. 2016

Reference Books:

1. Christian Nagel, "C# 6 and .NET Core 1.0", 1st Edition, Wiley India Pvt Ltd, 2016. Andrew Stellman and Jennifer Greene, "Head First C#", 3rd Edition, O'Reilly Publications, 2013.
2. Mark Michaelis, "Essential C# 6.0", 5th Edition, Pearson Education India, 2016.
3. Andrew Troelsen, "Prof C# 5.0 and the .NET 4.5 Framework", 6th Edition, Apress and Dreamtech Press, 2012.

CLOUD COMPUTING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – V

Subject Code	17CS565	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction ,Cloud Computing at a Glance, The Vision of Cloud Computing, Defining a Cloud, A Closer Look, Cloud Computing Reference Model, Characteristics and Benefits, Challenges Ahead, Historical Developments, Distributed Systems, Virtualization, Web 2.0, Service-Oriented Computing, Utility-Oriented Computing, Building Cloud Computing Environments, Application Development, Infrastructure and System Development, Computing Platforms and Technologies, Amazon Web Services (AWS), Google AppEngine, Microsoft Azure, Hadoop, Force.com and Salesforce.com, Manjrasoft Aneka Virtualization, Introduction, Characteristics of Virtualized, Environments Taxonomy of Virtualization Techniques, Execution Virtualization, Other Types of Virtualization, Virtualization and Cloud Computing, Pros and Cons of Virtualization, Technology	**8 Hours**
Module – 2	
Cloud Computing Architecture, Introduction, Cloud Reference Model, Architecture, Infrastructure / Hardware as a Service, Platform as a Service, Software as a Service, Types of Clouds, Public Clouds, Private Clouds, Hybrid Clouds, Community Clouds, Economics of the Cloud, Open Challenges, Cloud Definition, Cloud Interoperability and Standards Scalability and Fault Tolerance Security, Trust, and Privacy Organizational Aspects Aneka: Cloud Application Platform, Framework Overview, Anatomy of the Aneka Container, From the Ground Up: Platform Abstraction Layer, Fabric Services, foundation Services, Application Services, Building Aneka Clouds, Infrastructure Organization, Logical Organization, Private Cloud Deployment Mode, Public Cloud Deployment Mode, Hybrid Cloud Deployment Mode, Cloud Programming and Management, Aneka SDK, Management Tools	**8 Hours**
Module – 3	
Concurrent Computing: Thread Programming, Introducing Parallelism for Single Machine Computation, Programming Applications with Threads, What is a Thread?, Thread APIs, Techniques for Parallel Computation with Threads, Multithreading with Aneka, Introducing the Thread Programming Model, Aneka Thread vs. Common Threads, Programming Applications with Aneka Threads, Aneka Threads Application Model, Domain Decomposition: Matrix Multiplication, Functional Decomposition: Sine, Cosine, and Tangent. High-Throughput Computing: Task Programming, Task Computing, Characterizing a Task, Computing Categories, Frameworks for Task Computing, Task-based Application Models, Embarrassingly Parallel Applications, Parameter Sweep Applications, MPI Applications, Workflow Applications with Task Dependencies, Aneka Task-Based Programming, Task Programming	**8 Hours**

Model, Developing Applications with the Task Model, Developing Parameter Sweep Application, Managing Workflows.	
Module – 4	
Data Intensive Computing: Map-Reduce Programming, What is Data-Intensive Computing?, Characterizing Data-Intensive Computations, Challenges Ahead, Historical Perspective, Technologies for Data-Intensive Computing, Storage Systems, Programming Platforms, Aneka MapReduce Programming, Introducing the MapReduce Programming Model, Example Application	**8 Hours**
Module – 5	
Cloud Platforms in Industry, Amazon Web Services, Compute Services, Storage Services, Communication Services, Additional Services, Google AppEngine, Architecture and Core Concepts, Application Life-Cycle, Cost Model, Observations, Microsoft Azure, Azure Core Concepts, SQL Azure, Windows Azure Platform Appliance. Cloud Applications Scientific Applications, Healthcare: ECG Analysis in the Cloud, , Social Networking, Media Applications, Multiplayer Online Gaming.	**8 Hours**
Course outcomes: The students should be able to:	
• Explain the concepts and terminologies of cloud computing • Demonstrate cloud frameworks and technologies • Define data intensive computing • Demonstrate cloud applications	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books: 1. Rajkumar Buyya, Christian Vecchiola, and Thamarai Selvi Mastering Cloud. Computing McGraw Hill Education	
Reference Books:	
NIL	

COMPUTER NETWORK LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CSL57	IA Marks	40
Number of Lecture Hours/Week	01I + 02P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description (If any):

For the experiments below modify the topology and parameters set for the experiment and take multiple rounds of reading and analyze the results available in log files. Plot necessary graphs and conclude. Use NS2/NS3.

Lab Experiments:

PART A

1. Implement three nodes point – to – point network with duplex links between them. Set the queue size, vary the bandwidth and find the number of packets dropped.
2. Implement transmission of ping messages/trace route over a network topology consisting of 6 nodes and find the number of packets dropped due to congestion.
3. Implement an Ethernet LAN using n nodes and set multiple traffic nodes and plot congestion window for different source / destination.
4. Implement simple ESS and with transmitting nodes in wire-less LAN by simulation and determine the performance with respect to transmission of packets.
5. Implement and study the performance of GSM on NS2/NS3 (Using MAC layer) or equivalent environment.
6. Implement and study the performance of CDMA on NS2/NS3 (Using stack called Call net) or equivalent environment.

PART B

Implement the following in Java:

7. Write a program for error detecting code using CRC-CCITT (16- bits).
8. Write a program to find the shortest path between vertices using bellman-ford algorithm.
9. Using TCP/IP sockets, write a client – server program to make the client send the file name and to make the server send back the contents of the requested file if present.
10. Write a program on datagram socket for client/server to display the messages on client side, typed at the server side.
11. Write a program for simple RSA algorithm to encrypt and decrypt the data.
12. Write a program for congestion control using leaky bucket algorithm.

Study Experiment / Project:

NIL

Course outcomes: The students should be able to:

- Analyze and Compare various networking protocols.
- Demonstrate the working of different concepts of networking.
- Implement and analyze networking protocols in NS2 / NS3

Conduction of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Students are allowed to pick one experiment from part A and part B with lot.
3. Strictly follow the instructions as printed on the cover page of answer script

4. Marks distribution: Procedure + Conduction + Viva: 100

Part A: 8+35+7 =50

Part B: 8+35+7 =50

5. Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.

DBMS LABORATORY WITH MINI PROJECT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-2018)
SEMESTER – V

Subject Code	17CSL58	IA Marks	40
Number of Lecture Hours/Week	01I + 02P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description (If any):

PART-A: SQL Programming (Max. Exam Mks. 50)

- Design, develop, and implement the specified queries for the following problems using Oracle, MySQL, MS SQL Server, or any other DBMS under LINUX/Windows environment.
- Create Schema and insert at least 5 records for each table. Add appropriate database constraints.

PART-B: Mini Project (Max. Exam Mks. 30)

- Use Java, C#, PHP, Python, or any other similar front-end tool. All applications must be demonstrated on desktop/laptop as a stand-alone or web based application (Mobile apps on Android/IOS are not permitted.)

Lab Experiments:

Part A: SQL Programming

1 Consider the following schema for a Library Database:
BOOK(Book_id, Title, Publisher_Name, Pub_Year)
BOOK_AUTHORS(Book_id, Author_Name)
PUBLISHER(Name, Address, Phone)
BOOK_COPIES(Book_id, Branch_id, No-of_Copies)
BOOK_LENDING(Book_id, Branch_id, Card_No, Date_Out, Due_Date)
LIBRARY_BRANCH(Branch_id, Branch_Name, Address)
Write SQL queries to

1. Retrieve details of all books in the library – id, title, name of publisher, authors, number of copies in each branch, etc.
2. Get the particulars of borrowers who have borrowed more than 3 books, but from Jan 2017 to Jun 2017.
3. Delete a book in BOOK table. Update the contents of other tables to reflect this data manipulation operation.
4. Partition the BOOK table based on year of publication. Demonstrate its working with a simple query.
5. Create a view of all books and its number of copies that are currently available in the Library.

2 Consider the following schema for Order Database:
SALESMAN(Salesman_id, Name, City, Commission)
CUSTOMER(Customer_id, Cust_Name, City, Grade, Salesman_id)
ORDERS(Ord_No, Purchase_Amt, Ord_Date, Customer_id, Salesman_id)
Write SQL queries to

1. Count the customers with grades above Bangalore's average.
2. Find the name and numbers of all salesman who had more than one customer.
3. List all the salesman and indicate those who have and don't have customers in their cities (Use UNION operation.)
4. Create a view that finds the salesman who has the customer with the highest order of a day.

	5. Demonstrate the DELETE operation by removing salesman with id 1000. All his orders must also be deleted.
3	Consider the schema for Movie Database: ACTOR(Act_id, Act_Name, Act_Gender) DIRECTOR(Dir_id, Dir_Name, Dir_Phone) MOVIES(Mov_id, Mov_Title, Mov_Year, Mov_Lang, Dir_id) MOVIE_CAST(Act_id, Mov_id, Role) RATING(Mov_id, Rev_Stars) Write SQL queries to 1. List the titles of all movies directed by 'Hitchcock'. 2. Find the movie names where one or more actors acted in two or more movies. 3. List all actors who acted in a movie before 2000 and also in a movie after 2015 (use JOIN operation). 4. Find the title of movies and number of stars for each movie that has at least one rating and find the highest number of stars that movie received. Sort the result by movie title. 5. Update rating of all movies directed by 'Steven Spielberg' to 5.
4	Consider the schema for College Database: STUDENT(USN, SName, Address, Phone, Gender) SEMSEC(SSID, Sem, Sec) CLASS(USN, SSID) SUBJECT(Subcode, Title, Sem, Credits) IAMARKS(USN, Subcode, SSID, Test1, Test2, Test3, FinalIA) Write SQL queries to 1. List all the student details studying in fourth semester 'C' section. 2. Compute the total number of male and female students in each semester and in each section. 3. Create a view of Test1 marks of student USN '1BI17CS101' in all subjects. 4. Calculate the FinalIA (average of best two test marks) and update the corresponding table for all students. 5. Categorize students based on the following criterion: If FinalIA = 17 to 20 then CAT = 'Outstanding' If FinalIA = 12 to 16 then CAT = 'Average' If FinalIA< 12 then CAT = 'Weak' Give these details only for 8th semester A, B, and C section students.
5	Consider the schema for Company Database: EMPLOYEE(SSN, Name, Address, Sex, Salary, SuperSSN, DNo) DEPARTMENT(DNo, DName, MgrSSN, MgrStartDate) DLOCATION(DNo,DLoc) PROJECT(PNo, PName, PLocation, DNo) WORKS_ON(SSN, PNo, Hours) Write SQL queries to 1. Make a list of all project numbers for projects that involve an employee whose last name is 'Scott', either as a worker or as a manager of the department that controls the project. 2. Show the resulting salaries if every employee working on the 'IoT' project is given a 10 percent raise. 3. Find the sum of the salaries of all employees of the 'Accounts' department, as well as the maximum salary, the minimum salary, and the average salary in this department

	4. Retrieve the name of each employee who works on all the projects controlledby department number 5 (use NOT EXISTS operator). 5. For each department that has more than five employees, retrieve the department number and the number of its employees who are making more than Rs. 6,00,000.
Part B: Mini project	
• For any problem selected, write the ER Diagram, apply ER-mapping rules, normalize the relations, and follow the application development process. • Make sure that the application should have five or more tables, at least one trigger and one stored procedure, using suitable frontend tool. • Indicative areas include; health care, education, industry, transport, supply chain, etc.	
Course outcomes: The students should be able to:	
• Use Structured Query Language (SQL) for database Creation and manipulation. • Demonstrate the working of different concepts of DBMS • Implement and test the project developed for an application.	
Conduction of Practical Examination: 1. All laboratory experiments from part A are to be included for practical examination. 2. Mini project has to be evaluated for 40 Marks. 3. Report should be prepared in a standard format prescribed for project work. 4. Students are allowed to pick one experiment from the lot. 5. Strictly follow the instructions as printed on the cover page of answer script. 6. Marks distribution: a) Part A: Procedure + Conduction + Viva: **09 + 42 +09 =60 Marks** 7. Part B: Demonstration + Report + Viva voce = **20+14+06 = 40** Marks 8. Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.	

CRYPTOGRAPHY, NETWORK SECURITY AND CYBER LAW
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS61	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Introduction - Cyber Attacks, Defence Strategies and Techniques, Guiding Principles, Mathematical Background for Cryptography - Modulo Arithmetic's, The Greatest Comma Divisor, Useful Algebraic Structures, Chinese Remainder Theorem, Basics of Cryptography - Preliminaries, Elementary Substitution Ciphers, Elementary Transport Ciphers, Other Cipher Properties, Secret Key Cryptography – Product Ciphers, DES Construction.	**10 Hours**
Module – 2	
Public Key Cryptography and RSA – RSA Operations, Why Does RSA Work?, Performance, Applications, Practical Issues, Public Key Cryptography Standard (PKCS), Cryptographic Hash - Introduction, Properties, Construction, Applications and Performance, The Birthday Attack, Discrete Logarithm and its Applications - Introduction, Diffie-Hellman Key Exchange, Other Applications.	**10 Hours**
Module – 3	
Key Management - Introduction, Digital Certificates, Public Key Infrastructure, Identity–based Encryption, Authentication–I - One way Authentication, Mutual Authentication, Dictionary Attacks, Authentication – II – Centalised Authentication, The Needham-Schroeder Protocol, Kerberos, Biometrics, IPSec-Security at the Network Layer – Security at Different layers: Pros and Cons, IPSec in Action, Internet Key Exchange (IKE) Protocol, Security Policy and IPSEC, Virtual Private Networks, Security at the Transport Layer - Introduction, SSL Handshake Protocol, SSL Record Layer Protocol, OpenSSL.	**10 Hours**
Module – 4	
IEEE 802.11 Wireless LAN Security - Background, Authentication, Confidentiality and Integrity, Viruses, Worms, and Other Malware, Firewalls – Basics, Practical Issues, Intrusion Prevention and Detection - Introduction, Prevention Versus Detection, Types of Instruction Detection Systems, DDoS Attacks Prevention/Detection, Web Service Security – Motivation, Technologies for Web Services, WS- Security, SAML, Other Standards.	**10 Hours**
Module – 5	
IT act aim and objectives, Scope of the act, Major Concepts, Important provisions, Attribution, acknowledgement, and dispatch of electronic records, Secure electronic records and secure digital signatures, Regulation of certifying authorities: Appointment of Controller and Other officers, Digital Signature certificates, Duties of Subscribers, Penalties and adjudication, The cyber regulations appellate tribunal, Offences, Network service providers not to be liable in certain cases, Miscellaneous Provisions.	**10 Hours**

Course outcomes: The students should be able to:

- Discuss the cryptography and its need to various applications
- Design and Develop simple cryptography algorithms

- Understand the cyber security and need cyber Law

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Cryptography, Network Security and Cyber Laws – Bernard Menezes, Cengage Learning, 2010 edition (Chapters-1,3,4,5,6,7,8,9,10,11,12,13,14,15,19(19.1-19.5),21(21.1-21.2),22(22.1-22.4),25

Reference Books:

1. Cryptography and Network Security- Behrouz A Forouzan, DebdeepMukhopadhyay, Mc-GrawHill, 3rd Edition, 2015
2. Cryptography and Network Security- William Stallings, Pearson Education, 7th Edition
3. Cyber Law simplified- VivekSood, Mc-GrawHill, 11th reprint , 2013
4. Cyber security and Cyber Laws, Alfred Basta, Nadine Basta, Mary brown, ravindrakumar, Cengage learning

COMPUTER GRAPHICS AND VISUALIZATION
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS62	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Overview: Computer Graphics and OpenGL: Computer Graphics:Basics of computer graphics, Application of Computer Graphics, Video Display Devices: Random Scan and Raster Scan displays, color CRT monitors, Flat panel displays. Raster-scan systems: video controller, raster scan Display processor, graphics workstations and viewing systems, Input devices, graphics networks, graphics on the internet, graphics software. OpenGL: Introduction to OpenGL ,coordinate reference frames, specifying two-dimensional world coordinate reference frames in OpenGL, OpenGL point functions, OpenGL line functions, point attributes, line attributes, curve attributes, OpenGL point attribute functions, OpenGL line attribute functions, Line drawing algorithms(DDA, Bresenham's), circle generation algorithms(Bresenham's). **Text-1:Chapter -1: 1-1 to 1-9,2-1 to 2-9 (Excluding 2-5),3-1 to 3-5,3-9,3-20**	**10 Hours**
Module – 2	
Fill area Primitives, 2D Geometric Transformations and 2D viewing: Fill area Primitives: Polygon fill-areas, OpenGL polygon fill area functions, fill area attributes, general scan line polygon fill algorithm, OpenGL fill-area attribute functions. 2DGeometric Transformations: Basic 2D Geometric Transformations, matrix representations and homogeneous coordinates. Inverse transformations, 2DComposite transformations, other 2D transformations, raster methods for geometric transformations, OpenGL raster transformations, OpenGL geometric transformations function, 2D viewing: 2D viewing pipeline, OpenGL 2D viewing functions. **Text-1:Chapter 3-14 to 3-16,4-9,4-10,4-14,5-1 to 5-7,5-17,6-1,6-4**	**10 Hours**
Module – 3	
Clipping,3D Geometric Transformations, Color and Illumination Models: Clipping: clipping window, normalization and viewport transformations, clipping algorithms,2D point clipping, 2D line clipping algorithms: cohen-sutherland line clipping only -polygon fill area clipping: Sutherland-Hodgeman polygon clipping algorithm only.3DGeometric Transformations: 3D translation, rotation, scaling, composite 3D transformations, other 3D transformations, affine transformations, OpenGL geometric transformations functions. Color Models: Properties of light, color models, RGB and CMY color models. Illumination Models: Light sources, basic illumination models-Ambient light, diffuse reflection, specular and phong model, Corresponding openGL functions. **Text-1:Chapter :6-2 to 6-08 (Excluding 6-4),5-9 to 5-17(Excluding 5-15),12-1,12-2,12-4,12-6,10-1,10-3**	**10 Hours**
Module – 4	
3D Viewing and Visible Surface Detection: 3DViewing:3D viewing concepts, 3D viewing pipeline, 3D viewing coordinate parameters , Transformation from	**10 Hours**

world to viewing coordinates, Projection transformation, orthogonal projections, perspective projections, The viewport transformation and 3D screen coordinates. OpenGL 3D viewing functions. Visible Surface Detection Methods: Classification of visible surface Detection algorithms, back face detection, depth buffer method and OpenGL visibility detection functions. **Text-1:Chapter: 7-1 to 7-10(Excluding 7-7), 9-1 to 9-3, 9-14**	
Module – 5	
Input& interaction, Curves and Computer Animation: Input and Interaction: Input devices, clients and servers, Display Lists, Display Lists and Modelling, Programming Event Driven Input, Menus Picking, Building Interactive Models, Animating Interactive programs, Design of Interactive programs, Logic operations .Curved surfaces, quadric surfaces, OpenGL Quadric-Surface and Cubic-Surface Functions, Bezier Spline Curves, Bezier surfaces, OpenGL curve functions. Corresponding openGL functions. **Text-1:Chapter :8-3 to 8-6 (Excluding 8-5),8-9,8-10,8-11,3-8,8-18,13-11,3-2,13-3,13-4,13-10** **Text-2:Chapter 3: 3-1 to 3.11: Input& interaction**	**10 Hours**
Course outcomes: The students should be able to:	
• Design and implement algorithms for 2D graphics primitives and attributes. • Illustrate Geometric transformations on both 2D and 3D objects. • Understand the concepts of clipping and visible surface detection in 2D and 3D viewing, and Illumination Models. • Discussabout suitable hardware and software for developing graphics packages using OpenGL.	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. Donald Hearn & Pauline Baker: Computer Graphics with OpenGL Version,3rd/ 4thEdition, Pearson Education,2011 2. Edward Angel: Interactive Computer Graphics- A Top Down approach with OpenGL, 5th edition. Pearson Education, 2008	
Reference Books:	
1. James D Foley, Andries Van Dam, Steven K Feiner, John F Huges Computer graphics with OpenGL: pearson education 2. Xiang, Plastock : Computer Graphics , sham's outline series, 2nd edition, TMG. 3. Kelvin Sung, Peter Shirley, steven Baer : Interactive Computer Graphics, concepts and applications, Cengage Learning 4. M MRaiker, Computer Graphics using OpenGL, Filip learning/Elsevier	

SYSTEM SOFTWARE AND COMPILER DESIGN
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS63	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Introduction to System Software, Machine Architecture of SIC and SIC/XE. **Assemblers:** Basic assembler functions, machine dependent assembler features, machine independent assembler features, assembler design options. **Macroprocessors:** Basicmacro processor functions, **Text book 1: Chapter 1: 1.1,1.2,1.3.1,1.3.2, Chapter2 : 2.1-2.4,Chapter4: 4.1.1,4.1.2**	**10 Hours**
Module – 2	
Loaders and Linkers: Basic Loader Functions**,** Machine Dependent Loader Features, Machine Independent Loader Features, Loader Design Options, Implementation Examples. **Text book 1 : Chapter 3 ,3.1 -3.5**	**10 Hours**
Module – 3	
Introduction: Language Processors, The structure of a compiler, The evaluation of programming languages, The science of building compiler, Applications of compiler technology, Programming language basics **Lexical Analysis:** The role of lexical analyzer, Input buffering, Specifications of token, recognition of tokens, lexical analyzer generator, Finite automate. **Text book 2:Chapter 1 1.1-1.6 Chapter 3 3.1 – 3.6**	**10 Hours**
Module – 4	
Syntax Analysis: Introduction, Role Of Parsers, Context Free Grammars, Writing a grammar, Top Down Parsers, Bottom-Up Parsers, Operator-Precedence Parsing **Text book 2: Chapter 4 4.1 4.2 4.3 4.4 4.5 4.6 Text book 1 : 5.1.3**	**10 Hours**
Module – 5	
Syntax Directed Translation, Intermediate code generation, Code generation **Text book 2: Chapter 5.1, 5.2, 5.3, 6.1, 6.2, 8.1, 8.2**	**10 Hours**

Course outcomes: The students should be able to:

- Illustrate system software such as assemblers, loaders, linkers and macroprocessors
- Design and develop lexical analyzers, parsers and code generators
- Discuss about lex and yacc tools for implementing different concepts of system software

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. System Software by Leland. L. Beck, D Manjula, 3rd edition, 2012

2. Compilers-Principles, Techniques and Tools by Alfred V Aho, Monica S. Lam, Ravi Sethi, Jeffrey D. Ullman. Pearson, 2nd edition, 2007

Reference Books:

1. Systems programming – Srimanta Pal , Oxford university press, 2016
2. System programming and Compiler Design, K C Louden, Cengage Learning
3. System software and operating system by D. M. Dhamdhere TMG
4. Compiler Design, K Muneeswaran, Oxford University Press 2013.

OPERATING SYSTEMS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS64	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Introduction to operating systems, System structures: What operating systems do; Computer System organization; Computer System architecture; Operating System structure; Operating System operations; Process management; Memory management; Storage management; Protection and Security; Distributed system; Special-purpose systems; Computing environments. Operating System Services; User - Operating System interface; System calls; Types of system calls; System programs; Operating system design and implementation; Operating System structure; Virtual machines; Operating System generation; System boot. **Process Management** Process concept; Process scheduling; Operations on processes; Inter process communication	**10 Hours**
Module – 2	
Multi-threaded Programming: Overview; Multithreading models; Thread Libraries; Threading issues. Process Scheduling: Basic concepts; Scheduling Criteria; Scheduling Algorithms; Multiple-processor scheduling; Thread scheduling. **Process Synchronization:** Synchronization: The critical section problem; Peterson's solution; Synchronization hardware; Semaphores; Classical problems of synchronization; Monitors.	**10 Hours**
Module – 3	
Deadlocks : Deadlocks; System model; Deadlock characterization; Methods for handling deadlocks; Deadlock prevention; Deadlock avoidance; Deadlock detection and recovery from deadlock. **Memory Management:** Memory management strategies: Background; Swapping; Contiguous memory allocation; Paging; Structure of page table; Segmentation.	**10 Hours**
Module – 4	
Virtual Memory Management: Background; Demand paging; Copy-on-write; Page replacement; Allocation of frames; Thrashing. **File System, Implementation of File System:** File system: File concept; Access methods; Directory structure; File system mounting; File sharing; Protection: Implementing File system: File system structure; File system implementation; Directory implementation; Allocation methods; Free space management.	**10 Hours**
Module – 5	
Secondary Storage Structures, Protection: Mass storage structures; Disk structure; Disk attachment; Disk scheduling; Disk management; Swap space management. Protection: Goals of protection, Principles of protection, Domain of protection, Access matrix, Implementation of access matrix, Access control, Revocation of access rights, Capability- Based systems. **Case Study: The Linux Operating System:** Linux history; Design principles; Kernel modules; Process management; Scheduling; Memory Management; File systems, Input and output;	**10 Hours**

Inter-process communication.	

Course outcomes: The students should be able to:

- Demonstrate need for OS and different types of OS
- Discuss suitable techniques for management of different resources
- Illustrate processor, memory, storage and file system commands
- Explain the different concepts of OS in platform of usage through case studies

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Abraham Silberschatz, Peter Baer Galvin, Greg Gagne, Operating System Principles 7th edition, Wiley-India, 2006.

Reference Books

1. Ann McHoes Ida M Fylnn, Understanding Operating System, Cengage Learning, 6th Edition
2. D.M Dhamdhere, Operating Systems: A Concept Based Approach 3rd Ed, McGraw-Hill, 2013.
3. P.C.P. Bhatt, An Introduction to Operating Systems: Concepts and Practice 4th Edition, PHI(EEE), 2014.
4. William Stallings Operating Systems: Internals and Design Principles, 6th Edition, Pearson.

DATA MINING AND DATA WAREHOUSING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS651	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Data Warehousing&modeling: Basic Concepts: Data Warehousing: A multitier Architecture, Data warehouse models: Enterprise warehouse,Data mart and virtual warehouse, Extraction, Transformation and loading, Data Cube: A multidimensional data model, Stars, Snowflakes and Fact constellations: Schemas for multidimensional Data models, Dimensions: The role of concept Hierarchies, Measures: Their Categorization and computation, Typical OLAP Operations.	**8 Hours**
Module – 2	
Data warehouse implementation& Data mining: Efficient Data Cube computation: An overview, Indexing OLAP Data: Bitmap index and join index, Efficient processing of OLAP Queries, OLAP server Architecture ROLAP versus MOLAP Versus HOLAP.**:** Introduction: What is data mining, Challenges, Data Mining Tasks, Data: Types of Data, Data Quality, Data Preprocessing, Measures of Similarity and Dissimilarity,	**8 Hours**
Module – 3	
Association Analysis: Association Analysis: Problem Definition, Frequent Item set Generation, Rule generation. Alternative Methods for Generating Frequent Item sets, FP-Growth Algorithm, Evaluation of Association Patterns.	**8 Hours**
Module – 4	
Classification : Decision Trees Induction,Method for Comparing Classifiers, Rule Based Classifiers, Nearest Neighbor Classifiers,Bayesian Classifiers.	**8 Hours**
Module – 5	
Clustering Analysis: Overview, K-Means, Agglomerative Hierarchical Clustering, DBSCAN, Cluster Evaluation, Density-Based Clustering, Graph-Based Clustering, Scalable Clustering Algorithms.	**8 Hours**

Course outcomes: The students should be able to:

- Understands data mining problems and implement the data warehouse
- Demonstrate the association rules for a given data pattern.
- Discuss between classification and clustering solution.

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Pang-Ning Tan, Michael Steinbach, Vipin Kumar: Introduction to Data Mining,

Pearson, First impression,2014.
2. Jiawei Han, MichelineKamber, Jian Pei: Data Mining -Concepts and Techniques, 3rd Edition,Morgan Kaufmann Publisher, 2012.

Reference Books:

1. Sam Anahory, Dennis Murray: Data Warehousing in the Real World, Pearson,Tenth Impression,2012.
2. Michael.J.Berry,Gordon.S.Linoff: Mastering Data Mining , Wiley Edition, second edtion,2012.

SOFTWARE ARCHITECTURE AND DESIGN PATTERNS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS652	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction: what is a design pattern? describing design patterns, the catalog of design pattern, organizing the catalog, how design patterns solve design problems, how to select a design pattern, how to use a design pattern. What is object-oriented development? , key concepts of object oriented design other related concepts, benefits and drawbacks of the paradigm	**8 Hours**
Module – 2	
Analysis a System: overview of the analysis phase, stage 1: gathering the requirements functional requirements specification, defining conceptual classes and relationships, using the knowledge of the domain. Design and Implementation, discussions and further reading.	**8 Hours**
Module – 3	
Design Pattern Catalog: Structural patterns, Adapter, bridge, composite, decorator, facade, flyweight, proxy.	**8 Hours**
Module – 4	
Interactive systems and the MVC architecture:Introduction , The MVC architectural pattern, analyzing a simple drawing program , designing the system, designing of the subsystems, getting into implementation , implementing undo operation , drawing incomplete items, adding a new feature , pattern based solutions.	**8 Hours**
Module – 5	
Designing with Distributed Objects: Client server system, java remote method invocation, implementing an object oriented system on the web (discussions and further reading) a note on input and output, selection statements, loops arrays.	**8 Hours**

Course outcomes: The students should be able to:

- Design and implement codes with higher performance and lower complexity
- Demonstrate code qualities needed to keep code flexible
- Illustrate design principles and be able to assess the quality of a design with respect to these principles.
- Explain principles in the design of object oriented systems.
- Understand a range of design patterns.
- Discuss suitable patterns in specific contexts

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Object-oriented analysis, design and implementation, brahma dathan, sarnathrammath, universities press,2013
2. Design patterns, erich gamma, Richard helan, Ralph johman , john vlissides ,PEARSON Publication,2013.

Reference Books:

1. Frank Bachmann, ReginеMeunier, Hans Rohnert “Pattern Oriented Software Architecture” –Volume 1, 1996.
2. William J Brown et al., "Anti-Patterns: Refactoring Software, Architectures and Projects in Crisis", John Wiley, 1998.

OPERATIONS RESEARCH
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS653	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction, Linear Programming: Introduction: The origin, natureand impact of OR; Defining the problem and gathering data; Formulating amathematical model; Deriving solutions from the model; Testing the model;Preparing to apply the model; Implementation . **Introduction to Linear Programming Problem (LPP):** Prototype example, Assumptions of LPP, Formulation of LPP and Graphical method various examples.	**8 Hours**
Module – 2	
Simplex Method – 1: The essence of the simplex method; Setting up the simplex method; Types of variables, Algebraof the simplex method; the simplex method in tabular form; Tie breaking inthe simplex method, Big M method, Two phase method.	**8 Hours**
Module – 3	
Simplex Method – 2: Duality Theory - The essence of duality theory, Primaldual relationship, conversion of primal to dual problem and vice versa. The dual simplex method.	**8 Hours**
Module – 4	
Transportation and Assignment Problems: The transportation problem, Initial Basic Feasible Solution (IBFS) by North West Corner Rule method, Matrix Minima Method, Vogel's Approximation Method. Optimal solution by Modified Distribution Method (MODI). The Assignment problem; A Hungarian algorithm for the assignment problem. Minimization and Maximization varieties in transportation and assignment problems.	**8 Hours**
Module – 5	
Game Theory: Game Theory: The formulation of twopersons, zero sum games; saddle point, maximin and minimax principle, Solving simple games- a prototype example;Games with mixed strategies; Graphical solution procedure. **Metaheuristics:** The nature of Metaheuristics, Tabu Search, SimulatedAnnealing, Genetic Algorithms.	**8 Hours**

Course outcomes: The students should be able to:

- Explain optimization techniques for various problems.
- Understand the given problem as transportation and assignment problem and solve.
- Illustrate game theory for decision support system.

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:
1. D.S. Hira and P.K. Gupta, Operations Research, (Revised Edition), Published by S. Chand & Company Ltd, 2014
Reference Books:
1. S Kalavathy, Operation Research, Vikas Publishing House Pvt Limited, 01-Aug-2002 2. S D Sharma, Operation Research,KedarNath Ram Nath Publishers.

DISTRIBUTED COMPUTING SYSTEM
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CS654	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Characterization of Distributed Systems: Introduction, Examples of DS, Resource sharing and the Web, Challenges **System Models:** Architectural Models, Fundamental Models	**8 Hours**
Module – 2	
Inter Process Communication: Introduction, API for Internet Protocols, External Data Representation and Marshalling, Client – Server Communication, Group Communication **Distributed Objects and RMI:** Introduction, Communication between Distributed Objects, RPC, Events and Notifications	**8 Hours**
Module – 3	
Operating System Support: Introduction, The OS layer, Protection, Processes and Threads, Communication and Invocation , Operating system architecture **Distributed File Systems:** Introduction, File Service architecture, Sun Network File System	**8 Hours**
Module – 4	
Time and Global States: Introduction, Clocks, events and process status, Synchronizing physical clocks, Logical time and logical clocks, Global states **Coordination and Agreement:** Introduction, Distributed mutual exclusion, Elections	**8 Hours**
Module – 5	
Distributed Transactions: Introduction, Flat and nested distributed transactions, Atomic commit protocols, Concurrency control in distributed transactions, distributed deadlocks	**8 Hours**

Course outcomes: The students should be able to:

- Explain the characteristics of a distributed system along with its and design challenges
- Illustrate the mechanism of IPC between distributed objects
- Describe the distributed file service architecture and the important characteristics of SUN NFS.
- Discuss concurrency control algorithms applied in distributed transactions

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. George Coulouris, Jean Dollimore and Tim Kindberg: Distributed Systems – Concepts and Design, 5th Edition, Pearson Publications, 2009

Reference Books:

1. Andrew S Tanenbaum: Distributed Operating Systems, 3rd edition, Pearson publication, 2007
2. Ajay D. Kshemkalyani and MukeshSinghal, Distributed Computing: Principles, Algorithms and Systems, Cambridge University Press, 2008
3. SunitaMahajan, Seema Shan, " Distributed Computing", Oxford University Press,2015

MOBILE APPLICATION DEVELOPMENT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VI

Subject Code	17CS661	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Get started, Build your first app, Activities, Testing, debugging and using support libraries	**8 Hours**
Module – 2	
User Interaction, Delightful user experience, Testing your UI	**8 Hours**
Module – 3	
Background Tasks, Triggering, scheduling and optimizing background tasks	**8 Hours**
Module – 4	
All about data, Preferences and Settings, Storing data using SQLite, Sharing data with content providers, Loading data using Loaders	**8 Hours**
Module – 5	
Permissions, Performance and Security, Firebase and AdMob, Publish	**8 Hours**

Course outcomes: The students should be able to:

- Design and Develop Android application by setting up Android development environment
- Implement adaptive, responsive user interfaces that work across a wide range of devices.
- Explainlong running tasks and background work in Android applications
- Demonstrate methods in storing, sharing and retrieving data in Android applications
- Discuss the performance of android applications and understand the role of permissions and security
- Describe the steps involved in publishing Android application to share with the world

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Google Developer Training, "Android Developer Fundamentals Course – Concept Reference", Google Developer Training Team, 2017. https://www.gitbook.com/book/google-developer-training/android-developer-fundamentals-course-concepts/details (Download pdf file from the above link)

Reference Books:

1. Erik Hellman, "Android Programming – Pushing the Limits", 1st Edition, Wiley India Pvt Ltd, 2014.
2. Dawn Griffiths and David Griffiths, "Head First Android Development", 1st Edition, O'Reilly SPD Publishers, 2015.
3. J F DiMarzio, "Beginning Android Programming with Android Studio", 4th Edition,

Wiley India Pvt Ltd, 2016. ISBN-13: 978-8126565580
4. AnubhavPradhan, Anil V Deshpande, " Composing Mobile Apps" using Android, Wiley 2014, ISBN: 978-81-265-4660-2

BIG DATA ANALYTICS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VI

Subject Code	17CS662	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction to Data Analytics and Decision Making: Introduction, Overview of the Book, The Methods, The Software, Modeling and Models, Graphical Models, Algebraic Models, Spreadsheet Models, Seven-Step ModelingProcess.**Describing the Distribution of a Single Variable**:Introduction,Basic Concepts, Populations and Samples, Data Sets,Variables,and Observations, Types of Data, Descriptive Measures for Categorical Variables, Descriptive Measures for Numerical Variables, Numerical Summary Measures, Numerical Summary Measures with StatTools,Charts for Numerical Variables, Time Series Data, Outliers and Missing Values,Outliers,Missing Values, Excel Tables for Filtering,Sorting,and Summarizing. **Finding Relationships among Variables**: Introduction, Relationships among Categorical Variables, Relationships among Categorical Variables and a Numerical Variable, Stacked and Unstacked Formats, Relationships among Numerical Variables, Scatterplots, Correlation and Covariance, Pivot Tables.	**08 Hours**
Module – 2	
Probability and Probability Distributions:Introduction,Probability Essentials, Rule of Complements, Addition Rule, Conditional Probability and the Multiplication Rule, Probabilistic Independence, Equally Likely Events, Subjective Versus Objective Probabilities, Probability Distribution of a Single Random Variable, Summary Measures of a Probability Distribution, Conditional Mean and Variance, Introduction to Simulation. **Normal,Binormal,Poisson,and Exponential Distributions**:Introduction,The Normal Distribution, Continuous Distributions and Density Functions, The Normal Density,Standardizing:Z-Values,Normal Tables and Z-Values, Normal Calculations in Excel, Empirical Rules Revisited, Weighted Sums of Normal Random Variables, Applications of the Normal Random Distribution, The Binomial Distribution, Mean and Standard Deviation of the Binomial Distribution, The Binomial Distribution in the Context of Sampling, The Normal Approximation to the Binomial, Applications of the Binomial Distribution, The Poisson and Exponential Distributions, The Poisson Distribution, The Exponential Distribution.	**08 Hours**
Module – 3	
Decision Making under Uncertainty:Introduction,Elements of Decision Analysis, Payoff Tables, Possible Decision Criteria, Expected Monetary Value(EMY),Sensitivity Analysis, Decision Trees, Risk Profiles, The Precision Tree Add-In,Bayes' Rule, Multistage Decision Problems and the Value of Information, The Value of Information, Risk Aversion and Expected Utility, Utility Functions, Exponential Utility, Certainty Equivalents, Is Expected Utility	**08 Hours**

Maximization Used? **Sampling and Sampling Distributions**: Introduction, Sampling Terminology, Methods for Selecting Random Samples, Simple Random Sampling, Systematic Sampling, Stratified Sampling, Cluster Sampling, Multistage Sampling Schemes, Introduction to Estimation, Sources of Estimation Error, Key Terms in Sampling, Sampling Distribution of the Sample Mean, The Central Limit Theorem, Sample Size Selection, Summary of Key Ideas for Simple Random Sampling.	
Module – 4	
Confidence Interval Estimation: Introduction, Sampling Distributions, The t Distribution, Other Sampling Distributions, Confidence Interval for a Mean, Confidence Interval for a Total, Confidence Interval for a Proportion, Confidence Interval for a Standard Deviation, Confidence Interval for the Difference between Means, Independent Samples, Paired Samples, Confidence Interval for the Difference between Proportions, Sample Size Selection, Sample Size Selection for Estimation of the Mean, Sample Size Selection for Estimation of Other Parameters. **Hypothesis Testing**:Introduction,Concepts in Hypothesis Testing, Null and Alternative Hypothesis, One-Tailed Versus Two-Tailed Tests, Types of Errors, Significance Level and Rejection Region, Significance from p-values, Type II Errors and Power, Hypothesis Tests and Confidence Intervals, Practical versus Statistical Significance, Hypothesis Tests for a Population Mean, Hypothesis Tests for Other Parameters, Hypothesis Tests for a Population Proportion, Hypothesis Tests for Differences between Population Means, Hypothesis Test for Equal Population Variances, Hypothesis Tests for Difference between Population Proportions, Tests for Normality, Chi-Square Test for Independence.	**08 Hours**
Module – 5	
Regression Analysis: Estimating Relationships: Introduction, Scatterplots : Graphing Relationships, Linear versus Nonlinear Relationships,Outliers,Unequal Variance, No Relationship,Correlations:Indications of Linear Relationships, Simple Linear Regression, Least Squares Estimation, Standard Error of Estimate, The Percentage of Variation Explained:R-Square,Multiple Regression, Interpretation of Regression Coefficients, Interpretation of Standard Error of Estimate and R-Square, Modeling Possibilities, Dummy Variables, Interaction Variables, Nonlinear Transformations, Validation of the Fit. **Regression Analysis**: Statistical Inference:Introduction,The Statistical Model, Inferences About the Regression Coefficients, Sampling Distribution of the Regression Coefficients, Hypothesis Tests for the Regression Coefficients and p-Values, A Test for the Overall Fit: The ANOVA Table,Multicollinearity,Include/Exclude Decisions, Stepwise Regression,Outliers,Violations of Regression Assumptions,Nonconstant Error Variance,Nonnormality of Residuals,Autocorrelated Residuals ,Prediction.	**08 Hours**
Course outcomes: The students should be able to:	
• Explain the importance of data and data analysis • Interpret the probabilistic models for data • Illustrate hypothesis, uncertainty principle • Demonstrate the regression analysis	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module.	

Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.
Text Books:
1. S C Albright and W L Winston, Business analytics: data analysis and decision making, 5/e Cenage Learning
Reference Books:

WIRELESS NETWORKS AND MOBILE COMPUTING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VI

Subject Code	17CS663	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Mobile Communication, Mobile Computing, Mobile Computing Architecture, Mobile Devices Mobile System Networks, Data Dissemination, Mobility Management, Security Cellular Networks and Frequency Reuse, Mobile Smartphone, Smart Mobiles, and Systems Handheld Pocket Computers, Handheld Devices, Smart Systems, Limitations of Mobile Devices Automotive Systems	**8 Hours**
Module – 2	
GSM-Services and System Architecture, Radio Interfaces of GSM, Protocols of GSM Localization, Call Handling Handover, Security, New Data Services, General Packet Radio Service High-speed Circuit Switched Data, DECT, Modulation, Multiplexing, Controlling the Medium Access Spread Spectrum, Frequency Hopping Spread Spectrum (FHSS),Coding Methods, Code Division Multiple Access, IMT-2000 3G Wireless Communication Standards, WCDMA 3G Communications Standards ,CDMMA2000 3G Communication Standards, I-mode, OFDM, High Speed Packet Access (HSPA) 3G Network Long-term Evolution, WiMaxRel 1.0 IEEE 802.16e, Broadband Wireless Access,4G Networks, Mobile Satellite Communication Networks	**8 Hours**
Module – 3	
IP and Mobile IP Network Layers, Packet Delivery and Handover Management Location Management, Registration, Tunnelling and Encapsulation, Route Optimization Dynamic Host Configuration Protocol, VoIP, IPsec Conventional TCP/IP Transport Layer Protocols, Indirect TCP, Snooping TCP Mobile TCP, Other Methods of Mobile TCP-layer Transmission ,TCP over 2.5G/3G Mobile Networks	**8 Hours**
Module – 4	
Data Organization, Database Transactional Models – ACID Rules, Query Processing Data Recovery Process, Database Hoarding Techniques , Data Caching, Client-Server Computing for Mobile Computing and Adaptation Adaptation Software for Mobile Computing, Power-Aware Mobile Computing, Context-aware Mobile Computing	**8 Hours**
Module – 5	
Communication Asymmetry, Classification of Data-delivery Mechanisms, Data Dissemination Broadcast Models, Selective Tuning and Indexing techniques, Digital Audio Broadcasting (DAB), Digital Video Broadcasting Synchronization, Synchronization Software for Mobile Devices, Synchronization Software for Mobile Devices SyncML-Synchronization Language for Mobile Computing,Sync4J (Funambol), Synchronized Multimedia Markup Language (SMIL)	**8 Hours**
Course outcomes: The students should be able to:	

- Understand the various mobile communication systems.
- Describe various multiplexing systems used in mobile computing.
- Explain the use and importance of data synchronization in mobile computing

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Raj kamal: Mobile Computing, 2ND EDITION, Oxford University Press, 2007/2012
2. MartynMallik: Mobile and Wireless Design Essentials, Wiley India, 2003

Reference Books:

1. Ashok Talukder, RoopaYavagal, Hasan Ahmed: Mobile Computing, Technology, Applications and Service Creation, 2nd Edition, Tata McGraw Hill, 2010.
2. ItiSahaMisra: Wireless Communications and Networks, 3G and Beyond, Tata McGraw Hill, 2009.

PYTHON APPLICATION PROGRAMMING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VI

Subject Code	17CS664	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Why should you learn to write programs, Variables, expressions and statements, Conditional execution, Functions	**8 Hours**
Module – 2	
Iteration, Strings, Files	**8 Hours**
Module – 3	
Lists, Dictionaries, Tuples, Regular Expressions	**8 Hours**
Module – 4	
Classes and objects, Classes and functions, Classes and methods	**8 Hours**
Module – 5	
Networked programs, Using Web Services, Using databases and SQL	**8 Hours**

Course outcomes: The students should be able to:

- Understand Python syntax and semantics and be fluent in the use of Python flow control and functions.
- Demonstrate proficiency in handling Strings and File Systems.
- Implement Python Programs using core data structures like Lists, Dictionaries and use Regular Expressions.
- Interpret the concepts of Object-Oriented Programming as used in Python.
- Implement exemplary applications related to Network Programming, Web Services and Databases in Python.

Question paper pattern:
The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Charles R. Severance, "Python for Everybody: Exploring Data Using Python 3", 1st Edition, CreateSpace Independent Publishing Platform, 2016. (http://do1.dr-chuck.com/pythonlearn/EN_us/pythonlearn.pdf) (Chapters 1 – 13, 15)
2. Allen B. Downey, "Think Python: How to Think Like a Computer Scientist", 2nd Edition, Green Tea Press, 2015. (http://greenteapress.com/thinkpython2/thinkpython2.pdf) (Chapters 15, 16, 17)(Download pdf files from the above links)

Reference Books:

1. Charles Dierbach, "Introduction to Computer Science Using Python", 1st Edition, Wiley India Pvt Ltd. ISBN-13: 978-8126556014
2. Mark Lutz, "Programming Python", 4th Edition, O'Reilly Media, 2011.ISBN-13: 978-9350232873

3. Wesley J Chun, “Core Python Applications Programming”, 3rd Edition,Pearson Education India, 2015. ISBN-13: 978-9332555365
4. Roberto Tamassia, Michael H Goldwasser, Michael T Goodrich, “Data Structures and Algorithms in Python”,1st Edition, Wiley India Pvt Ltd, 2016. ISBN-13: 978-8126562176
5. ReemaThareja, “Python Programming using problem solving approach”, Oxford university press, 2017

SERVICE ORIENTED ARCHITECTURE
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VI

Subject Code	17CS665	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
SOA BASICS:Software Architecture;Need for Software Architecture, Objectives of Software Architecture, Types of IT Architecture, Architecture Patterns and Styles, **Service oriented Architecture;**Service Orientation in Daily Life, Evolution of SOA, Drives for SOA, Dimension of SOA, Key components, perspective of SOA, **Enterprise-wide SOA;** Considerations for Enterprise-Wide SOA, Strawman Architecture For Enterprise-Wide-SOA-Enterprise, SOA-Layers, Application Development Process, SOA Methodology For Enterprise **Text 1: Ch2: 2.1 – 2.4; Ch3:3.1-3.7; Ch4: 4.1 – 4.5**	**8 Hours**
Module – 2	
Enterprise Applications;Architecture Considerations, Solution Architecture for enterprise application, **Software platforms for enterprise Applications;**Package Application Platforms, Enterprise Application Platforms, **Service-oriented-Enterprise Applications;** Considerations for Service-Oriented Enterprise Applications, Patterns for SOA, Pattern-Based Architecture for Service-Oriented Enterprise Application(java reference model only).Composite Applications, SOA programming models. **Text 1: Ch5:5.1, 5.2, 6.1, 6.2**(PageNo 74-81)**, 7.1 – 7.5**	**8 Hours**
Module – 3	
SOA ANALYSIS AND DESIGN;Need For Models, Principles of Service Design, Design of Activity Services, Design of Datasevices, Design of Client services and Design of business process services, **Technologies of SOA;**Technologies For Service Enablement, Technologies For Service Integration, Technologies for Service orchestration. **Text 1: Ch 8: 8.1 – 8.6, 9.1 – 9.3**	**8 Hours**
Module – 4	
Business case for SOA; Stakeholder OBJECTIVES, Benefits of SOA, Cost Savings, Return on Investment, SOA Governance, **Security and implementation;** SOA Governance, SOA Security, approach for enterprise wide SOA implementation,**Trends in SOA;** Technologies in Relation to SOA, Advances in SOA. **Text 1: Ch 10: 10.1 -10.4, Ch 11: 11.1 to 11.3, Ch12:12.2, 12.3**	**8 Hours**
Module – 5	
SOA Technologies-PoC;Loan Management System(LMS), PoC-Requirements Architectures of LMS **SOA based integration;**integrating existing application, **SOA best practices,** Basic SOA using REST. Role of WSDL,SOAP and JAVA/XML Mapping in SOA. **Text 1:Page No 245-248; ReferenceBook:Chapter3; Text 1:Page No 307-310** **Text 2: Ch 3, Ch4**	**8 Hours**
Course outcomes: The students should be able to:	

- Understand the different IT architectures
- Explain SOA based applications
- Illustrate web service and realization of SOA
- DiscussRESTful services

Question paper pattern:

The question paper will have TEN questions.
There will be TWO questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

1. Shankar Kambhampaly, "Service–Oriented Architecture for Enterprise Applications",Wiley Second Edition, 2014.
2. Mark D. Hansen, "SOA using Java Web Services", Practice Hall, 2007.

Reference Books:

1. WaseemRoshen, "SOA-Based Enterprise Integration", Tata McGraw-HILL, 2009.

MULTI-CORE ARCHITECTURE AND PROGRAMMING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VI

Subject Code	17CS666	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction to Multi-core Architecture Motivation for Concurrency in software, Parallel Computing Platforms, Parallel Computing in Microprocessors, Differentiating Multi-core Architectures from Hyper- Threading Technology, Multi-threading on Single-Core versus Multi-Core Platforms Understanding Performance, Amdahl's Law, Growing Returns: Gustafson's Law. **System Overview of Threading** : Defining Threads, System View of Threads, Threading above the Operating System, Threads inside the OS, Threads inside the Hardware, What Happens When a Thread Is Created, Application Programming Models and Threading, Virtual Environment: VMs and Platforms, Runtime Virtualization, System Virtualization.	**8 Hours**
Module – 2	
Fundamental Concepts of Parallel Programming : Designing for Threads, Task Decomposition, Data Decomposition, Data Flow Decomposition, Implications of Different Decompositions, Challenges You'll Face, Parallel Programming Patterns, A Motivating Problem: Error Diffusion, Analysis of the Error Diffusion Algorithm, An Alternate Approach: Parallel Error Diffusion, Other Alternatives. **Threading and Parallel Programming Constructs:** Synchronization, Critical Sections, Deadlock, Synchronization Primitives, Semaphores, Locks, Condition Variables, Messages, Flow Control- based Concepts, Fence, Barrier, Implementation-dependent Threading Features	**8 Hours**
Module – 3	
Threading APIs : ThreadingAPls for Microsoft Windows, Win32/MFC Thread APls, Threading APls for Microsoft. NET Framework, Creating Threads, Managing Threads, Thread Pools, Thread Synchronization, POSIX Threads, Creating Threads, Managing Threads, Thread Synchronization, Signaling, Compilation and Linking.	**8 Hours**
Module – 4	
OpenMP: A Portable Solution for Threading : Challenges in Threading a Loop, Loop-carried Dependence, Data-race Conditions, Managing Shared and Private Data, Loop Scheduling and Portioning, Effective Use of Reductions, Minimizing Threading Overhead, Work-sharing Sections, Performance-oriented Programming, Using Barrier and No wait, Interleaving Single-thread and Multi-thread Execution, Data Copy-in and Copy-out, Protecting Updates of Shared Variables, Intel Task queuing Extension to OpenMP, OpenMP Library Functions, OpenMP Environment Variables, Compilation, Debugging, performance	**8 Hours**
Module – 5	
Solutions to Common Parallel Programming Problems : Too Many Threads, Data Races, Deadlocks, and Live Locks, Deadlock, Heavily Contended Locks,	**8 Hours**

Priority Inversion, Solutions for Heavily Contended Locks, Non-blocking Algorithms, ABA Problem, Cache Line Ping-ponging, Memory Reclamation Problem, Recommendations, Thread-safe Functions and Libraries, Memory Issues, Bandwidth, Working in the Cache, Memory Contention, Cache-related Issues, False Sharing, Memory Consistency, Current IA-32 Architecture, Itanium Architecture, High-level Languages, Avoiding Pipeline Stalls on IA-32,Data Organization for High Performance.	
Course outcomes: The students should be able to:	
• Identify the issues involved in multicore architectures • Explain fundamental concepts of parallel programming and its design issues • Solve the issues related to multiprocessing and suggest solutions • Discuss salient features of different multicore architectures and how they exploit parallelism • Illustrate OpenMP and programming concept	
Question paper pattern: The question paper will have TEN questions. There will be TWO questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer FIVE full questions, selecting ONE full question from each module.	
Text Books:	
1. Multicore Programming , Increased Performance through Software Multi-threading by ShameemAkhter and Jason Roberts , Intel Press , 2006	
Reference Books:	
NIL	

SYSTEM SOFTWARE AND OPERATING SYSTEM LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CSL67	IA Marks	40
Number of Lecture Hours/Week	01I + 02P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description (If any):

Exercises to be prepared with minimum three files (Where ever necessary):

i. Header file.
ii. Implementation file.
iii. Application file where main function will be present.

The idea behind using three files is to differentiate between the developer and user sides. In the developer side, all the three files could be made visible. For the user side only header file and application files could be made visible, which means that the object code of the implementation file could be given to the user along with the interface given in the header file, hiding the source file, if required. Avoid I/O operations (printf/scanf) and use ***data input file*** where ever it is possible

Lab Experiments:

1.
   a) Write a LEX program to recognize valid ***arithmetic expression.*** Identifiers in the expression could be only integers and operators could be + and *. Count the identifiers & operators present and print them separately.

   b) Write YACC program to evaluate ***arithmetic expression*** involving operators: +, -, *, and /

2. Develop, Implement and Execute a program using YACC tool to recognize all strings ending with ***b*** preceded by ***na's*** using the grammar $a^n b$ (note: input ***n*** value)

3. Design, develop and implement YACC/C program to construct ***Predictive / LL(1) Parsing Table*** for the grammar rules: $A \rightarrow aBa$, $B \rightarrow bB \mid \varepsilon$. Use this table to parse the sentence: ***abba$***

4. Design, develop and implement YACC/C program to demonstrate ***Shift Reduce Parsing*** techniquefor the grammar rules: $E \rightarrow E+T \mid T$, $T \rightarrow T*F \mid F$, $F \rightarrow (E) \mid id$ and parse the sentence: ***id + id * id***.
5. Design, develop and implement a C/Java program to generate the machine code using ***Triples*** for the statement ***A = -B * (C +D)*** whose intermediate code in three-address form:

   T1 = -B
   T2 = C + D
   T3 = T1 + T2
   A = T3

6. a) Write a LEX program to eliminate ***comment lines*** in a ***C*** program and copy the

resulting program into a separate file.

b) Write YACC program to recognize valid ***identifier, operators and keywords*** in the given text (*C program*) file.

7. Design, develop and implement a C/C++/Java program to simulate the working of Shortest remaining time and Round Robin (RR) scheduling algorithms. Experiment with different quantum sizes for RR algorithm.
8. Design, develop and implement a C/C++/Java program to implement Banker's algorithm. Assume suitable input required to demonstrate the results.
9. Design, develop and implement a C/C++/Java program to implement page replacement algorithms LRU and FIFO. Assume suitable input required to demonstrate the results.

Study Experiment / Project:

NIL

Course outcomes: The students should be able to:

- Implement and demonstrate Lexer's and Parser's
- Implement different algorithms required for management, scheduling, allocation and communication used in operating system.

Conduction of Practical Examination:

- All laboratory experiments are to be included for practical examination.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script
- Marks distribution: Procedure + Conduction + Viva:**15 + 70 +15 (100)**
- **Change of experiment is allowed only once and marks allotted to the procedure part to be made zero**

COMPUTER GRAPHICS LABORATORY WITH MINI PROJECT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VI

Subject Code	17CSL68	IA Marks	40
Number of Lecture Hours/Week	01I + 02P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description (If any):

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Lab Experiments:

PART A

Design, develop, and implement the following programs using OpenGL API

1. Implement Brenham's line drawing algorithm for all types of slope.
   Refer:Text-1: Chapter 3.5
   Refer:Text-2: Chapter 8
2. Create and rotate a triangle about the origin and a fixed point.
   Refer:Text-1: Chapter 5-4
3. Draw a colour cube and spin it using OpenGL transformation matrices.
   Refer:Text-2: Modelling a Coloured Cube
4. Draw a color cube and allow the user to move the camera suitably to experiment with perspective viewing.
   Refer:Text-2: Topic: Positioning of Camera
5. Clip a lines using Cohen-Sutherland algorithm
   Refer:Text-1: Chapter 6.7
   Refer:Text-2: Chapter 8
6. To draw a simple shaded scene consisting of a tea pot on a table. Define suitably the position and properties of the light source along with the properties of the surfaces of the solid object used in the scene.
   Refer:Text-2: Topic: Lighting and Shading
7. Design, develop and implement recursively subdivide a tetrahedron to form 3D sierpinski gasket. The number of recursive steps is to be specified by the user.
   Refer: Text-2: Topic:sierpinski gasket.
8. Develop a menu driven program to animate a flag using Bezier Curve algorithm
   Refer: Text-1: Chapter 8-10
9. Develop a menu driven program to fill the polygon using scan line algorithm

Project:

PART –B (MINI-PROJECT) :

Student should develop mini project on the topics mentioned below or similar applications using Open GL API. Consider all types of attributes like color, thickness, styles, font, background, speed etc., while doing mini project.

(**During the practical exam: the students should demonstrate and answer Viva-Voce**)

Sample Topics:

Simulation of concepts of OS, Data structures, algorithms etc.

Course outcomes: The students should be able to:

- Apply the concepts of computer graphics
- Implement computer graphics applications using OpenGL
- Implement real world problems using OpenGL

Conduction of Practical Examination:

1. All laboratory experiments from part A are to be included for practical examination.
2. Mini project has to be evaluated for 40 Marks.
3. Report should be prepared in a standard format prescribed for project work.
4. Students are allowed to pick one experiment from the lot.
5. Strictly follow the instructions as printed on the cover page of answer script.
6. Marks distribution:
   a) Part A: Procedure + Conduction + Viva: **09 + 42 +09 =60 Marks**
   b) Part B: Demonstration + Report + Viva voce = **20+14+06 = 40 Marks**
7. Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.

Reference books:

1. Donald Hearn & Pauline Baker: Computer Graphics-OpenGL Version,3rd Edition, Pearson Education,2011
2. Edward Angel: Interactive computer graphics- A Top Down approach with OpenGL, 5th edition. Pearson Education, 2011
3. M MRaikar, Computer Graphics using OpenGL, Fillip Learning / Elsevier, Bangalore / New Delhi (2013)

WEB TECHNOLOGY AND ITS APPLICATIONS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	**17CS71**	IA Marks	40
Number of Lecture Hours/Week	04	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Introduction to HTML, What is HTML and Where did it come from?, HTML Syntax, Semantic Markup, Structure of HTML Documents, Quick Tour of HTML Elements, HTML5 Semantic Structure Elements, Introduction to CSS, What is CSS, CSS Syntax, Location of Styles, Selectors, The Cascade: How Styles Interact, The Box Model, CSS Text Styling.	**10 Hours**
Module – 2	
HTML Tables and Forms, Introducing Tables, Styling Tables, Introducing Forms, Form Control Elements, Table and Form Accessibility, Microformats, Advanced CSS: Layout, Normal Flow, Positioning Elements, Floating Elements, Constructing Multicolumn Layouts, Approaches to CSS Layout, Responsive Design, CSS Frameworks.	**10 Hours**
Module – 3	
JavaScript: Client-Side Scripting, What is JavaScript and What can it do?, JavaScript Design Principles, Where does JavaScript Go?, Syntax, JavaScript Objects, The Document Object Model (DOM), JavaScript Events, Forms, Introduction to Server-Side Development with PHP, What is Server-Side Development, A Web Server's Responsibilities, Quick Tour of PHP, Program Control, Functions	**10 Hours**
Module – 4	
PHP Arrays and Superglobals, Arrays, $_GET and $_POST Superglobal Arrays, $_SERVER Array, $_Files Array, Reading/Writing Files, PHP Classes and Objects, Object-Oriented Overview, Classes and Objects in PHP, Object Oriented Design, Error Handling and Validation, What are Errors and Exceptions?, PHP Error Reporting, PHP Error and Exception Handling	**10 Hours**
Module – 5	
Managing State, The Problem of State in Web Applications, Passing Information via Query Strings, Passing Information via the URL Path, Cookies, Serialization, Session State, HTML5 Web Storage, Caching, Advanced JavaScript and jQuery, JavaScript Pseudo-Classes, jQuery Foundations, AJAX, Asynchronous File Transmission, Animation, Backbone MVC Frameworks, XML Processing and Web Services, XML Processing, JSON, Overview of Web Services.	**10 Hours**

Course Outcomes: After studying this course, students will be able to

- Define HTML and CSS syntax and semantics to build web pages.
- Understand the concepts of Construct , visually format tables and forms using HTML using CSS
- Develop Client-Side Scripts using JavaScript and Server-Side Scripts using PHP to generate and display the contents dynamically.
- List the principles of object oriented development using PHP
- Illustrate JavaScript frameworks like jQuery and Backbone which facilitates

developer to focus on core features.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Randy Connolly, Ricardo Hoar, **"Fundamentals of Web Development",** 1st Edition, Pearson Education India. (**ISBN:**978-9332575271)

Reference Books:

1) Robin Nixon, **"Learning PHP, MySQL &JavaScript with jQuery, CSS and HTML5",** 4th Edition, O'Reilly Publications, 2015. (**ISBN:**978-9352130153)
2) Luke Welling, Laura Thomson, **"PHP and MySQL Web Development",** 5th Edition, Pearson Education, 2016. (**ISBN:**978-9332582736)
3) Nicholas C Zakas, **"Professional JavaScript for Web Developers",** 3rd Edition, Wrox/Wiley India, 2012. (**ISBN:**978-8126535088)
4) David Sawyer Mcfarland, **"JavaScript & jQuery: The Missing Manual",** 1st Edition, O'Reilly/Shroff Publishers & Distributors Pvt Ltd, 2014 (**ISBN:**978-9351108078)
5) Zak Ruvalcaba Anne Boehm, **"Murach's HTML5 and CSS3"**, 3rd Edition, Murachs/Shroff Publishers & Distributors Pvt Ltd, 2016. (**ISBN:**978-9352133246)

ADVANCED COMPUTER ARCHITECTURES
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS72	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Theory of Parallelism: Parallel Computer Models, The State of Computing, Multiprocessors and Multicomputer ,Multivector and SIMD Computers ,PRAM and VLSI Models, Program and Network Properties ,Conditions of Parallelism, Program Partitioning and Scheduling, Program Flow Mechanisms, System Interconnect Architectures, Principles of Scalable Performance, Performance Metrics and Measures, Parallel Processing Applications, Speedup Performance Laws, Scalability Analysis and Approaches.	**10 Hours**
Module – 2	
Hardware Technologies: Processors and Memory Hierarchy, Advanced Processor Technology, Superscalar and Vector Processors, Memory Hierarchy Technology, Virtual Memory Technology.	**10 Hours**
Module – 3	
Bus, Cache, and Shared Memory ,Bus Systems ,Cache Memory Organizations ,Shared Memory Organizations ,Sequential and Weak Consistency Models ,Pipelining and Superscalar Techniques ,Linear Pipeline Processors ,Nonlinear Pipeline Processors ,Instruction Pipeline Design ,Arithmetic Pipeline Design (Upto 6.4).	**10 Hours**
Module – 4	
Parallel and Scalable Architectures: Multiprocessors and Multicomputers ,Multiprocessor System Interconnects, Cache Coherence and Synchronization Mechanisms, Three Generations of Multicomputers ,Message-Passing Mechanisms ,Multivector and SIMD Computers ,Vector Processing Principles ,Multivector Multiprocessors ,Compound Vector Processing ,SIMD Computer Organizations (Upto 8.4),Scalable, Multithreaded, and Dataflow Architectures, Latency-Hiding Techniques, Principles of Multithreading, Fine-Grain Multicomputers, Scalable and Multithreaded Architectures, Dataflow and Hybrid Architectures.	**10 Hours**
Module – 5	
Software for parallel programming: Parallel Models, Languages, and Compilers ,Parallel Programming Models, Parallel Languages and Compilers ,Dependence Analysis of Data Arrays ,Parallel Program Development and Environments, Synchronization and Multiprocessing Modes. Instruction and System Level Parallelism, Instruction Level Parallelism ,Computer Architecture ,Contents, Basic Design Issues ,Problem Definition ,Model of a Typical Processor ,Compiler-detected Instruction Level Parallelism ,Operand Forwarding ,Reorder Buffer, Register Renaming ,Tomasulo's Algorithm ,Branch Prediction, Limitations in Exploiting Instruction Level Parallelism ,Thread Level Parallelism.	**10 Hours**
Course outcomes: The students should be able to:	

- Understand the concepts of parallel computing and hardware technologies
- Illustrate and contrast the parallel architectures
- Recall parallel programming concepts

Question paper pattern

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Kai Hwang and Naresh Jotwani, Advanced Computer Architecture (SIE): Parallelism, Scalability, Programmability, McGraw Hill Education 3/e. 2015

Reference Books:

1. John L. Hennessy and David A. Patterson, Computer Architecture: A quantitative approach, 5th edition, Morgan Kaufmann Elseveir, 2013

MACHINE LEARNING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS73	IA Marks	40
Number of Lecture Hours/Week	03	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Introduction: Well posed learning problems, Designing a Learning system, Perspective and Issues in Machine Learning. **Concept Learning:** Concept learning task, Concept learning as search, Find-S algorithm, Version space, Candidate Elimination algorithm, Inductive Bias. **Text Book1, Sections: 1.1 – 1.3, 2.1-2.5, 2.7**	**10 Hours**
Module – 2	
Decision Tree Learning: Decision tree representation, Appropriate problems for decision tree learning, Basic decision tree learning algorithm, hypothesis space search in decision tree learning, Inductive bias in decision tree learning, Issues in decision tree learning. **Text Book1, Sections: 3.1-3.7**	**10 Hours**
Module – 3	
Artificial Neural Networks: Introduction, Neural Network representation, Appropriate problems, Perceptrons, Backpropagation algorithm. **Text book 1, Sections: 4.1 – 4.6**	**08 Hours**
Module – 4	
Bayesian Learning: Introduction, Bayes theorem, Bayes theorem and concept learning, ML and LS error hypothesis, ML for predicting probabilities, MDL principle, Naive Bayes classifier, Bayesian belief networks, EM algorithm **Text book 1, Sections: 6.1 – 6.6, 6.9, 6.11, 6.12**	**10 Hours**
Module – 5	
Evaluating Hypothesis: Motivation, Estimating hypothesis accuracy, Basics of sampling theorem, General approach for deriving confidence intervals, Difference in error of two hypothesis, Comparing learning algorithms. **Instance Based Learning:** Introduction, k-nearest neighbor learning, locally weighted regression, radial basis function, cased-based reasoning, **Reinforcement Learning:** Introduction, Learning Task, Q Learning **Text book 1, Sections: 5.1-5.6, 8.1-8.5, 13.1-13.3**	**12 Hours**

Course Outcomes: After studying this course, students will be able to

- Recall the problems for machine learning. And select the either supervised, unsupersvised or reinforcement learning.
- Understand theory of probability and statistics related to machine learning
- Illustrate concept learning, ANN, Bayes classifier, k nearest neighbor, Q,

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.

Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Tom M. Mitchell, Machine Learning**,** India Edition 2013, McGraw Hill Education.

Reference Books:

1. Trevor Hastie, Robert Tibshirani, Jerome Friedman, h The Elements of Statistical Learning, 2nd edition, springer series in statistics.
2. Ethem Alpaydın, Introduction to machine learning, second edition, MIT press.

NATURAL LANGUAGE PROCESSING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS741	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Overview and language modeling: Overview: Origins and challenges of NLP-Language and Grammar-Processing Indian Languages- NLP Applications-Information Retrieval. Language Modeling: Various Grammar- based Language Models-Statistical Language Model.	**8 Hours**
Module – 2	
Word level and syntactic analysis: Word Level Analysis: Regular Expressions-Finite-State Automata-Morphological Parsing-Spelling Error Detection and correction-Words and Word classes-Part-of Speech Tagging. Syntactic Analysis: Context-free Grammar-Constituency- Parsing-Probabilistic Parsing.	**8 Hours**
Module – 3	
Extracting Relations from Text: From Word Sequences to Dependency Paths: Introduction, Subsequence Kernels for Relation Extraction, A Dependency-Path Kernel for Relation Extraction and Experimental Evaluation. **Mining Diagnostic Text Reports by Learning to Annotate Knowledge Roles:** Introduction, Domain Knowledge and Knowledge Roles, Frame Semantics and Semantic Role Labeling, Learning to Annotate Cases with Knowledge Roles and Evaluations. **A Case Study in Natural Language Based Web Search:** InFact System Overview, The GlobalSecurity.org Experience.	**8 Hours**
Module – 4	
Evaluating Self-Explanations in iSTART: Word Matching, Latent Semantic Analysis, and Topic Models: Introduction, iSTART: Feedback Systems, iSTART: Evaluation of Feedback Systems, **Textual Signatures: Identifying Text-Types Using Latent Semantic Analysis to Measure the Cohesion of Text Structures:** Introduction, Cohesion, Coh-Metrix, Approaches to Analyzing Texts, Latent Semantic Analysis, Predictions, Results of Experiments. **Automatic Document Separation: A Combination of Probabilistic Classification and Finite-State Sequence Modeling:** Introduction, Related Work, Data Preparation, Document Separation as a Sequence Mapping Problem, Results. **Evolving Explanatory Novel Patterns for Semantically-Based Text Mining:** Related Work, A Semantically Guided Model for Effective Text Mining.	**8 Hours**
Module – 5	
INFORMATION RETRIEVAL AND LEXICAL RESOURCES: Information Retrieval: Design features of Information Retrieval Systems-Classical, Non classical, Alternative Models of Information Retrieval – valuation Lexical Resources: World Net-Frame Net- Stemmers-POS Tagger- Research Corpora.	**8 Hours**

Course outcomes: The students should be able to:

- Analyze the natural language text.
- Define the importance of natural language.
- Understand the concepts Text mining.
- Illustrate information retrieval techniques.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Tanveer Siddiqui, U.S. Tiwary, "Natural Language Processing and Information Retrieval", Oxford University Press, 2008.
2. Anne Kao and Stephen R. Poteet (Eds), "Natural LanguageProcessing and Text Mining", Springer-Verlag London Limited 2007.

Reference Books:

1. Daniel Jurafsky and James H Martin, "Speech and Language Processing: Anintroduction to Natural Language Processing, Computational Linguistics and SpeechRecognition", 2nd Edition, Prentice Hall, 2008.
2. James Allen, "Natural Language Understanding", 2nd edition, Benjamin/Cummingspublishing company, 1995.
3. Gerald J. Kowalski and Mark.T. Maybury, "Information Storage and Retrieval systems", Kluwer academic Publishers, 2000.

CLOUD COMPUTING AND ITS APPLICATIONS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS742	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction ,Cloud Computing at a Glance, The Vision of Cloud Computing, Defining a Cloud, A Closer Look, Cloud Computing Reference Model, Characteristics and Benefits, Challenges Ahead, Historical Developments, Distributed Systems, Virtualization, Web 2.0, Service-Oriented Computing, Utility-Oriented Computing, Building Cloud Computing Environments, Application Development, Infrastructure and System Development, Computing Platforms and Technologies, Amazon Web Services (AWS), Google AppEngine, Microsoft Azure, Hadoop, Force.com and Salesforce.com, Manjrasoft Aneka Virtualization, Introduction, Characteristics of Virtualized, Environments Taxonomy of Virtualization Techniques, Execution Virtualization, Other Types of Virtualization, Virtualization and Cloud Computing, Pros and Cons of Virtualization, Technology Examples Xen: Paravirtualization, VMware: Full Virtualization, Microsoft Hyper-V	**8 Hours**
Module – 2	
Cloud Computing Architecture, Introduction, Cloud Reference Model, Architecture, Infrastructure / Hardware as a Service, Platform as a Service, Software as a Service, Types of Clouds, Public Clouds, Private Clouds, Hybrid Clouds, Community Clouds, Economics of the Cloud, Open Challenges, Cloud Definition, Cloud Interoperability and Standards Scalability and Fault Tolerance Security, Trust, and Privacy Organizational Aspects Aneka: Cloud Application Platform, Framework Overview, Anatomy of the Aneka Container, From the Ground Up: Platform Abstraction Layer, Fabric Services, foundation Services, Application Services, Building Aneka Clouds, Infrastructure Organization, Logical Organization, Private Cloud Deployment Mode, Public Cloud Deployment Mode, Hybrid Cloud Deployment Mode, Cloud Programming and Management, Aneka SDK, Management Tools	**8 Hours**
Module – 3	
Concurrent Computing: Thread Programming, Introducing Parallelism for Single Machine Computation, Programming Applications with Threads, What is a Thread?, Thread APIs, Techniques for Parallel Computation with Threads, Multithreading with Aneka, Introducing the Thread Programming Model, Aneka Thread vs. Common Threads, Programming Applications with Aneka Threads, Aneka Threads Application Model, Domain Decomposition: Matrix Multiplication, Functional Decomposition: Sine, Cosine, and Tangent. High-Throughput Computing: Task Programming, Task Computing, Characterizing a Task, Computing Categories, Frameworks for Task Computing, Task-based Application Models, Embarrassingly Parallel Applications,	**8 Hours**

Parameter Sweep Applications, MPI Applications, Workflow Applications with Task Dependencies, Aneka Task-Based Programming, Task Programming Model, Developing Applications with the Task Model, Developing Parameter Sweep Application, Managing Workflows.	
Module – 4	
Data Intensive Computing: Map-Reduce Programming, What is Data-Intensive Computing?, Characterizing Data-Intensive Computations, Challenges Ahead, Historical Perspective, Technologies for Data-Intensive Computing, Storage Systems, Programming Platforms, Aneka MapReduce Programming, Introducing the MapReduce Programming Model, Example Application	**8 Hours**
Module – 5	
Cloud Platforms in Industry, Amazon Web Services, Compute Services, Storage Services, Communication Services, Additional Services, Google AppEngine, Architecture and Core Concepts, Application Life-Cycle, Cost Model, Observations, Microsoft Azure, Azure Core Concepts, SQL Azure, Windows Azure Platform Appliance. Cloud Applications Scientific Applications, Healthcare: ECG Analysis in the Cloud, Biology: Protein Structure Prediction, Biology: Gene Expression Data Analysis for Cancer Diagnosis, Geoscience: Satellite Image Processing, Business and Consumer Applications, CRM and ERP, Productivity, Social Networking, Media Applications, Multiplayer Online Gaming.	**8 Hours**
Course outcomes: The students should be able to:	
• Understand the concepts of cloud computing, virtualization and classify services of cloud computing • Illustrate architecture and programming in cloud • Define the platforms for development of cloud applications and List the application of cloud.	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books: 1. Rajkumar Buyya, Christian Vecchiola, and Thamarai Selvi Mastering Cloud. Computing McGraw Hill Education	
Reference Books:	
1. Dan C. Marinescu, Cloud Computing Theory and Practice, Morgan Kaufmann, Elsevier 2013.	

INFORMATION AND NETWORK SECURITY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS743	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction. How to Speak Crypto. Classic Crypto. Simple Substitution Cipher. Cryptanalysis of a Simple Substitution. Definition of Secure. Double Transposition Cipher. One-time Pad. Project VENONA. Codebook Cipher. Ciphers of the Election of 1876. Modern Crypto History. Taxonomy of Cryptography. Taxonomy of Cryptanalysis.	**8 Hours**
Module – 2.	
What is a Hash Function? The Birthday Problem.Non-cryptographic Hashes. Tiger Hash. HMAC. Uses of Hash Functions. Online Bids. Spam Reduction. Other Crypto-Related Topics. Secret Sharing. Key Escrow. Random Numbers. Texas Hold 'em Poker. Generating Random Bits. Information Hiding.	**8 Hours**
Module – 3	
Random number generation Providing freshness Fundamentals of entity authentication Passwords Dynamic password schemes Zero-knowledge mechanisms Further reading Cryptographic Protocols Protocol basics From objectives to a protocol Analysing a simple protocol Authentication and key establishment protocols	**8 Hours**
Module – 4	
Key management fundamentals Key lengths and lifetimes Key generation Key establishment Key storage Key usage Governing key management Public-Key Management Certification of public keys The certificate lifecycle Public-key management models Alternative approaches	**8 Hours**
Module – 5	
Cryptographic Applications Cryptography on the Internet Cryptography for wireless local area networks Cryptography for mobile telecommunications Cryptography for secure payment card transactions Cryptography for video broadcasting Cryptography for identity cards Cryptography for home users	**8 Hours**

Course outcomes: The students should be able to:

- Analyze the Digitals security lapses
- Illustrate the need of key management

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Information Security: Principles and Practice, 2nd Edition by Mark Stamp Wiley
2. Everyday Cryptography: Fundamental Principles and Applications Keith M. Martin Oxford Scholarship Online: December 2013

Reference Books:

1. Applied Cryptography Protocols, Algorithms, and Source Code in C by Bruce Schneier

UNIX SYSTEM PROGRAMMING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS744	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction: UNIX and ANSI Standards: The ANSI C Standard, The ANSI/ISO C++ Standards, Difference between ANSI C and C++, The POSIX Standards, The POSIX.1 FIPS Standard, The X/Open Standards. UNIX and POSIX APIs: The POSIX APIs, The UNIX and POSIX Development Environment, API Common Characteristics.	**8 Hours**
Module – 2	
UNIX Files and APIs: File Types, The UNIX and POSIX File System, The UNIX and POSIX File Attributes, Inodes in UNIX System V, Application Program Interface to Files, UNIX Kernel Support for Files, Relationship of C Stream Pointers and File Descriptors, Directory Files, Hard and Symbolic Links. UNIX File APIs: General File APIs, File and Record Locking, Directory File APIs, Device File APIs, FIFO File APIs, Symbolic Link File APIs.	**8 Hours**
Module – 3	
UNIX Processes and Process Control: The Environment of a UNIX Process: Introduction, main function, Process Termination, Command-Line Arguments, Environment List, Memory Layout of a C Program, Shared Libraries, Memory Allocation, Environment Variables, setjmp and longjmp Functions, getrlimit, setrlimit Functions, UNIX Kernel Support for Processes. Process Control: Introduction, Process Identifiers, fork, vfork, exit, wait, waitpid, wait3, wait4 Functions, Race Conditions, exec Functions, Changing User IDs and Group IDs, Interpreter Files, system Function, Process Accounting, User Identification, Process Times, I/O Redirection. Process Relationships: Introduction, Terminal Logins, Network Logins, Process Groups, Sessions, Controlling Terminal, tcgetpgrp and tcsetpgrp Functions, Job Control, Shell Execution of Programs, Orphaned Process Groups.	**8 Hours**
Module – 4	
Signals and Daemon Processes: Signals: The UNIX Kernel Support for Signals, signal, Signal Mask, sigaction, The SIGCHLD Signal and the waitpid Function, The sigsetjmp and siglongjmp Functions, Kill, Alarm, Interval Timers, POSIX.lb Timers. Daemon Processes: Introduction, Daemon Characteristics, Coding Rules, Error Logging, Client-Server Model.	**8 Hours**
Module – 5	
Interprocess Communication : Overview of IPC Methods, Pipes, popen, pclose Functions, Coprocesses, FIFOs, System V IPC, Message Queues, Semaphores. Shared Memory, Client-Server Properties, Stream Pipes, Passing File Descriptors, An Open Server-Version 1, Client-Server Connection Functions.	**8 Hours**

Course outcomes: The students should be able to:

- Understand the working of Unix Systems
- Illustrate the application/service over a UNIX system.

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Unix System Programming Using C++ - Terrence Chan, PHI, 1999.
2. Advanced Programming in the UNIX Environment - W.Richard Stevens, Stephen A. Rago, 3nd Edition, Pearson Education / PHI, 2005.

Reference Books:

1. Advanced Unix Programming- Marc J. Rochkind, 2nd Edition, Pearson Education, 2005.
2. The Design of the UNIX Operating System - Maurice.J.Bach, Pearson Education / PHI, 1987.
3. Unix Internals - Uresh Vahalia, Pearson Education, 2001.

SOFT AND EVOLUTIONARY COMPUTING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS751	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction to soft computing: ANN, FS,GA, SI, ES, Comparing among intelligent systems ANN: introduction, biological inspiration, BNN&ANN, classification, first Generation NN, perceptron, illustrative problems **Text Book 1: Chapter1: 1.1-1.8, Chapter2: 2.1-2.6**	**8 Hours**
Module – 2	
Adaline, Medaline, ANN: (2^{nd} generation), introduction, BPN, KNN,HNN, BAM, RBF,SVM and illustrative problems **Text Book 1: Chapter2: 3.1,3.2,3.3,3.6,3.7,3.10,3.11**	**8 Hours**
Module – 3	
Fuzzy logic: introduction, human learning ability, undecidability, probability theory, classical set and fuzzy set, fuzzy set operations, fuzzy relations, fuzzy compositions, natural language and fuzzy interpretations, structure of fuzzy inference system, illustrative problems **Text Book 1: Chapter 5**	**8 Hours**
Module – 4	
Introduction to GA, GA, procedures, working of GA, GA applications, applicability, evolutionary programming, working of EP, GA based Machine learning classifier system, illustrative problems **Text Book 1: Chapter 7**	**8 Hours**
Module – 5	
Swarm Intelligent system: Introduction, Background of SI, Ant colony system Working of ACO, Particle swarm Intelligence(PSO). **Text Book 1: 8.1-8.4, 8.7**	**8 Hours**

Course outcomes: The students should be able to:

- Understand soft computing techniques
- Apply the learned techniques to solve realistic problems
- Differentiate soft computing with hard computing techniques

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Soft computing : N. P Padhy and S P Simon , Oxford University Press 2015

Reference Books:

1. Principles of Soft Computing, Shivanandam, Deepa S. N Wiley India, 2011.

COMPUTER VISION AND ROBOTICS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS752	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
CAMERAS: Pinhole Cameras**, Radiometry – Measuring Light:** Light in Space, Light Surfaces, Important Special Cases, **Sources, Shadows, And Shading:** Qualitative Radiometry, Sources and Their Effects, Local Shading Models, Application: Photometric Stereo, Interreflections: Global Shading Models, **Color:** The Physics of Color, Human Color Perception, Representing Color, A Model for Image Color, Surface Color from Image Color.	**8 Hours**
Module – 2	
Linear Filters: Linear Filters and Convolution, Shift Invariant Linear Systems, Spatial Frequency and Fourier Transforms, Sampling and Aliasing, Filters as Templates, **Edge Detection:** Noise, Estimating Derivatives, Detecting Edges, **Texture:** Representing Texture, Analysis (and Synthesis) Using Oriented Pyramids, Application: Synthesis by Sampling Local Models, Shape from Texture.	**8 Hours**
Module – 3	
The Geometry of Multiple Views: Two Views, **Stereopsis:** Reconstruction, Human Stereposis, Binocular Fusion, Using More Cameras, **Segmentation by Clustering:** What Is Segmentation?, Human Vision: Grouping and Getstalt, Applications: Shot Boundary Detection and Background Subtraction, Image Segmentation by Clustering Pixels, Segmentation by Graph-Theoretic Clustering,	**8 Hours**
Module – 4	
Segmentation by Fitting a Model: The Hough Transform, Fitting Lines, Fitting Curves, Fitting as a Probabilistic Inference Problem, Robustness, **Segmentation and Fitting Using Probabilistic Methods:** Missing Data Problems, Fitting, and Segmentation, The EM Algorithm in Practice, **Tracking With Linear Dynamic Models:** Tracking as an Abstract Inference Problem, Linear Dynamic Models, Kalman Filtering, Data Association, Applications and Examples**.**	**8 Hours**
Module – 5	
Geometric Camera Models: Elements of Analytical Euclidean Geometry, Camera Parameters and the Perspective Projection, Affine Cameras and Affine Projection Equations, **Geometric Camera Calibration:** Least-Squares Parameter Estimation, A Linear Approach to Camera Calibration, Taking Radial Distortion into Account, Analytical Photogrammetry, An Application: Mobile Robot Localization, **Model- Based Vision:** Initial Assumptions, Obtaining Hypotheses by Pose Consistency, Obtaining Hypotheses by pose Clustering, Obtaining Hypotheses Using Invariants, Verification, Application: Registration In Medical Imaging Systems, Curved Surfaces and Alignment.	**8 Hours**

Course outcomes: The students should be able to:

- Implement fundamental image processing techniques required for computer vision
- Perform shape analysis

- Implement boundary tracking techniques
- Apply chain codes and other region descriptors
- Apply Hough Transform for line, circle, and ellipse detections.
- Apply 3D vision techniques.
- Implement motion related techniques.
- Develop applications using computer vision techniques.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. David A. Forsyth and Jean Ponce: Computer Vision – A Modern Approach, PHI Learning (Indian Edition), 2009.

Reference Books:

2. E. R. Davies: Computer and Machine Vision – Theory, Algorithms and Practicalities, Elsevier (Academic Press), 4th edition, 2013.

DIGITAL IMAGE PROCESSING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS753	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction Fundamental Steps in Digital Image Processing, Components of an Image Processing System, Sampling and Quantization, Representing Digital Images (Data structure), Some Basic Relationships Between Pixels- Neighbors and Connectivity of pixels in image, Applications of Image Processing: Medical imaging, Robot vision, Character recognition, Remote Sensing.	**8 Hours**
Module – 2	
Image Enhancement In The Spatial Domain: Some Basic Gray Level Transformations, Histogram Processing, Enhancement Using Arithmetic/Logic Operations, Basics of Spatial Filtering, Smoothing Spatial Filters, Sharpening Spatial Filters, Combining Spatial Enhancement Methods.	**8 Hours**
Module – 3	
Image Enhancement In Frequency Domain: Introduction, Fourier Transform, Discrete Fourier Transform (DFT), properties of DFT , Discrete Cosine Transform (DCT), Image filtering in frequency domain.	**8 Hours**
Module – 4	
Image Segmentation: Introduction, Detection of isolated points, line detection, Edge detection, Edge linking, Region based segmentation- Region growing, split and merge technique, local processing, regional processing, Hough transform, Segmentation using Threshold.	**8 Hours**
Module – 5	
Image Compression: Introduction, coding Redundancy , Inter-pixel redundancy, image compression model, Lossy and Lossless compression, Huffman Coding, Arithmetic Coding, LZW coding, Transform Coding, Sub-image size selection, blocking, DCT implementation using FFT, Run length coding.	**8 Hours**

Course outcomes: The students should be able to:

- Explain fundamentals of image processing
- Compare transformation algorithms
- Contrast enhancement, segmentation and compression techniques

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Rafael C G., Woods R E. and Eddins S L, Digital Image Processing, Prentice Hall, 3rd edition, 2008.

Reference Books:

1. Milan Sonka,"Image Processing, analysis and Machine Vision", Thomson Press India

Ltd, Fourth Edition.
2. Fundamentals of Digital Image Processing- Anil K. Jain, 2nd Edition, Prentice Hall of India.
3. S. Sridhar , Digital Image Processing, Oxford University Press, 2nd Ed, 2016.

STORAGE AREA NETWORKS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CS754	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Storage System Introduction to evolution of storage architecture, key data center elements, virtualization, and cloud computing. Key data center elements – Host (or compute), connectivity, storage, and application in both classic and virtual environments. RAID implementations, techniques, and levels along with the impact of RAID on application performance.Components of intelligent storage systems and virtual storage provisioning and intelligent storage system implementations.	**8 Hours**
Module – 2	
Storage Networking Technologies and Virtualization Fibre Channel SAN components, connectivity options, and topologies including access protection mechanism 'zoning", FC protocol stack, addressing and operations, SAN-based virtualization and VSAN technology, iSCSI and FCIP protocols for storage access over IP network, Converged protocol FCoE and its components, Network Attached Storage (NAS) - components, protocol and operations, File level storage virtualization, Object based storage and unified storage platform.	**8 Hours**
Module – 3	
Backup, Archive, and Replication This unit focuses on information availability and business continuity solutions in both virtualized and non-virtualized environments. Business continuity terminologies, planning and solutions, Clustering and multipathing architecture to avoid single points of failure, Backup and recovery - methods, targets and topologies, Data deduplication and backup in virtualized environment, Fixed content and data archive, Local replication in classic and virtual environments, Remote replication in classic and virtual environments, Three-site remote replication and continuous data protection	**8 Hours**
Module – 4	
Cloud Computing Characteristics and benefits This unit focuses on the business drivers, definition, essential characteristics, and phases of journey to the Cloud. ,Business drivers for Cloud computing, Definition of Cloud computing, Characteristics of Cloud computing, Steps involved in transitioning from Classic data center to Cloud computing environment Services and deployment models, Cloud infrastructure components, Cloud migration considerations	**8 Hours**
Module – 5	
Securing and Managing Storage Infrastructure This chapter focuses on framework and domains of storage security along with covering security. implementation at storage networking. Security threats, and countermeasures in various domains Security solutions for FC-SAN, IP-SAN and NAS environments, Security in virtualized and cloud environments, Monitoring and managing various information infrastructure components in classic and virtual environments, Information lifecycle management (ILM) and storage tiering,	**8 Hours**

Cloud service management activities	
Course outcomes: The students should be able to:	
• Identify key challenges in managing information and analyze different storage networking technologies and virtualization • Explain components and the implementation of NAS • Describe CAS architecture and types of archives and forms of virtualization • Illustrate the storage infrastructure and management activities	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books:	
1. Information Storage and Management,Author :EMC Education Services, Publisher: Wiley ISBN: 9781118094839 2. Storage Virtualization, Author: Clark Tom, Publisher: Addison Wesley Publishing Company ISBN : 9780321262516	
Reference Books:	
NIL	

MACHINE LEARNING LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CSL76	IA Marks	40
Number of Lecture Hours/Week	01I + 02P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description (If any):

1. The programs can be implemented in either JAVA or Python.
2. For Problems 1 to 6 and 10, programs are to be developed without using the built-in classes or APIs of Java/Python.
3. Data sets can be taken from standard repositories (https://archive.ics.uci.edu/ml/datasets.html) or constructed by the students.

Lab Experiments:

1. Implement and demonstratethe **FIND-Salgorithm** for finding the most specific hypothesis based on a given set of training data samples. Read the training data from a .CSV file.
2. For a given set of training data examples stored in a .CSV file, implement and demonstrate the **Candidate-Elimination algorithm**to output a description of the set of all hypotheses consistent with the training examples.
3. Write a program to demonstrate the working of the decision tree based **ID3 algorithm**. Use an appropriate data set for building the decision tree and apply this knowledge toclassify a new sample.
4. Build an Artificial Neural Network by implementing the **Backpropagation algorithm** and test the same using appropriate data sets.
5. Write a program to implement the **naïve Bayesian classifier** for a sample training data set stored as a .CSV file. Compute the accuracy of the classifier, considering few test data sets.
6. Assuming a set of documents that need to be classified, use the **naïve Bayesian Classifier** model to perform this task. Built-in Java classes/API can be used to write the program. Calculate the accuracy, precision, and recall for your data set.
7. Write a program to construct a**Bayesian network** considering medical data. Use this model to demonstrate the diagnosis of heart patients using standard Heart Disease Data Set. You can use Java/Python ML library classes/API.
8. Apply **EM algorithm** to cluster a set of data stored in a .CSV file. Use the same data set for clustering using ***k*-Means algorithm**. Compare the results of these two algorithms and comment on the quality of clustering. You can add Java/Python ML library classes/API in the program.
9. Write a program to implement ***k*-Nearest Neighbour algorithm** to classify the iris data set. Print both correct and wrong predictions. Java/Python ML library classes can be used for this problem.
10. Implement the non-parametric **Locally Weighted Regressionalgorithm** in order to fit data points. Select appropriate data set for your experiment and draw graphs.

Study Experiment / Project:

NIL

Course outcomes: The students should be able to:

1. Understand the implementation procedures for the machine learning algorithms.

2. Design Java/Python programs for various Learning algorithms.
3. Apply appropriate data sets to the Machine Learning algorithms.
4. Identify and apply Machine Learning algorithms to solve real world problems.

Conduction of Practical Examination:

- All laboratory experiments are to be included for practical examination.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script
- Marks distribution: Procedure + Conduction + Viva:**15 + 70 +15 (100)**

Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.

WEB TECHNOLOGY LABORATORY WITH MINI PROJECT
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VII

Subject Code	17CSL77	IA Marks	40
Number of Lecture Hours/Week	01I + 02P	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 02

Description (If any):

NIL

Lab Experiments:

PART A

1. Write a JavaScript to design a simple calculator to perform the following operations: sum, product, difference and quotient.
2. Write a JavaScript that calculates the squares and cubes of the numbers from 0 to 10 and outputs HTML text that displays the resulting values in an HTML table format.
3. Write a JavaScript code that displays text “TEXT-GROWING” with increasing font size in the interval of 100ms in RED COLOR, when the font size reaches 50pt it displays “TEXT-SHRINKING” in BLUE color. Then the font size decreases to 5pt.
4. Develop and demonstrate a HTML5 file that includes JavaScript script that uses functions for the following problems:
   a. Parameter: A string
   b. Output: The position in the string of the left-most vowel
   c. Parameter: A number
   d. Output: The number with its digits in the reverse order
5. Design an XML document to store information about a student in an engineering college affiliated to VTU. The information must include USN, Name, and Name of the College, Branch, Year of Joining, and email id. Make up sample data for 3 students. Create a CSS style sheet and use it to display the document.
6. Write a PHP program to keep track of the number of visitors visiting the web page and to display this count of visitors, with proper headings.
7. Write a PHP program to display a digital clock which displays the current time of the server.
8. Write the PHP programs to do the following:
   a. Implement simple calculator operations.
   b. Find the transpose of a matrix.
   c. Multiplication of two matrices.
   d. Addition of two matrices.

9. Write a PHP program named states.py that declares a variable states with value "Mississippi Alabama Texas Massachusetts Kansas". write a PHP program that does the following:
   a. Search for a word in variable states that ends in xas. Store this word in element 0 of a list named statesList.

b. Search for a word in states that begins with k and ends in s. Perform a case-insensitive comparison. [Note: Passing re.Ias a second parameter to method compile performs a case-insensitive comparison.] Store this word in element1 of statesList.
c. Search for a word in states that begins with M and ends in s. Store this word in element 2 of the list.
d. Search for a word in states that ends in a. Store this word in element 3 of the list.

10. Write a PHP program to sort the student records which are stored in the database using selection sort.

Study Experiment / Project:

Develop a web application project using the languages and concepts learnt in the theory and exercises listed in part A with a good look and feel effects. You can use any web technologies and frameworks and databases.

Note:

1. In the examination each student picks one question from part A.
2. A team of two or three students must develop the mini project. However during the examination, each student must demonstrate the project individually.
3. The team must submit a brief project report (15-20 pages) that must include the following
    a. Introduction
    b. Requirement Analysis
    c. Software Requirement Specification
    d. Analysis and Design
    e. Implementation
    f. Testing

Course outcomes: The students should be able to:

- Design and develop dynamic web pages with good aesthetic sense of designing and latest technical know-how's.
- Understand the concepts of Web Application Terminologies, Internet Tools other web services.
- Recall how to link and publish web sites

Conduction of Practical Examination:

1. All laboratory experiments from part A are to be included for practical examination.
2. Mini project has to be evaluated for 40 Marks.
3. Report should be prepared in a standard format prescribed for project work.
4. Students are allowed to pick one experiment from the lot.
5. Strictly follow the instructions as printed on the cover page of answer script.
6. Marks distribution:
    a) Part A: Procedure + Conduction + Viva: **09 + 42 +09 =60 Marks**
    b) Part B: Demonstration + Report + Viva voce **20+14+06 = 40** Marks

Change of experiment is allowed only once and marks allotted to the procedure part to be made zero.

INTERNET OF THINGS TECHNOLOGY
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VIII

Subject Code	**17CS81**	IA Marks	40
Number of Lecture Hours/Week	04	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
What is IoT, Genesis of IoT, IoT and Digitization, IoT Impact, Convergence of IT and IoT, IoT Challenges, IoT Network Architecture and Design, Drivers Behind New Network Architectures, Comparing IoT Architectures, A Simplified IoT Architecture, The Core IoT Functional Stack, IoT Data Management and Compute Stack.	**10 Hours**
Module – 2	
Smart Objects: The "Things" in IoT, Sensors, Actuators, and Smart Objects, Sensor Networks, Connecting Smart Objects, Communications Criteria, IoT Access Technologies.	**10 Hours**
Module – 3	
IP as the IoT Network Layer, The Business Case for IP, The need for Optimization, Optimizing IP for IoT, Profiles and Compliances, Application Protocols for IoT, The Transport Layer, IoT Application Transport Methods.	**10 Hours**
Module – 4	
Data and Analytics for IoT, An Introduction to Data Analytics for IoT, Machine Learning, Big Data Analytics Tools and Technology, Edge Streaming Analytics, Network Analytics, Securing IoT, A Brief History of OT Security, Common Challenges in OT Security, How IT and OT Security Practices and Systems Vary, Formal Risk Analysis Structures: OCTAVE and FAIR, The Phased Application of Security in an Operational Environment	**10 Hours**
Module – 5	
IoT Physical Devices and Endpoints - Arduino UNO: Introduction to Arduino, Arduino UNO, Installing the Software, Fundamentals of Arduino Programming. IoT Physical Devices and Endpoints - RaspberryPi: Introduction to RaspberryPi, About the RaspberryPi Board: Hardware Layout, Operating Systems on RaspberryPi, Configuring RaspberryPi, Programming RaspberryPi with Python, Wireless Temperature Monitoring System Using Pi, DS18B20 Temperature Sensor, Connecting Raspberry Pi via SSH, Accessing Temperature from DS18B20 sensors, Remote access to RaspberryPi, Smart and Connected Cities, An IoT Strategy for Smarter Cities, Smart City IoT Architecture, Smart City Security Architecture, Smart City Use-Case Examples.	**10 Hours**

Course Outcomes: After studying this course, students will be able to

- Interpret the impact and challenges posed by IoT networks leading to new architectural models.
- Compare and contrast the deployment of smart objects and the technologies to connect them to network.

- Appraise the role of IoT protocols for efficient network communication.
- Elaborate the need for Data Analytics and Security in IoT.
- Illustrate different sensor technologies for sensing real world entities and identify the applications of IoT in Industry.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. David Hanes, Gonzalo Salgueiro, Patrick Grossetete, Robert Barton, Jerome Henry,**"IoT Fundamentals: Networking Technologies, Protocols, and Use Cases for the Internet of Things",** 1st Edition, Pearson Education (Cisco Press Indian Reprint). (**ISBN:** 978-9386873743)
2. Srinivasa K G, **"Internet of Things",**CENGAGE Leaning India, 2017

Reference Books:

1. Vijay Madisetti and ArshdeepBahga, **"Internet of Things (A Hands-on-Approach)",** 1st Edition, VPT, 2014. (**ISBN:** 978-8173719547)
2. Raj Kamal, **"Internet of Things: Architecture and Design Principles",** 1st Edition, McGraw Hill Education, 2017. (**ISBN:** 978-9352605224)

BIG DATA ANALYTICS
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VIII

Subject Code	17CS82	IA Marks	40
Number of Lecture Hours/Week	4	Exam Marks	60
Total Number of Lecture Hours	50	Exam Hours	03

CREDITS – 04

Module – 1	**Teaching Hours**
Hadoop Distributed File System Basics, Running Example Programs and Benchmarks, Hadoop MapReduce Framework, MapReduce Programming	**10 Hours**
Module – 2	
Essential Hadoop Tools, Hadoop YARN Applications, Managing Hadoop with Apache Ambari, Basic Hadoop Administration Procedures	**10 Hours**
Module – 3	
Business Intelligence Concepts and Application, Data Warehousing, Data Mining, Data Visualization	**10 Hours**
Module – 4	
Decision Trees, Regression, Artificial Neural Networks, Cluster Analysis, Association Rule Mining	**10 Hours**
Module – 5	
Text Mining, Naïve-Bayes Analysis, Support Vector Machines, Web Mining, Social Network Analysis	**10 Hours**

Course outcomes: The students should be able to:

- Explain the concepts of HDFS and MapReduce framework
- Investigate Hadoop related tools for Big Data Analytics and perform basic Hadoop Administration
- Recognize the role of Business Intelligence, Data warehousing and Visualization in decision making
- Infer the importance of core data mining techniques for data analytics
- Compare and contrast different Text Mining Techniques

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Douglas Eadline,**"Hadoop 2 Quick-Start Guide: Learn the Essentials of Big Data Computing in the Apache Hadoop 2 Ecosystem",** 1st Edition, Pearson Education, 2016. ISBN-13: 978-9332570351
2. Anil Maheshwari, **"Data Analytics",** 1st Edition, McGraw Hill Education, 2017. ISBN-13: 978-9352604180

Reference Books:

1) Tom White, **"Hadoop: The Definitive Guide",** 4th Edition, O'Reilly Media, 2015.ISBN-13: 978-9352130672
2) Boris Lublinsky, Kevin T.Smith, Alexey Yakubovich,**"Professional Hadoop**

Solutions", 1st Edition, Wrox Press, 2014ISBN-13: 978-8126551071
3) Eric Sammer,**"Hadoop Operations: A Guide for Developers and Administrators",**1st Edition, O'Reilly Media, 2012.ISBN-13: 978-9350239261

HIGH PERFORMANCE COMPUTING
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VIII

Subject Code	17CS831	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction: Computational Science and Engineering: Computational Science and Engineering Applications; characteristics and requirements, Review of Computational Complexity, Performance: metrics and measurements, Granularity and Partitioning, Locality: temporal/spatial/stream/kernel, Basic methods for parallel programming, Real-world case studies (drawn from multi-scale, multi-discipline applications)	**08 Hours**
Module – 2	
High-End Computer Systems : Memory Hierarchies, Multi-core Processors: Homogeneous and Heterogeneous, Shared-memory Symmetric Multiprocessors, Vector Computers, Distributed Memory Computers, Supercomputers and Petascale Systems, Application Accelerators / Reconfigurable Computing, Novel computers: Stream, multithreaded, and purpose-built	**08 Hours**
Module – 3	
Parallel Algorithms: Parallel models: ideal and real frameworks, Basic Techniques: Balanced Trees, Pointer Jumping, Divide and Conquer, Partitioning, Regular Algorithms: Matrix operations and Linear Algebra, Irregular Algorithms: Lists, Trees, Graphs, Randomization: Parallel Pseudo-Random Number Generators, Sorting, Monte Carlo techniques	**08 Hours**
Module – 4	
Parallel Programming: Revealing concurrency in applications, Task and Functional Parallelism, Task Scheduling, Synchronization Methods, Parallel Primitives (collective operations), SPMD Programming (threads, OpenMP, MPI), I/O and File Systems, Parallel Matlabs (Parallel Matlab, Star-P, Matlab MPI), Partitioning Global Address Space (PGAS) languages (UPC, Titanium, Global Arrays)	**08 Hours**
Module – 5	
Achieving Performance: Measuring performance, Identifying performance bottlenecks, Restructuring applications for deep memory hierarchies, Partitioning applications for heterogeneous resources, using existing libraries, tools, and frameworks	**08 Hours**

Course outcomes: The students should be able to:

- Illustrate the key factors affecting performance of CSE applications
- Illusrate mapping of applications to high-performance computing systems
- Apply hardware/software co-design for achieving performance on real-world applications

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.

Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Introduction to Parallel Computing, AnanthGrama, Anshul Gupta, George Karypis, and Vipin Kumar, 2nd edition, Addison-Welsey, 2003.
2. Petascale Computing: Algorithms and Applications, David A. Bader (Ed.), Chapman & Hall/CRC Computational Science Series, 2007

Reference Books:

1. Grama, A. Gupta, G. Karypis, V. Kumar, An Introduction to Parallel Computing, Design and Analysis of Algorithms: 2/e, Addison-Wesley, 2003.
2. G.E. Karniadakis, R.M. Kirby II, Parallel Scientific Computing in C++ and MPI: A Seamless Approach to Parallel Algorithms and their Implementation, Cambridge University Press,2003.
3. Wilkinson and M. Allen, Parallel Programming: Techniques and Applications Using Networked Workstations and Parallel Computers, 2/E, Prentice Hall, 2005.
4. M.J. Quinn, Parallel Programming in C with MPI and OpenMP, McGraw-Hill, 2004.
5. G.S. Almasi and A. Gottlieb, Highly Parallel Computing, 2/E, Addison-Wesley, 1994.
6. David Culler Jaswinder Pal Singh,"Parallel Computer Architecture: A hardware/Software Approach", Morgan Kaufmann, 1999.
7. Kai Hwang, "Scalable Parallel Computing", McGraw Hill 1998.

USER INTERFACE DESIGN
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017-18)
SEMESTER – VIII

Subject Code	17CS832	IA Marks	40
Number of Lecture Hours/Week	03	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Course Objectives: This course will enable students

- To study the concept of menus, windows, interfaces.
- To study about business functions.
- To study the characteristics and components of windows and the various controls for the windows.
- To study about various problems in window design with text, graphics.
- To study the testing methods.

Module –1	**Teaching Hours**
The User Interface-Introduction, Overview, The importance of user interface – Defining the user interface, The importance of Good design, Characteristics of graphical and web user interfaces, Principles of user interface design.	**08 Hours**
Module –2	
The User Interface Design process- Obstacles, Usability, Human characteristics in Design, Human Interaction speeds, Business functions-Business definition and requirement analysis, Basic business functions, Design standards.	**08 Hours**
Module –3	
System menus and navigation schemes- Structures of menus, Functions of menus, Contents of menus, Formatting of menus, Phrasing the menu**,** Selecting menu choices, Navigating menus, Kinds of graphical menus.	**08 Hours**
Module–4	
Windows **-** Characteristics, Components of window, Window presentation styles, Types of window, Window management, Organizing window functions, Window operations, Web systems, Characteristics of device based controls.	**08 Hours**
Module–5	
Screen based controls- Operable control, Text control, Selection control, Custom control, Presentation control, Windows Tests-prototypes, kinds of tests.	**08 Hours**

Course outcomes: The Students should be able to:

- Design the User Interface, design, menu creation ,windows creation and connection between menus and windows.

Question paper pattern:
The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

1. Wilbert O. Galitz, "The Essential Guide to User Interface Design", John Wiley & Sons, Second Edition 2002.

Reference Books:

1. Ben Sheiderman, “Design the User Interface”, Pearson Education, 1998.
2. Alan Cooper, ”The Essential of User Interface Design”, Wiley- Dream Tech Ltd.,2002

NETWORK MANAGEMENT [As per Choice Based Credit System (CBCS) scheme] (Effective from the academic year 2017 - 2018) SEMESTER – VIII			
Subject Code	17CS833	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03
CREDITS – 03			

Module – 1	**Teaching Hours**
Introduction: Analogy of Telephone Network Management, Data and Telecommunication Network Distributed computing Environments, TCP/IP-Based Networks: The Internet and Intranets, Communications Protocols and Standards- Communication Architectures, Protocol Layers and Services; Case Histories of Networking and Management – The Importance of topology , Filtering Does Not Reduce Load on Node, Some Common Network Problems; Challenges of Information Technology Managers, Network Management: Goals, Organization, and Functions- Goal of Network Management, Network Provisioning, Network Operations and the NOC, Network Installation and Maintenance; Network and System Management, Network Management System platform, Current Status and Future of Network Management.	**8 Hours**
Module – 2	
Basic Foundations: Standards, Models, and Language: Network Management Standards, Network Management Model, Organization Model, Information Model – Management Information Trees, Managed Object Perspectives, Communication Model; ASN.1- Terminology, Symbols, and Conventions, Objects and Data Types, Object Names, An Example of ASN.1 from ISO 8824; Encoding Structure; Macros, Functional Model.	**8 Hours**
Module – 3	
SNMPv1 Network Management: Managed Network: The History of SNMP Management, Internet Organizations and standards, Internet Documents, The SNMP Model, The Organization Model, System Overview. The Information Model – Introduction, The Structure of Management Information, Managed Objects, Management Information Base. The SNMP Communication Model – The SNMP Architecture, Administrative Model, SNMP Specifications, SNMP Operations, SNMP MIB Group, Functional Model SNMP Management – RMON: Remote Monitoring, RMON SMI and MIB, RMONI1- RMON1 Textual Conventions, RMON1 Groups and Functions, Relationship Between Control and Data Tables, RMON1 Common and Ethernet Groups, RMON Token Ring Extension Groups, RMON2 – The RMON2 Management Information Base, RMON2 Conformance Specifications.	**8 Hours**
Module – 4	
Broadband Access Networks, Broadband Access Technology; HFCT Technology: The Broadband LAN, The Cable Modem, The Cable Modem Termination System, The HFC Plant, The RF Spectrum for Cable Modem; Data Over Cable, Reference Architecture; HFC Management – Cable Modem and CMTS Management, HFC Link Management, RF Spectrum Management, DSL Technology; Asymmetric Digital Subscriber Line Technology – Role of the	**8 Hours**

ADSL Access Network in an Overall Network, ADSL Architecture, ADSL Channeling Schemes, ADSL Encoding Schemes; ADSL Management – ADSL Network Management Elements, ADSL Configuration Management, ADSL Fault Management, ADSL Performance Management, SNMP-Based ADSL Line MIB, MIB Integration with Interfaces Groups in MIB-2, ADSL Configuration Profiles	
Module – 5	
Network Management Applications: Configuration Management- Network Provisioning, Inventory Management, Network Topology, Fault Management- Fault Detection, Fault Location and Isolation 24 Techniques, Performance Management – Performance Metrics, Data Monitoring, Problem Isolation, Performance Statistics; Event Correlation Techniques – Rule-Based Reasoning, Model-Based Reasoning, CaseBased Reasoning, Codebook correlation Model, State Transition Graph Model, Finite State Machine Model, Security Management – Policies and Procedures, Security Breaches and the Resources Needed to Prevent Them, Firewalls, Cryptography, Authentication and Authorization, Client/Server Authentication Systems, Messages Transfer Security, Protection of Networks from Virus Attacks, Accounting Management, Report Management, Policy- Based Management, Service Level Management.	**8 Hours**
Course outcomes: The students should be able to:	
• Analyze the issues and challenges pertaining to management of emerging network technologies such as wired/wireless networks and high-speed internets. • Apply network management standards to manage practical networks • Formulate possible approaches for managing OSI network model. • Infer SNMP for managing the network • Infer RMON for monitoring the behavior of the network • Identify the various components of network and formulate the scheme for the managing them	
Question paper pattern: The question paper will have ten questions. There will be 2 questions from each module. Each question will have questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books:	
1. Mani Subramanian: Network Management- Principles and Practice, 2nd Pearson Education, 2010.	
Reference Books:	
1. J. Richard Burke: Network management Concepts and Practices: a Hands-On Approach, PHI, 2008.	

SYSTEM MODELLING AND SIMULATION
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 - 2018)
SEMESTER – VIII

Subject Code	17CS834	IA Marks	40
Number of Lecture Hours/Week	3	Exam Marks	60
Total Number of Lecture Hours	40	Exam Hours	03

CREDITS – 03

Module – 1	**Teaching Hours**
Introduction: When simulation is the appropriate tool and when it is not appropriate, Advantages and disadvantages of Simulation; Areas of application, Systems and system environment; Components of a system; Discrete and continuous systems, Model of a system; Types of Models, Discrete-Event System Simulation Simulation examples: Simulation of queuing systems. **General Principles, Simulation Software:**Concepts in Discrete-Event Simulation. The Event-Scheduling / Time-Advance Algorithm, Manual simulation Using Event Scheduling	**08 Hours**
Module – 2	
Statistical Models in Simulation :Review of terminology and concepts, Useful statistical models,Discrete distributions. Continuous distributions,Poisson process, Empirical distributions. **Queuing Models:**Characteristics of queuing systems,Queuing notation,Long-run measures of performance of queuing systems,Long-run measures of performance of queuing systems cont…,Steady-state behavior of M/G/1 queue, Networks of queues,	**08 Hours**
Module – 3	
Random-NumberGeneration: Properties of random numbers; Generation of pseudo-random numbers, Techniques for generating random numbers,Tests for Random Numbers, **Random-Variate Generation:** ,Inverse transform technique Acceptance-Rejection technique.	**08 Hours**
Module – 4	
Input Modeling: Data Collection; Identifying the distribution with data, Parameter estimation, Goodness of Fit Tests, Fitting a non-stationary Poisson process, Selecting input models without data, Multivariate and Time-Series input models. **Estimation of Absolute Performance:** Types of simulations with respect to output analysis ,Stochastic nature of output data, Measures of performance and their estimation, **Contd..**	**08 Hours**
Module – 5	
Measures of performance and their estimation,Output analysis for terminating simulations Continued..,Output analysis for steady-state simulations. **Verification, Calibration And Validation:** Optimization: Model building, verification and validation, Verification of simulation models, Verification of simulation models,Calibration and validation of models, Optimization via Simulation.	**08 Hours**
Course outcomes: The students should be able to:	

- Explain the system concept and apply functional modeling method to model the activities of a static system
- Describe the behavior of a dynamic system and create an analogous model for a dynamic system;
- Illustrate the operation of a dynamic system and make improvement according to the simulation results.

Question paper pattern:

The question paper will have ten questions.
There will be 2 questions from each module.
Each question will have questions covering all the topics under a module.
The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Jerry Banks, John S. Carson II, Barry L. Nelson, David M. Nicol: Discrete-Event System Simulation, 5 th Edition, Pearson Education, 2010.

Reference Books:

1. Lawrence M. Leemis, Stephen K. Park: Discrete – Event Simulation: A First Course, Pearson Education, 2006.
2. Averill M. Law: Simulation Modeling and Analysis, 4 th Edition, Tata McGraw-Hill, 2007

INTERNSHIP / PROFESSIONAL PRACTISE
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VIII

Subject Code	17CS84	IA Marks	50
Duration	4 weeks	Exam Marks	50
		Exam Hours	03

CREDITS – 02

Description (If any):

With reference to the above subject, this is to inform that the following are the guidelines to be followed for the Internship Programme and the earlier circular as cited in ref (i) is hereby withdrawn:

1) As per the 15OB.9 the Internship Programme duration is of Eight weeks. However it has been reduced to Four weeks and it should be carried out between (VI and VII Semester) Vacation and/or (VII and VIII Semester) Vacation.

2) The internship can be carried out in any Industry/R and D Organization/Research Institute/ Educational institute of repute.

3) The Institutions may also suggest the students to enrol for the Internshala platform for free internships as there is a MoU with the AICTE for the beneficial of the affiliated Institutions (https://internshala.com/)

4) The Examination of Internship will be carried out in line with the University Project Viva-voce examination.

5) (a) The Department/college shall nominate staff member/s to facilitate, guide and supervise students under internship. (b) The Internal Guide has to visit place of internship at least once during the student's internship.

6) The students shall report the progress of the internship to the guide in regular intervals and seek his/her advice.

7) After the completion of Internship, students shall submit a report with completion and attendance certificates to the Head of the Department with the approval of both internal and external guides.

8) The Examination of Internship will be carried out in line with the University Project Viva-voce examination.

9) There will be 50 marks for CIE (Seminar: 25, Internship report: 25) and 50 marks for Viva – Voce conducted during SEE. The minimum requirement of CIE marks shall be 50% of the maximum marks.

10) The internal guide shall award the marks for seminar and internship report after evaluation. He/she will also be the internal examiner for Viva – Voce conducted during SEE.

11) The external guide from the industry shall be an examiner for the viva voce on Internship. Viva-Voce on internship shall be conducted at the college and the date of Viva-Voce shall be fixed in consultation with the external Guide. The Examiners shall jointly award the Viva - Voce marks.

12) In case the external Guide expresses his inability to conduct viva voce, the Chief Superintendent of the institution shall appoint a senior faculty of the Department to conduct viva-voce along with the internal guide. The same shall be informed in writing to the concerned Chairperson, Board of Examiners (BOE).

13) The students are permitted to carry out the internship anywhere in India or abroad. The University will not provide any kind of financial assistance to any student for carrying out the Internship.

Course outcomes: The students should be able to:

1. Adapt easily to the industry environment
2. Take part in team work
3. Make use of modern tools
4. Decide upon project planning and financing.
5. Adapt ethical values.
6. Motivate for lifelong learning

PROJECT WORK PHASE II
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VIII

Subject Code	17CSP85	IA Marks	100
Number of Lecture Hours/Week	06	Exam Marks	100
Total Number of Lecture Hours	--	Exam Hours	03

CREDITS – 06

Description (If any):

- Project: Carried out at the Institution or at an Industry.
- Project work shall preferably be batch wise, the strength of each batch shall not exceed maximum of four students
- Viva-voce examination in project work shall be conducted batch-wise.
- For Project Phase –I and Project seminar and Project Phase –II, the CIE shall be 100 respectively.
- The CIE marks in the case of projects in the final year shall be based on the evaluation at the end of VIII semester by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the project guide.
- Minimum requirement of CIE marks for Project work shall be 50% of the maximum marks.
- Students failing to secure a minimum of 50% of the CIE marks in Project work shall not be eligible for the Project examination conducted by the University and they shall be considered as failed in that/those Course/s. However, they can appear for University examinations conducted in other Courses of the same semester and backlog Courses if any. Students after satisfying the prescribed minimum CIE marks in the Course/s when offered during subsequent semester shall appear for SEE.
- Improvement of CIE marks shall not be allowed in Project where the student has already secured the minimum required marks
- For a pass in a Project/Viva-voce examination, a student shall secure a minimum of 40% of the maximum marks prescribed for the University Examination. The Minimum Passing Grade in a Course is ‘E’.
- The student who desires to reject the results of a semester shall reject performance in all the Courses of the semester, irrespective of whether the student has passed or failed in any Course. However, the rejection of performance of VIII semester project shall not be permitted

Course outcomes: The students should be able to:

1. Identify a issue and derive problem related to society, environment, economics, energy and technology
2. Formulate and Analyze the problem and determine the scope of the solution chosen
3. Determine , dissect, and estimate the parameters, required in the solution.
4. Evaluate the solution by considering the standard data / Objective function and by using appropriate performance metrics.
5. Compile the report and take part in present / publishing the finding in a reputed conference / publications
6. Attempt to obtain ownership of the solution / product developed.

SEMINAR
[As per Choice Based Credit System (CBCS) scheme]
(Effective from the academic year 2017 -2018)
SEMESTER – VIII

Subject Code	17CSS86	IA Marks	100
Number of Lecture Hours/Week	04	Exam Marks	--
Total Number of Lecture Hours	--	Exam Hours	--

CREDITS – 01

Description:

- Seminar: Deliverable at the Institution under the supervision of a Faculty.
- Seminar is one of the head of passing. i) Each candidate shall deliver seminar as per the Scheme of Teaching and Examination on the topics chosen from the relevant fields for about 30 minutes. ii) The Head of the Department shall make arrangements for conducting seminars through concerned faculty members of the Department. The committee constituted for the purpose by the Head of the Department shall award the CIE marks for the seminar. The committee shall consist of three faculty from the Department and the senior most acting as the Chairman/Chairperson. [To be read along with 17 OB 8.6]
- For Technical seminar, the CIE marks shall be 100.
- The CIE marks in the case of projects and seminars in the final year shall be based on the evaluation at the end of VIII semester by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the project / seminar guide.
- For seminar, the minimum requirement of CIE marks shall be 40% of the maximum marks.
- If any student fails to secure a minimum of 40% of the maximum CIE marks in seminar/ fails to deliver the seminar, he/she shall be considered as failed in that Course and shall not be eligible for the award of degree. However, the student shall become eligible for the award of degree after satisfying the requirements prescribed for seminar during the subsequent semester/s.
- Improvement of CIE marks shall not be allowed in Seminar where the student has already secured the minimum required marks.
- Seminar topics must be from recent advancements in the domain.
- Each candidate must submit three copies of the report to the department. One for the candidate, one for the guide and one for the department.

Course outcomes: The students should be able to:

- Survey the changes in the technologies relevant to the topic selected
- Discuss the technology and interpret the impact on the society, environment and domain.
- Compile report of the study and present to the audience, following the ethics.