2nd International Conference on

"POWER, CIRCUIT AND INFORMATION TECHNOLOGIES (ICPCIT-16)"

- **Date of Conference :** 20th - 21st May 2016

- **Venue:** Dr. A.P. J. Abdul Kalam Hall,RRCE,Bengaluru

- **Objective of the Conference:** This conference is to provide an international forum for industry professionals and researchers to deliberate and state their research findings, discuss the latest advancements and explore the future directions in the emerging areas of Engineering and Technology. The conference also bring together both novice and experienced scientists and developers, to meet new colleagues, collect new ideas and establish new cooperation between research groups and provide a platform for researchers from academic and industry to present their original work and exchange ideas, information, techniques and applications in the field of Circuit, Power and Computing Technology.

- **Chief Guest:** Dr. L M Patnaik

Adjunct Professor - IISC and INSA Senior Scientist National Institute of Advanced Studies (NIAS)

Background:

Prof. L.M. Patnaik obtained his Ph.D in 1978 in the area of Real-Time Systems, D.Sc. in 1989 in the areas of Computer Systems and Architectures, both from the Indian Institute of Science, Bangalore. During March 2008 – August 2011, he was the Vice Chancellor, Defense Institute of Advanced Technology, Deemed University, Pune. Currently he is an Honorary Professor with the Department of Electronic Systems Engineering, Indian Institute of Science, Bangalore, and INSA Senior Scientist and Adjunct Professor with the National Institute of Advanced Studies, Bangalore. Prior to this he was a Professor with the Department of Computer Science and Automation at the Indian Institute of Science, Bangalore. He has published over 1000 papers in refereed International Journals and refereed International Conference Proceedings and has authored 29 technical reports. He is a co-editor/co-author of twenty one books and has authored 12 chapters in other books in the areas of VLSI System Design and Parallel Computing.

He has supervised over 30 Doctoral theses and over 160 Masters' theses in the above areas. In the areas of Parallel and Distributed Computing, and Soft Computing, he has been a Principal Investigator for a large number of Government-sponsored research projects and a Consultant to a few industries. One of his research papers in the area of adaptive genetic algorithms has been cited over 2300 times by Google Scholar. His name appears in Asia's Who's Who of Men and Women of Achievement; Directory of Distinguished Computer Professionals of India. He has served on several significant committees of leading professional societies such as the IEEE, CSI, IETE and Institution of Engineers; review and policy committees of Government agencies such on the DST, DBT, MHRD, CSIR, DOS, UGC, AICTE, DRDO, DIT and UPSC. His participation in the reviews of several Engg Colleges in the states of Karnataka, Kerala, Andhra Pradesh, and Maharashtra, organized by the AICTE, UGC, NBA, and TEQIP has significantly improved the quality of technical education in the country. As Vice Chancellor of the Defense Institute of Advanced Technology, Deemed University, he was responsible for conducting large number of Doctoral, Masters', short and long courses, in the field of defense technologies for service officers from the Army, Air Force, Navy; DRDO scientists.

- **Keynote speaker:** Dr. Vivek S. Deshpande

  Professor, Dept. of CSE, VIT, Pune and IEEE Executive Council Member PUNE Section

  Background:

Dr Vivek Deshpande, Full Professor of Computer Engineering at Vishwakarma Institute of Technology under Pune University, India. He served as Associate Professor at MIT College of Engineering, Pune, India. He worked as Dean, Research and Head Information Technology Department from 2009 to 2011. Dr Vivek holds Bachelors and Masters Degree of Engineering in Electronics and Telecommunications from Pune University, India. He pursued his Doctorate from Nagpur University, India.

Currently Dr Vivek keenly doing his research in Wireless Sensor Networks, Real Time Embedded Systems, High Performance Computer Networks and Cyber Physical Systems as well. He has 16 patents published on his name. He wrote 3 books/Chapters from well known publishers which are referred by many researchers who are working in related domain. His 23 years of teaching, Research and Industry experience is an asset to the organization. His expertise in the field of Wireless computer Networks and Distributed system helps in guidance to the UG, PG as well as PhD students. To cater the need of society, Dr Vivek now turns his research attention to Internet Of Things.

Dr Vivek is senior member of IEEE and currently working as Chair, IEEE Communication Society, Pune Chapter. He is also a member of Computer Society, India Council. Dr Vivek is currently an IEEE Executive Committee Member of Pune Section, India.

- **Technical sponsor:** Institute for Research and Development (IRD) India, Bhubaneswar, Orissa, India

- **Num of papers received:** 781

- **Num of papers accepted:** 128

- **Name of publishing Journal:**

  - IJET

  - IJCTA

  - Springer

  - IRD Journals

- **Number of Sessions held:** 4

Session	Session Chair
1	Dr. Vivek S Deshpande ,VIT Pune Dr. P.V Rao & Prof. S M Vijaya, RRCE, Bengaluru
2	Dr. P.K Mallick, Chairman, IRD India Orissa Dr. L. Rangaiah & Prof. V. Sreepathi,RRCE, Bengaluru
3	Dr.Y R Manjunath,UVCE, Bengaluru Dr. L SwarnaJyothi & Prof. P Bhuvaneswari,RRCE, Bengaluru
4	Prof. A M Prasannakumar,Vice Principal, ACSCE Prof. B.B.S Kumar & Prof. Sunitha R,RRCE, Bengaluru

- **About conference flow:**

    International Conference started with the inaugural function 10Am on 20th May 2016, with all the dignitaries and the keynote speaker. After the inaugural function, ICPCIT-16 chief guest Dr. L.M. Patnaik Adjunct Professor - IISC and INSA Senior Scientist gave talk on applications of advanced technologies. The keynote address was delivered on the topic "QUALITY OF SMARTNESS", regarding patentable research and applications of Wireless Sensor Network. After the inauguration followed by tea break, morning sessions started in parallel. Post lunch afternoon session started followed by Valedictory, in which the Best paper awards and certificates for participants were distributed.