



RAJARAJESWARI COLLEGE OF ENGINEERING

Ramohalli Cross, Kumbalagodu, Mysore Road,
Bengaluru-560074.

DEPT. OF ELECTRONICS AND COMMUNICATION ENGINEERING



FACULTY DEVELOPMENT PROGRAMME ON "SUSTAINABLE RESEARCH"

- **Date of FDP:** 18th to 27th January 2016
- **Venue:** Seminar Hall, Dept of ECE

Session	Topics covered	Resource Person
DAY-1 18.01.2016	Inaugural Function	
	Key note address on "CMOS Nanotechnology"	Mr. Gururaj, Principal Engineer, QualComm India Pvt Ltd.
	Project proposals for funding- case studies	Dr. L SwarnaJyothi Prof & Head ECE, RRCE
DAY-2 19.01.2016	Collaborative Research & Funding	Dr. L Rangaiah, Professor, RRCE
	Project proposals for funding- case studies	Dr. L SwarnaJyothi Prof & Head ECE, RRCE
DAY-3 20.01.2016	Advances in research in networking	Dr. Poornima, Professor, BMSCE
	Advances in research image processing	Dr. A.Meera, Professor, BMSCE
	MATLAB hands on session on networking & Image processing algorithm	Prof. Umashankar, RLJIT Prof. Ravindranath K,DBIT
DAY-4 21.01.2016	Technical talk – Sustainable Research	Dr.T.R.Gopalakrishnannair, Rector, RRG
	MATLAB hands on session on networking & Image processing algorithm	Prof. Umashankar, RLJIT Prof. Ravindranath K,DBIT
	Patentable research	Prof. M R Devaraj, BMSCE
DAY-5 22.01.2016	Hands on Session :Patentable research	
DAY-6 23.01.2016	Algorithm prototyping using FPGA	Mr. Harish R, Senior Engineer winglobal Tech, Bangalore
DAY-7 25.01.2016	Technical Talk- Research, Publish & Patenting	Dr. R. Balakrishna, Principal, RRCE
	Introduction to LATEX & Technical Paper writing using LATEX	Prof. Sunitha R, RRCE
	Hands on Session : LATEX	
DAY-8 27.01.2016	Design prototype using open hardware and Demo	Mr. Prathik, NIE, Mysore
	Valedictory Function	

- **FDP objective:** The objective of this FDP is to enable the faculty to pursue the research and make it sustainable. The FDP covered various topics related to pursuing research in the field of image processing and wireless/wireless sensor networks. FDP was scheduled to cover topics on writing and publishing good technical paper, patentable research and making funded proposals for various funding agencies and schemes. It also provided hands on sessions/demo on algorithm prototyping On FPGAs, Hardware prototyping on open hardware and coding image processing algorithms using MATLAB.
- **Total num of participants:** 80
- **FDP Outcomes:**
 - 80 faculty members of different disciplines have registered and taken benefit
 - Participants have gained the knowledge on writing a good technical//research paper.
 - Participants have sufficient knowledge about different funding agencies and how to write project proposals for funded research. Many faculties have submitted technical proposals after attending FDP
 - Sessions on patentable research guided the participants to recognized and work towards patentable research.
 - Awareness on different skill, tools and methodologies have helped the participants to further their research.